

# Turning *Coders* into *Makers*: The Promise of Embedded Design Generation

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UNIVERSITY OF MICHIGAN

# Embedded Development is a key part of the maker movement.



# Embedded Development is a key part of the personal fabrication movement.



**More accessible and usable parts.**

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**Cheaper board fabrication.**



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**Low entry requirements.**

Embedded Development is a key part of the personal fabrication movement.



**Design is still a major bottleneck!**



More accessible and usable parts.

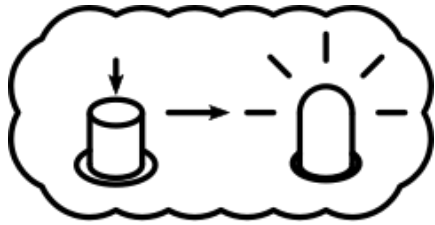
**Cheaper board fabrication.**



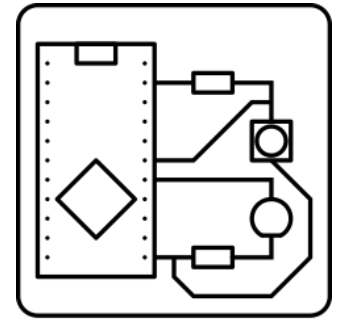
**Low entry requirements.**

We want to go from idea to finished device.

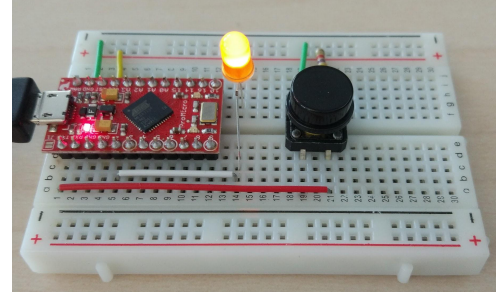
**When the button  
is pressed, the  
light should blink.**



Idea

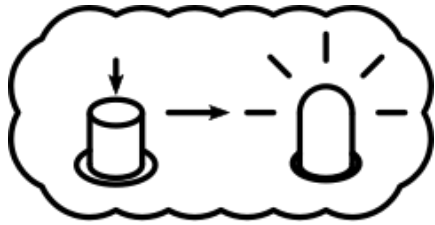


Completed  
Board

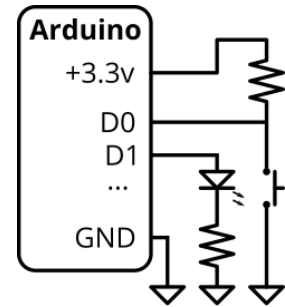
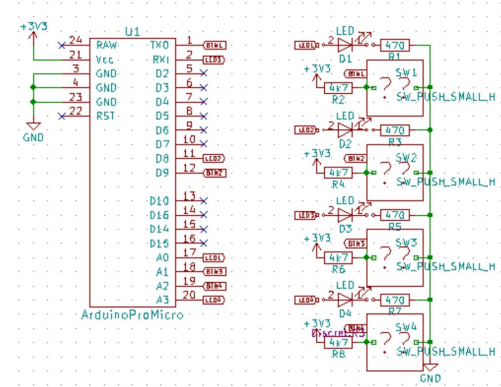


# Designing the circuit takes significant domain knowledge.

When the button is pressed, the light should blink.



Idea



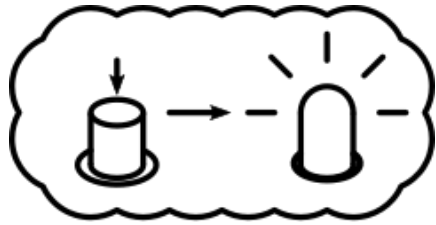
Netlist-level Schematic



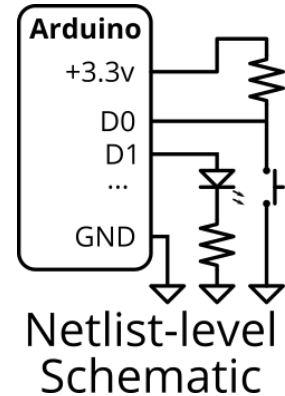
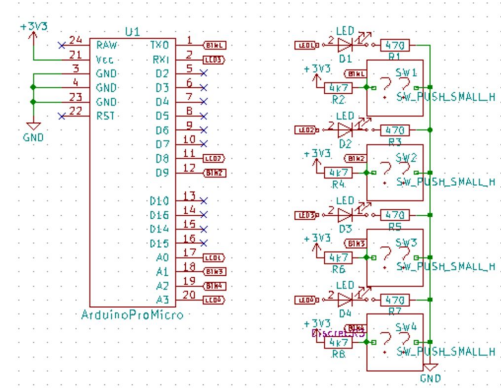
# Designing the circuit takes significant domain knowledge.

**When the button  
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light should blink.**

- What should the circuit have in it?



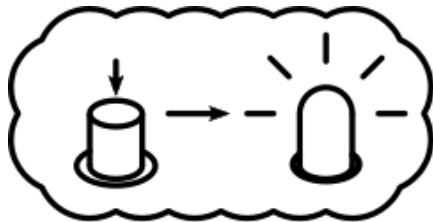
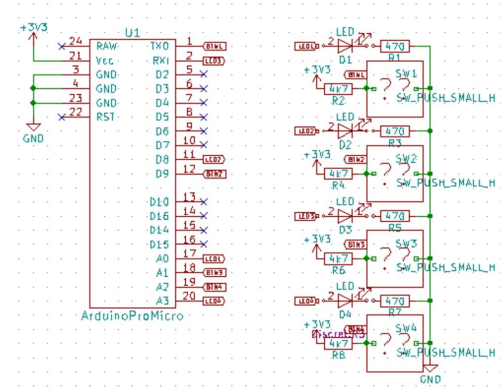
Idea



# Designing the circuit takes significant domain knowledge.

**When the button is pressed, the light should blink.**

- What should the circuit have in it?
- What do I need to check in order to verify part compatibility?

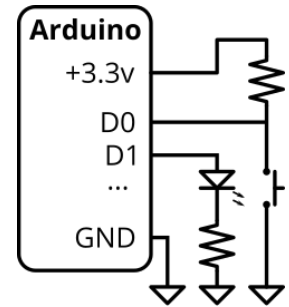
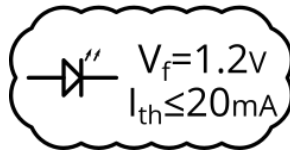
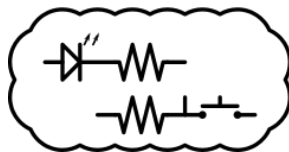


Idea



Design

Verification

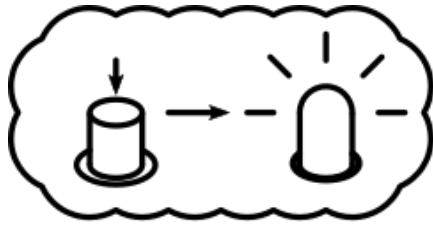


Netlist-level Schematic

# Designing the circuit takes significant domain knowledge.

**When the button is pressed, the light should blink.**

- What should the circuit have in it?
- What do I need to check in order to verify part compatibility?
- How do I even figure out what parts I want to use?



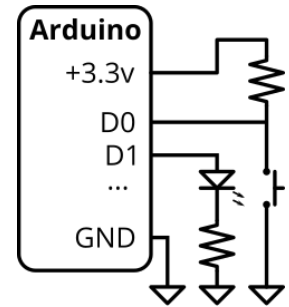
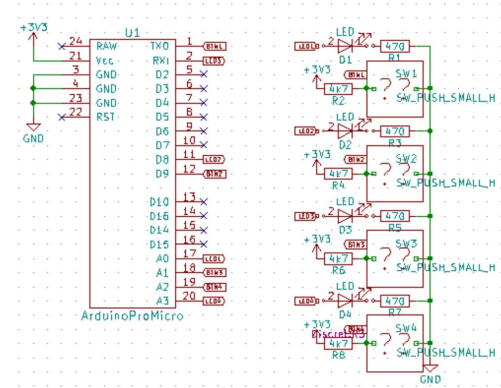
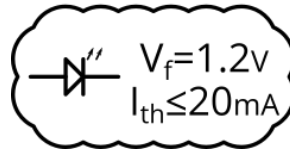
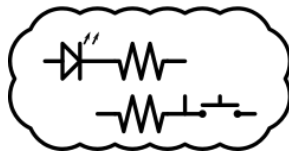
Idea



Design

Verification

Part Selection

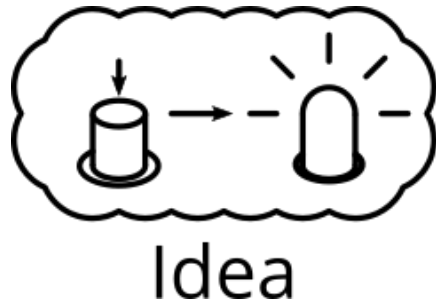
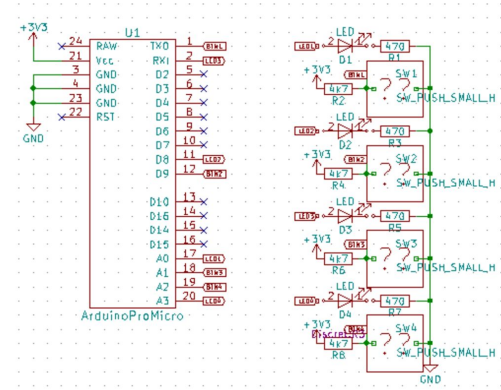


Netlist-level Schematic

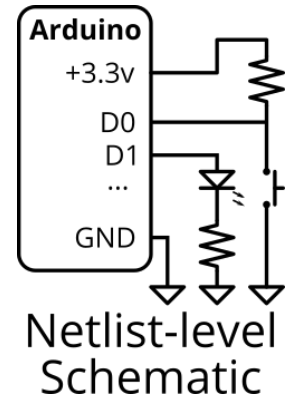
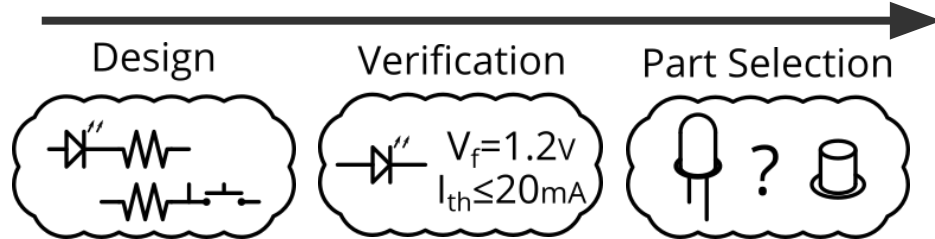
This domain knowledge is often hard for novices to acquire.

When the button is pressed, the light should blink.

“What I found challenging was to understand what are the components that have to be paired with the basic components that you are buying.”



- Luisa, closing discussion  
[Mellis et al. 2016]







# Working with the firmware is just coding.

```
#import "arduino.h"
#import "button_driver.h"

BUTTON button;
GPIOLED light;

void setup() {
  button.init(GPIO3);
  light.init(GPIO4);
}

void loop() {
  if(button.isPressed()){
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```

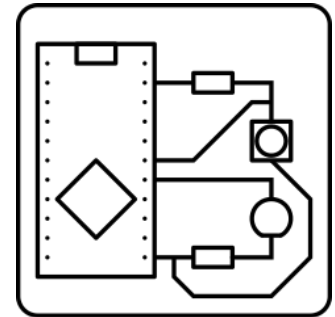
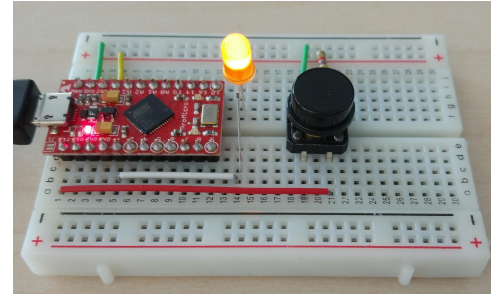
```
void loop {
  led = 1;
```

Embedded  
Firmware

- The circuit has to be designed first so we have information about connectivity and interfaces.
- Arduino and other frameworks wrap low-level abstractions in nice, comprehensible interfaces.

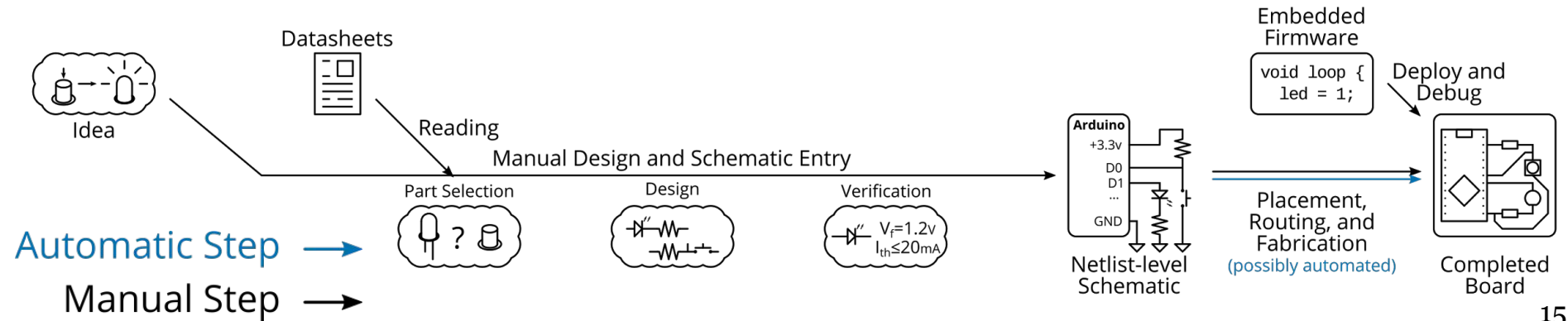


**Debug and deploy firmware**



Completed  
Board

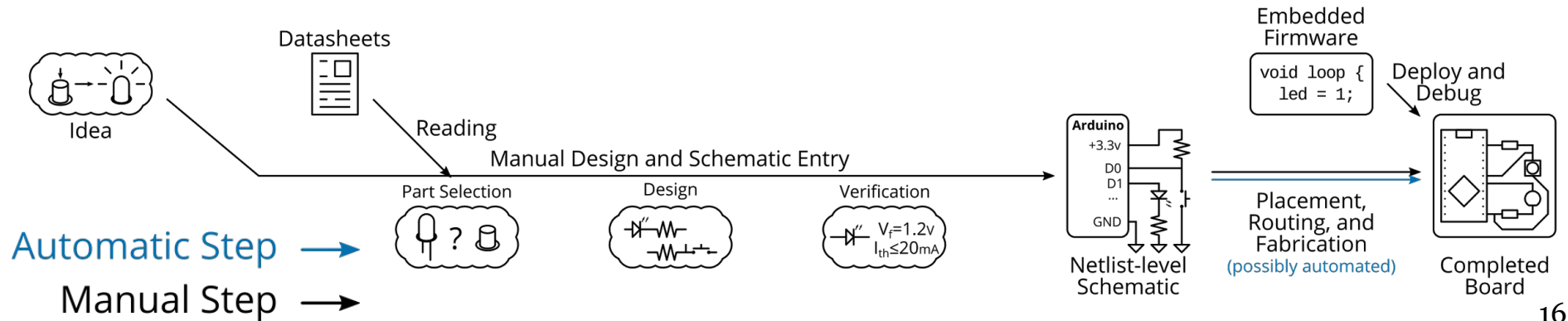
# We want to minimize the amount of knowledge needed to design electronics.



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## Anyone who can program should be able to design an embedded device

- There are many more programmers than electrical engineers.
- This applies for professionals *and* hobbyists.





# Embedded firmware requires domain knowledge to write.

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void setup() {
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void loop() {
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    }
    delay(1000); //milliseconds
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```

**Requires finished circuit diagram, and the knowledge to create it.**

**Captures the function of the device at a high level.**

We can make a version that doesn't need that domain knowledge.

## Annotated Code

A mockup of the input format for EDG, designed to allow people to specify devices without needing to know electrical engineers.

```
peripheral button = new MomentarySwitch();
```

```
peripheral light = new LED(color = Red);
```

```
void loop() {  
    if(button.isPressed()) {  
        light.toggle();  
    } else {  
        light.off();  
    }  
    delay(1000); //milliseconds  
}
```



**Ask for properties that matter to you, don't waste time picking parts.**

**Captures the function of the device at a high level.**

# EDG takes annotated code and a library of parts and produces SW and HW.

```
peripheral button = new MomentarySwitch();

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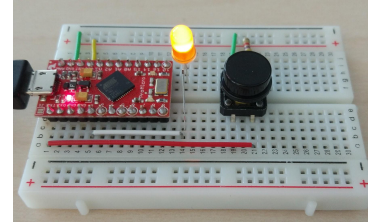
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Annotated Code

#pragma edg  
led(red)

Parse

EDG-Based  
Dev Tool

Datasheets

Library  
Creation

Design  
Instantiation

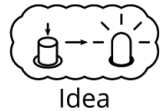
Embedded  
Firmware

void loop {  
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Deploy and  
Debug

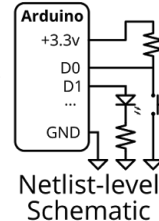
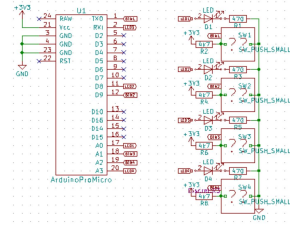
Placement,  
Routing, and  
Fabrication  
(possibly automated)

Completed  
Board

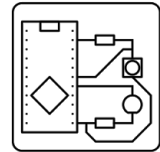


Idea

Describe  
Hardware



Netlist-level  
Schematic



Automatic Step →  
Manual Step →

# Embedded design generation uses constraint solving tools to do this.

Annotated  
Code

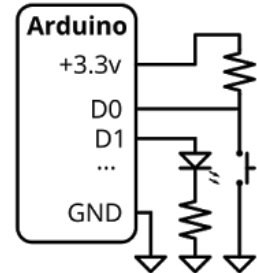
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Datasheets

Embedded  
Firmware

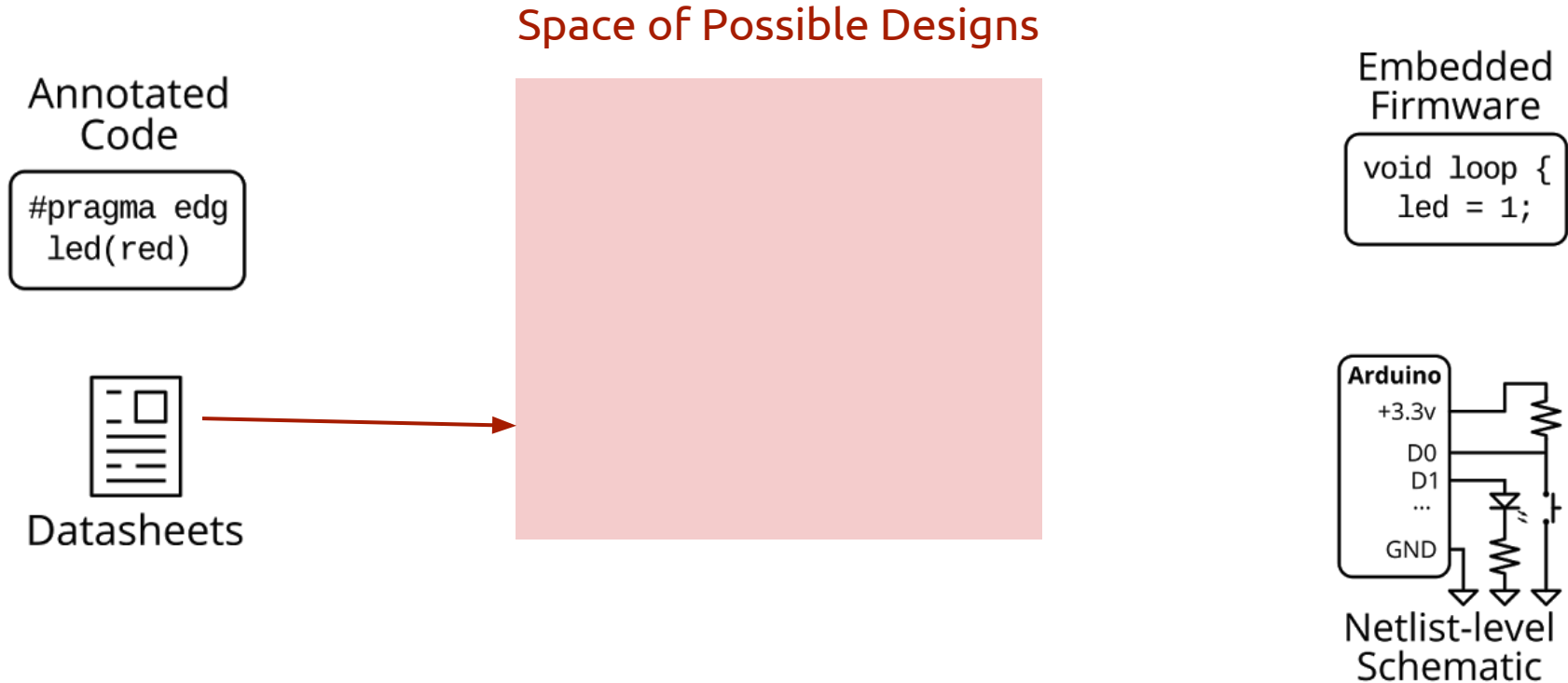
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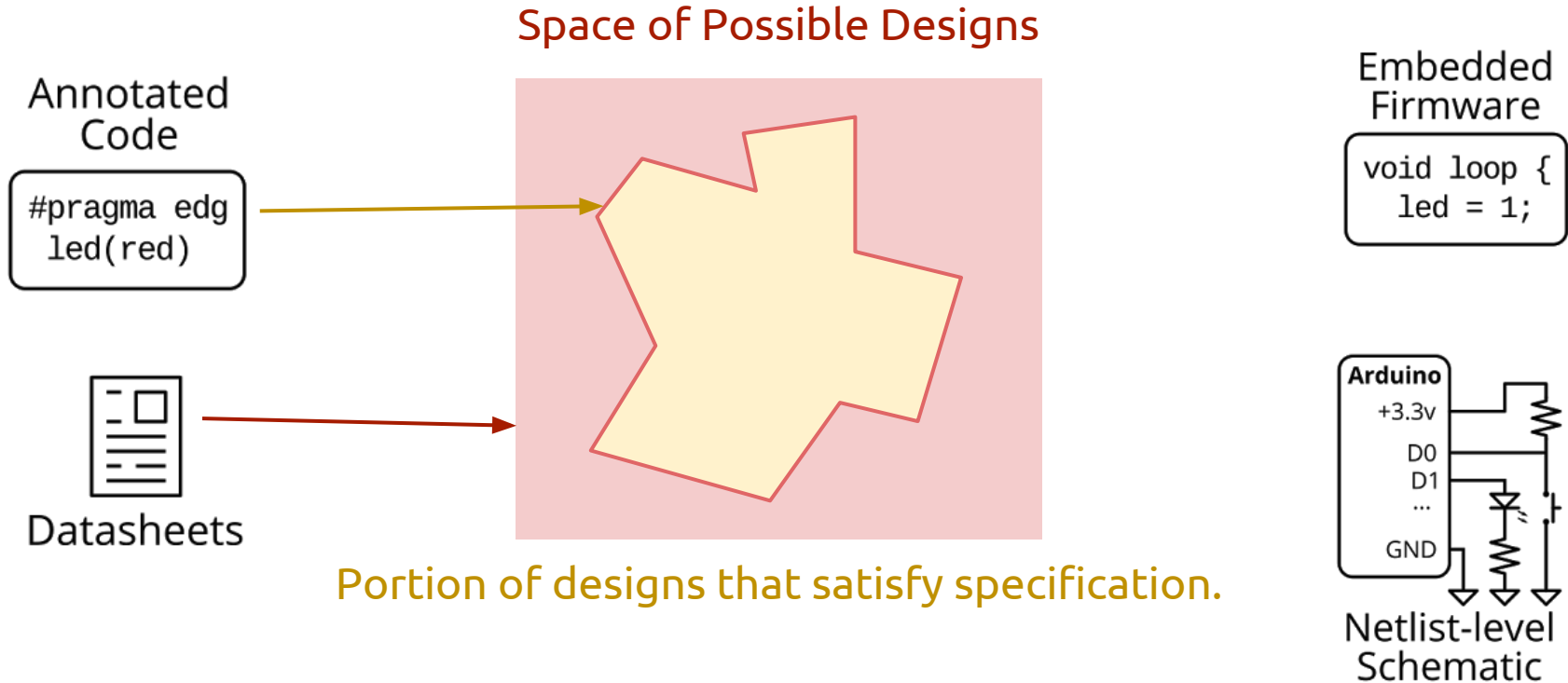
Netlist-level  
Schematic



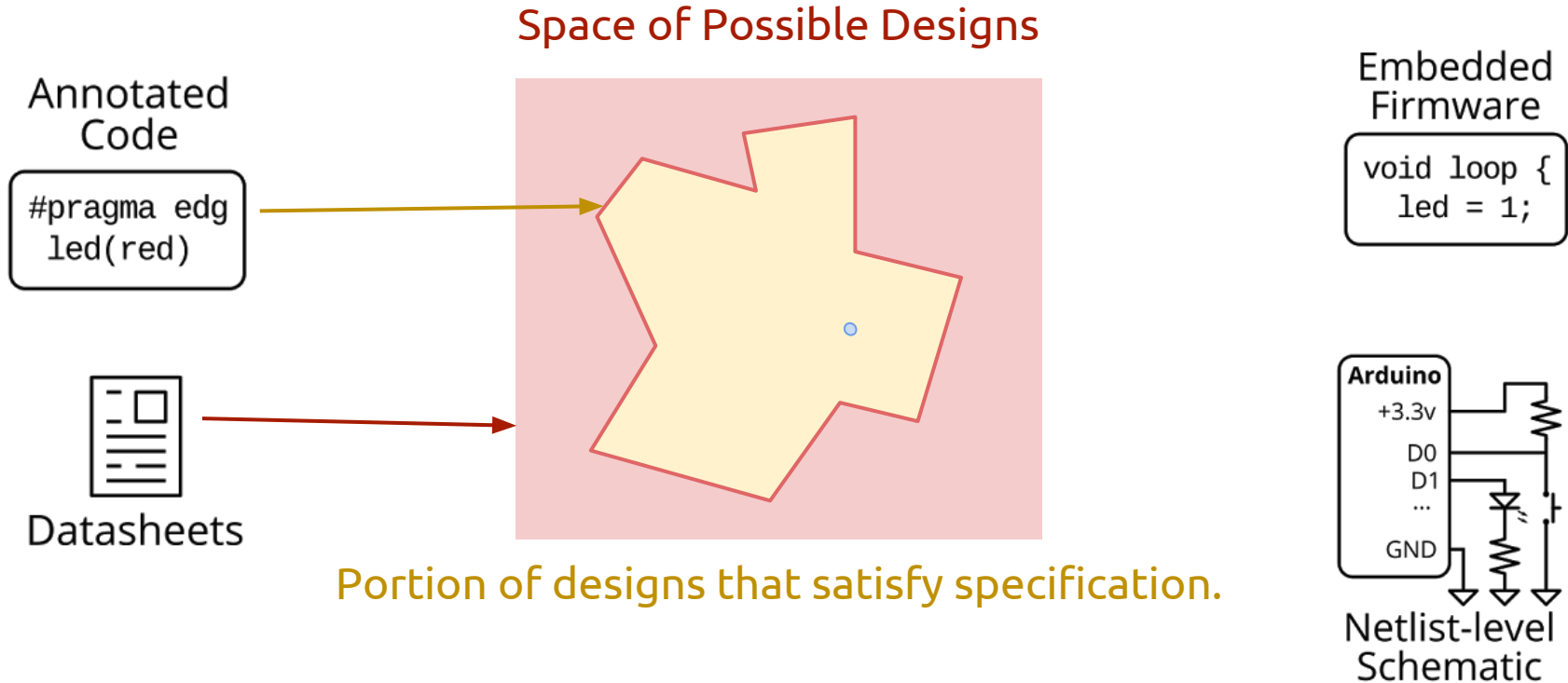
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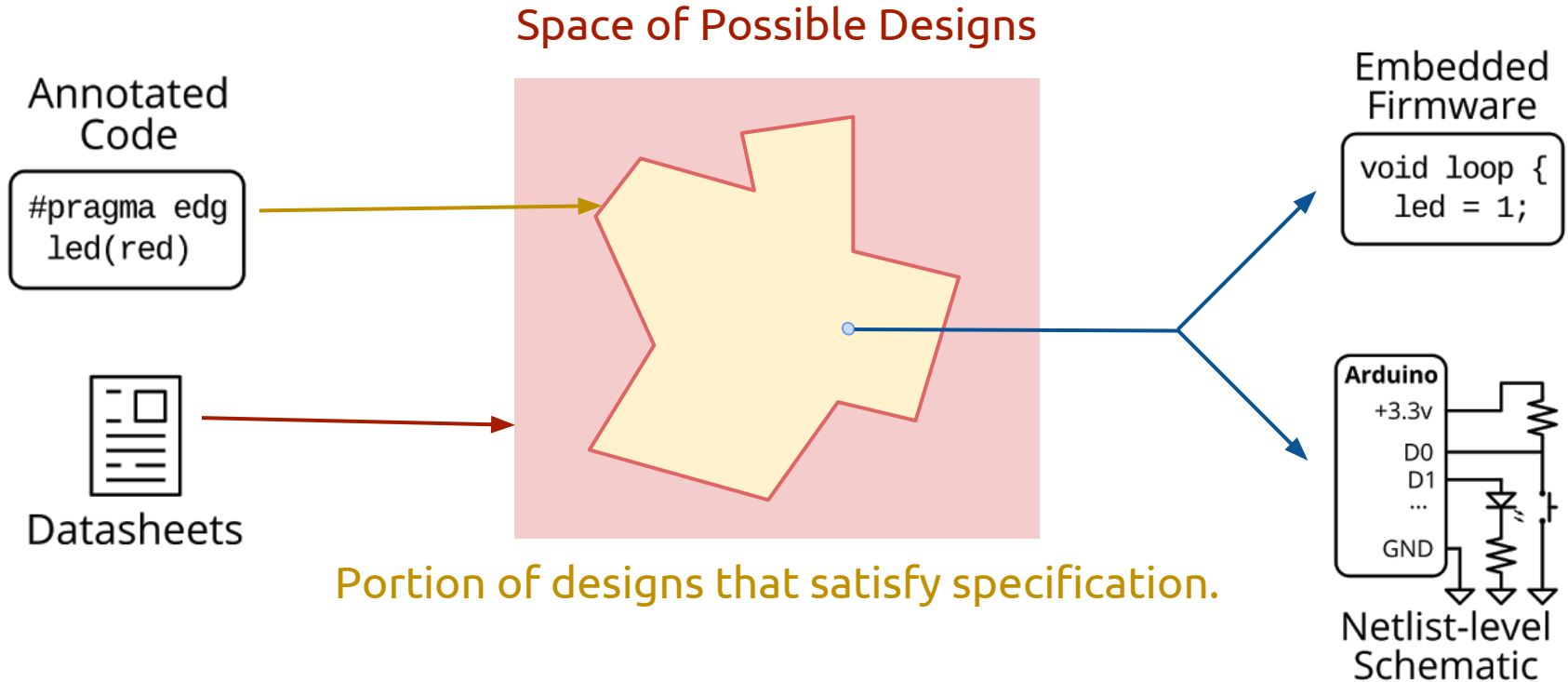
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# Turn coders into makers, through the power of constraint solvers

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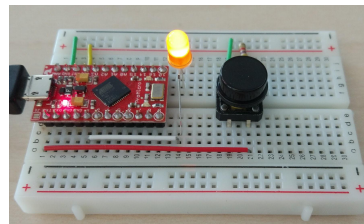
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Annotated Code

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Parse

EDG-Based  
Dev Tool

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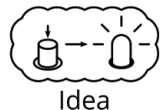
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Deploy and  
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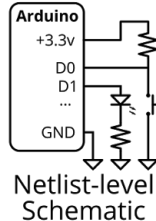
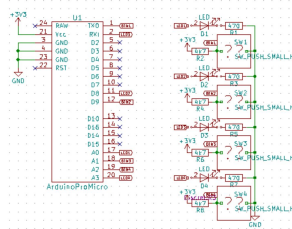
Placement,  
Routing, and  
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Completed  
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Idea

Describe  
Hardware



Netlist-level  
Schematic

Automatic Step →  
Manual Step →

# Outline

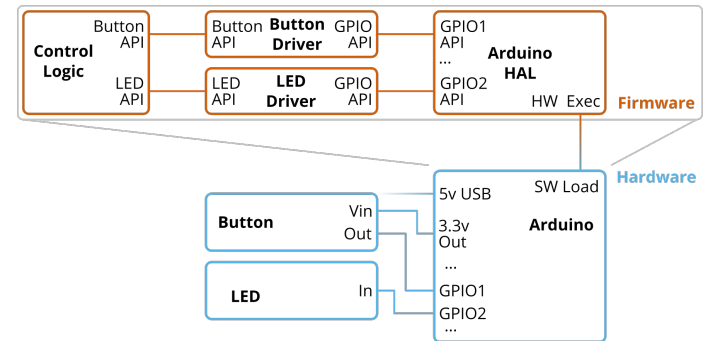
- The EDG Methodology
- Proof-of-Concept and Evaluation
- Discussion and Future Work

# The EDG Methodology

# How do we represent the parts our tool has available?



**We represent hardware and firmware components as blocks in a block diagram**



# Blocks are made up of a number of sub elements.





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## Ports



Digital Signal Input



User Facing Button API  
and Hardware API

# Blocks are made up of a number of sub elements.



Digital Signal Input



User Facing Button API  
and Hardware API

Code that wraps the HW  
API in a nice interface.

**Ports**

**Implementation**

# Blocks are made up of a number of sub elements.



Digital Signal Input



LED Color, Input Voltage



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API use and connectivity

**Ports**

**Implementation**

**Metadata**

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**Ports**

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**Metadata**

**Conditions**

Conditions allow us to specify the operating conditions of a component.



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**LiPo Battery**



**Mains Power**

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Input Voltage = 3.7v



**Metadata**

Input Voltage = 120v

# Conditions allow us to specify the operating conditions of a component.

**LiPo Battery**



**Mains Power**



Input Voltage = 3.7v

**Metadata**

Input Voltage = 120v

Input voltage is **above 1.2v** and **below 5v**

**Conditions**

Input voltage is **above 1.2v** and **below 5v**



# Conditions encode the functional correctness of a block.

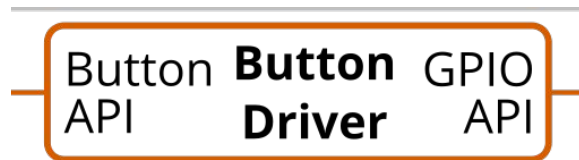


Digital Signal Input



LED Color, Input Voltage

Input voltage is above  
1.2v and below 5v



User Facing Button API  
and Hardware API

Code that wraps the HW  
API in a nice interface.

API use and connectivity

If button API is used, then GPIO  
API must be active

**Ports**

**Implementation**

**Metadata**

**Conditions**

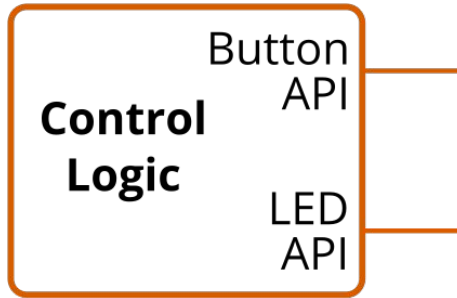
# The annotated code has to actually run on the device, and can be in a block.

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peripheral light = new LED(color = Red);  
  
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## Control Logic:

Block created by parsing annotated code, which will eventually run on the device.

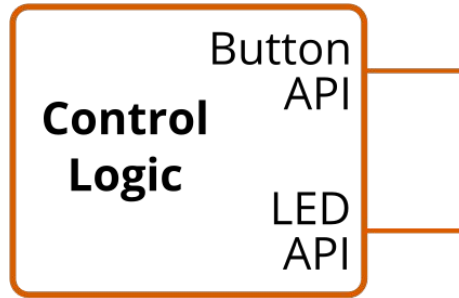


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Implementation

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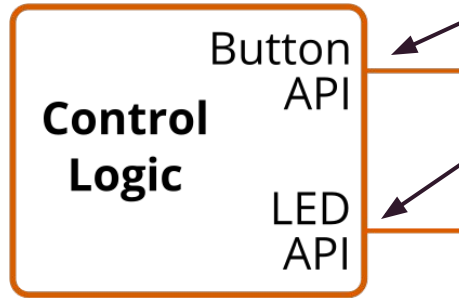
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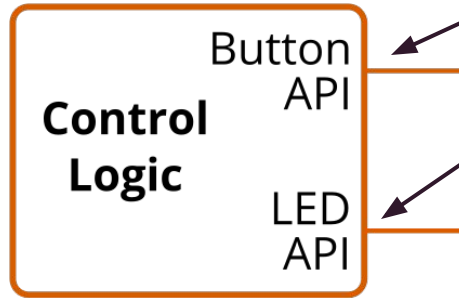
Implementation

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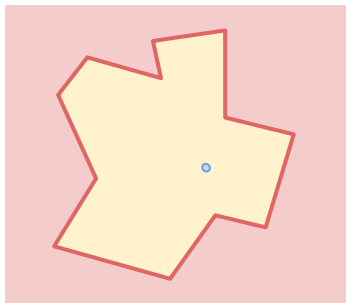
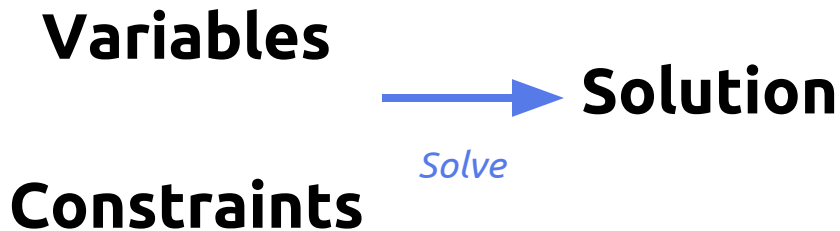
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**Implementation**

**Conditions**

Both the ports must be provided a valid api to the corresponding physical component.

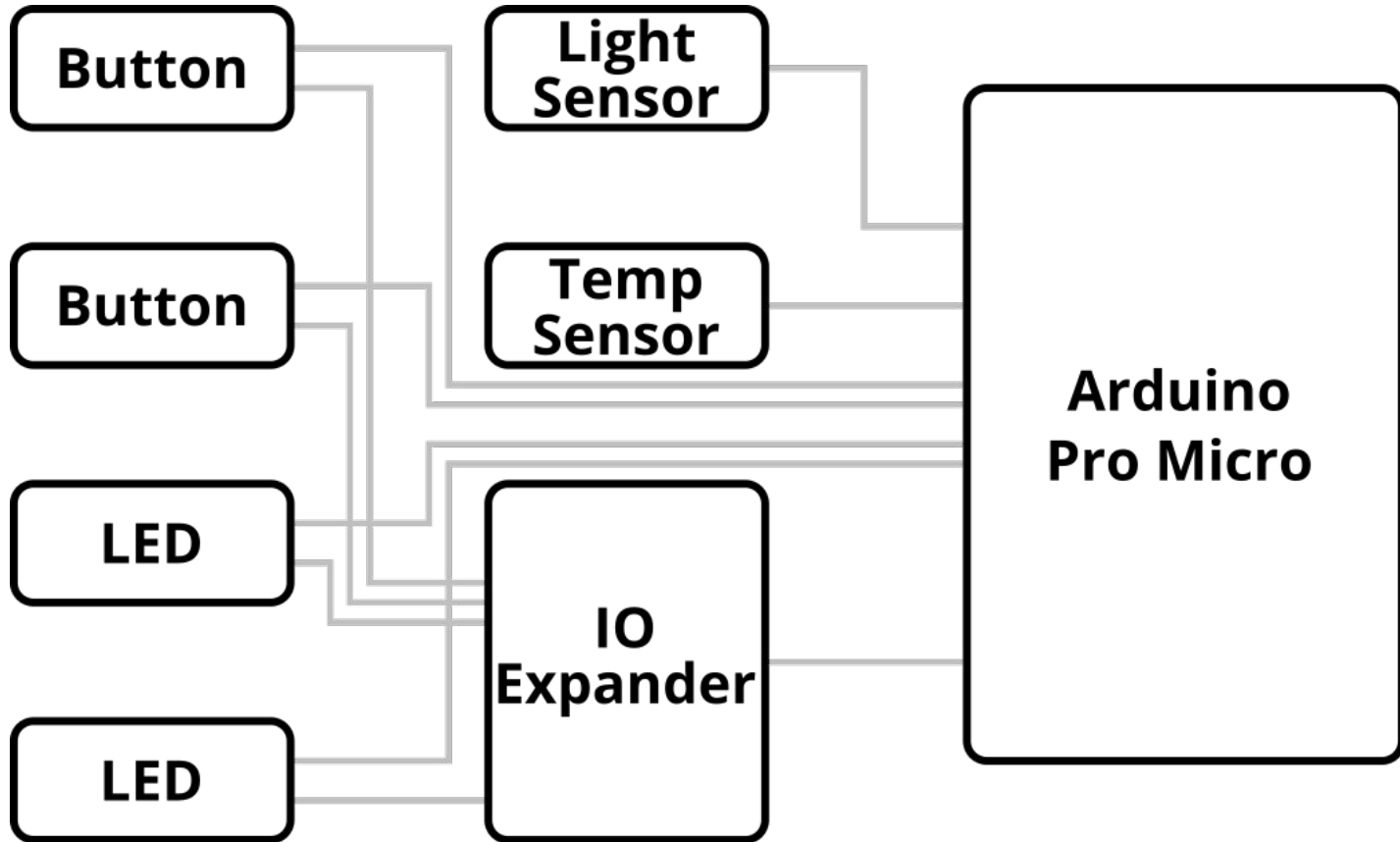
# Solvers find variable assignments such that some set of constraints hold;



$$\begin{array}{l} A < 12 \\ B = 3 + A \\ C > 0 \\ C = 3 - A \end{array} \xrightarrow{\text{Solve}} \begin{array}{l} A = 2 \\ B = 5 \\ C = 1 \end{array}$$

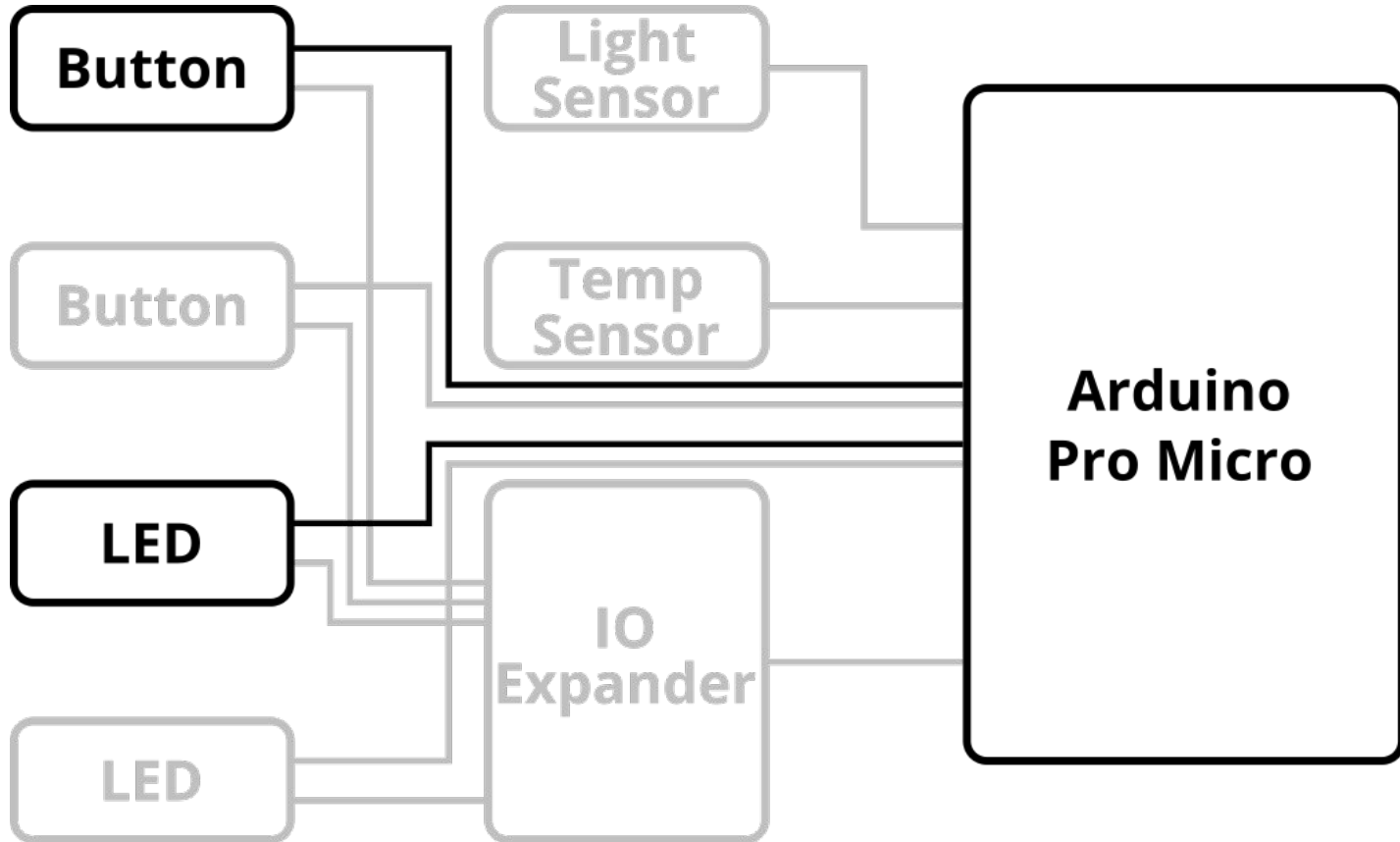
- The solver finds *one* assignment such that *every* constraint is met
- The solver is nondeterministic and might output any of the valid assignments.

We can combine every block in the library, and all possible potential links.





## A pegboard connection is a block diagram



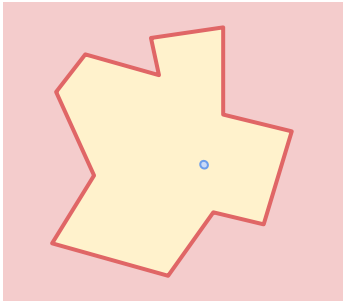
# Mix with block conditions and control logic, and bake

Metadata → Variables

→ Solution → Block Diagram

*Solve*

Conditions → Constraints



$$\begin{array}{l} A < 12 \\ B = 3 + A \\ C > 0 \\ C = 3 - A \end{array}$$

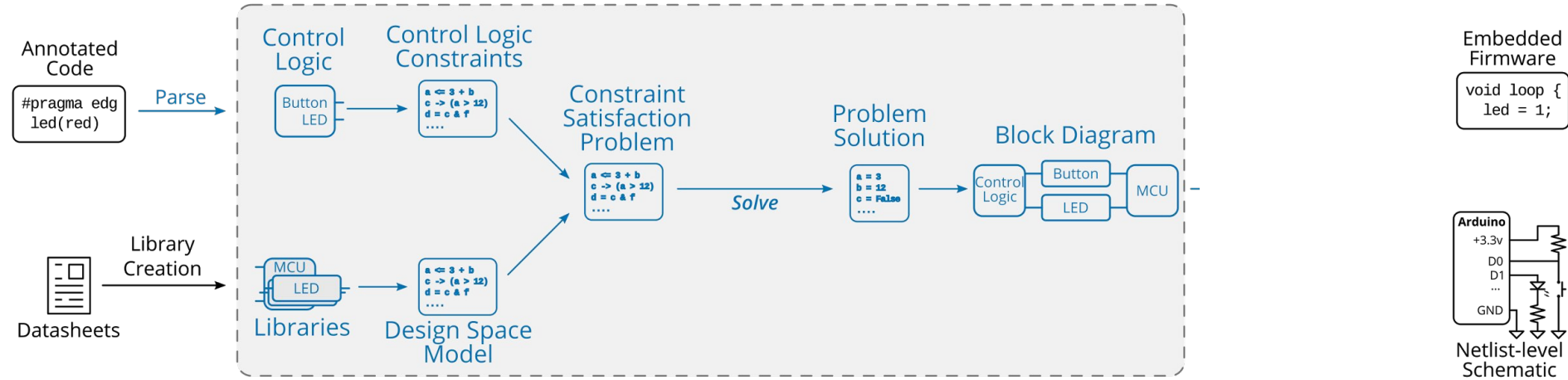


*Solve*

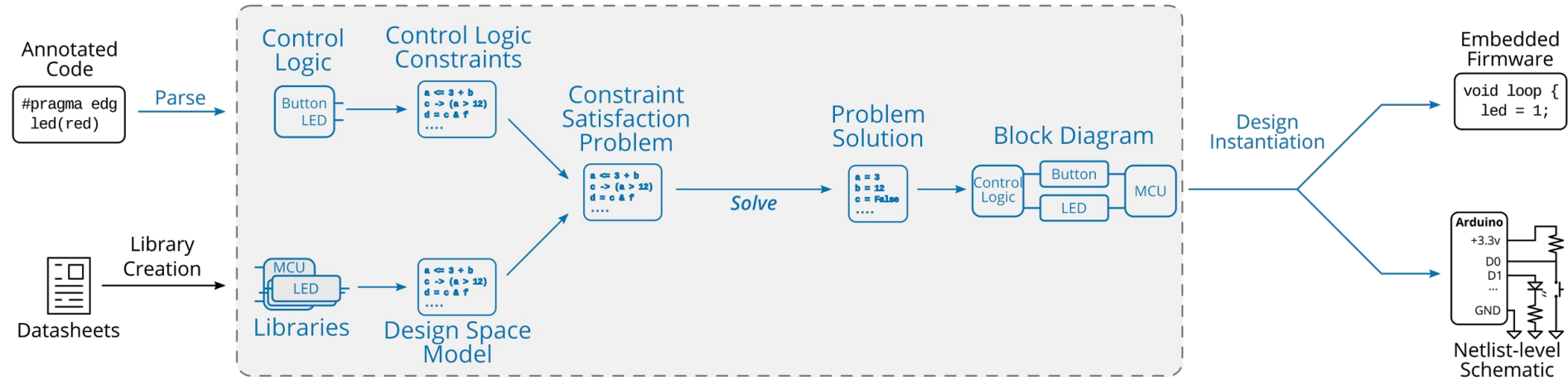
$$\begin{array}{l} A = 2 \\ B = 5 \\ C = 1 \end{array}$$

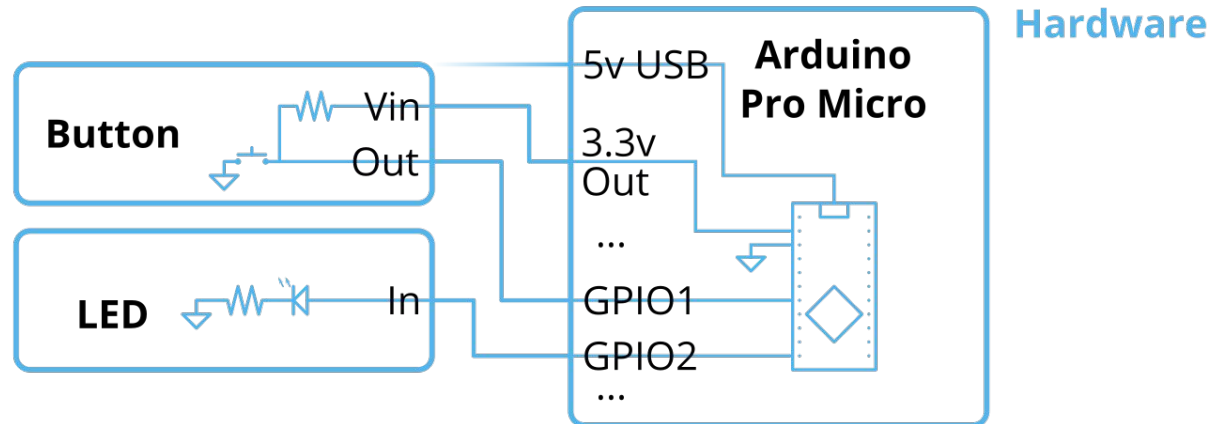
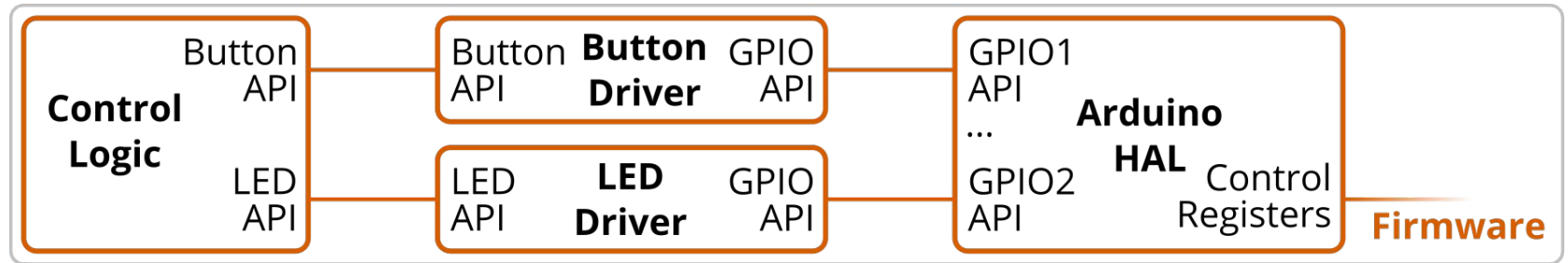
- The solver finds *one* assignment such that *every* constraint is met
- The solver is nondeterministic and might output any of the valid assignments.

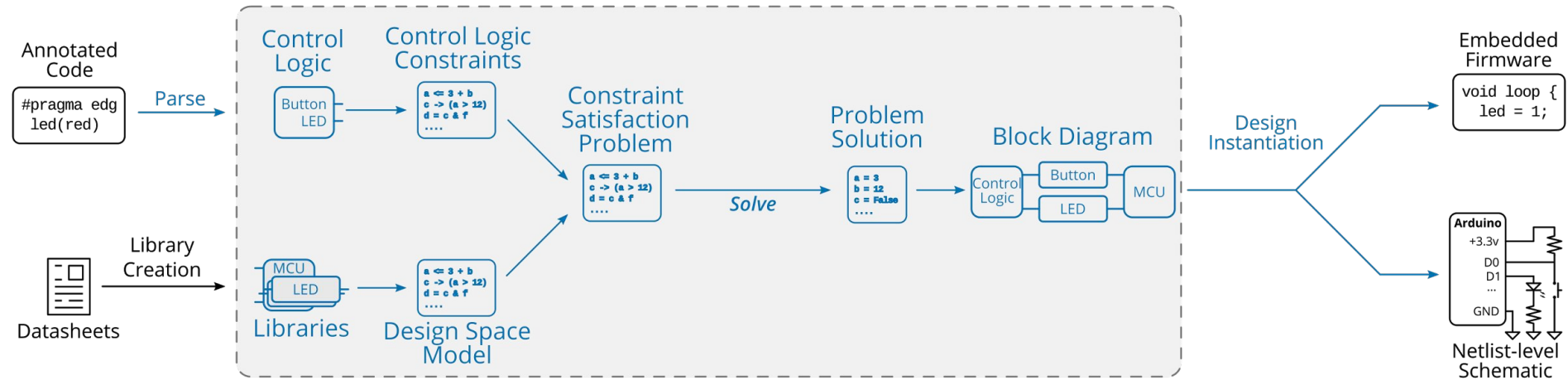
# We can instantiate a design from the solver output



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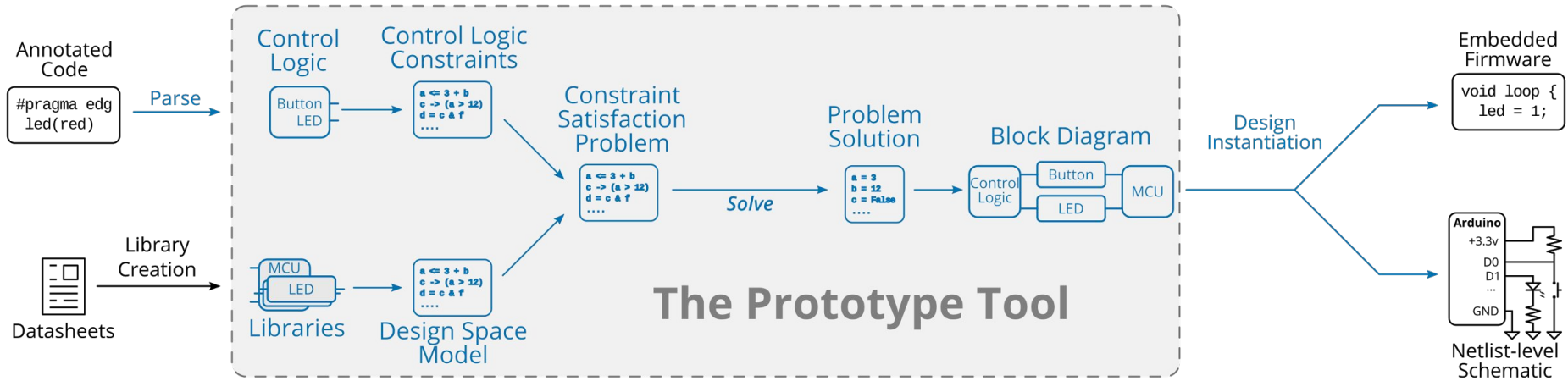




**We have gone from a high-level specification and a set of parts, to a design that can be realized.**

# Proof-of-Concept and Evaluation

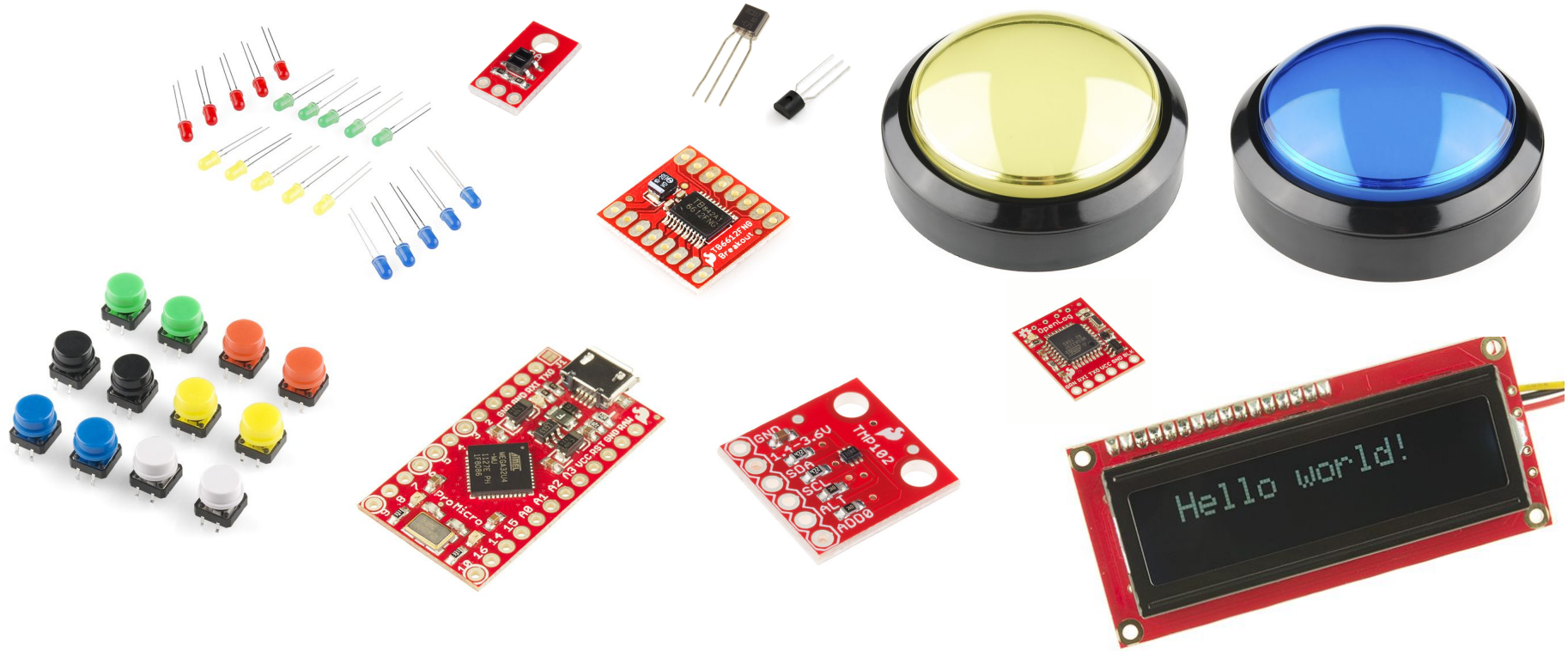
# Our prototype translates the EDG methodology onto available tools.



- The tool is built with Haskell on top of the Z3 constraint solver.
- Parsing and design instantiation is still manual to allow for faster iteration on the system internals.



# Our test library has a wide variety of parts



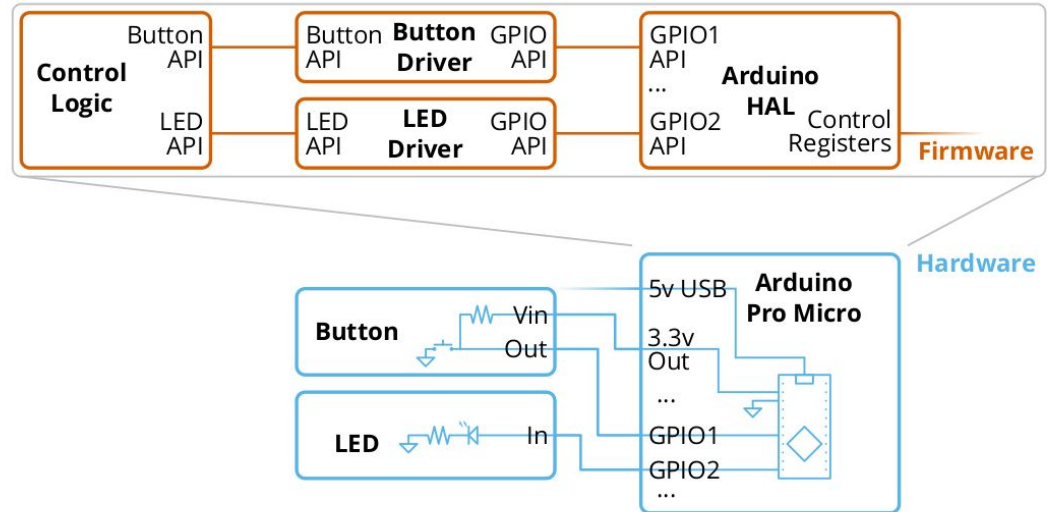
Images from [sparkfun.com](https://www.sparkfun.com), under a CC-BY 2.0 license

# Can we generate designs at all?

```
peripheral button = new MomentarySwitch();
```

```
peripheral light = new LED(color = Red);
```

```
void loop() {  
    if(button.isPressed()){  
        light.toggle();  
    } else {  
        light.off();  
    }  
    delay(1000); //milliseconds  
}
```



# Can we generate designs at all? Yes!

```
periphe
```

```
periphe
```

```
void lo
```

```
if(bu
```

```
lig
```

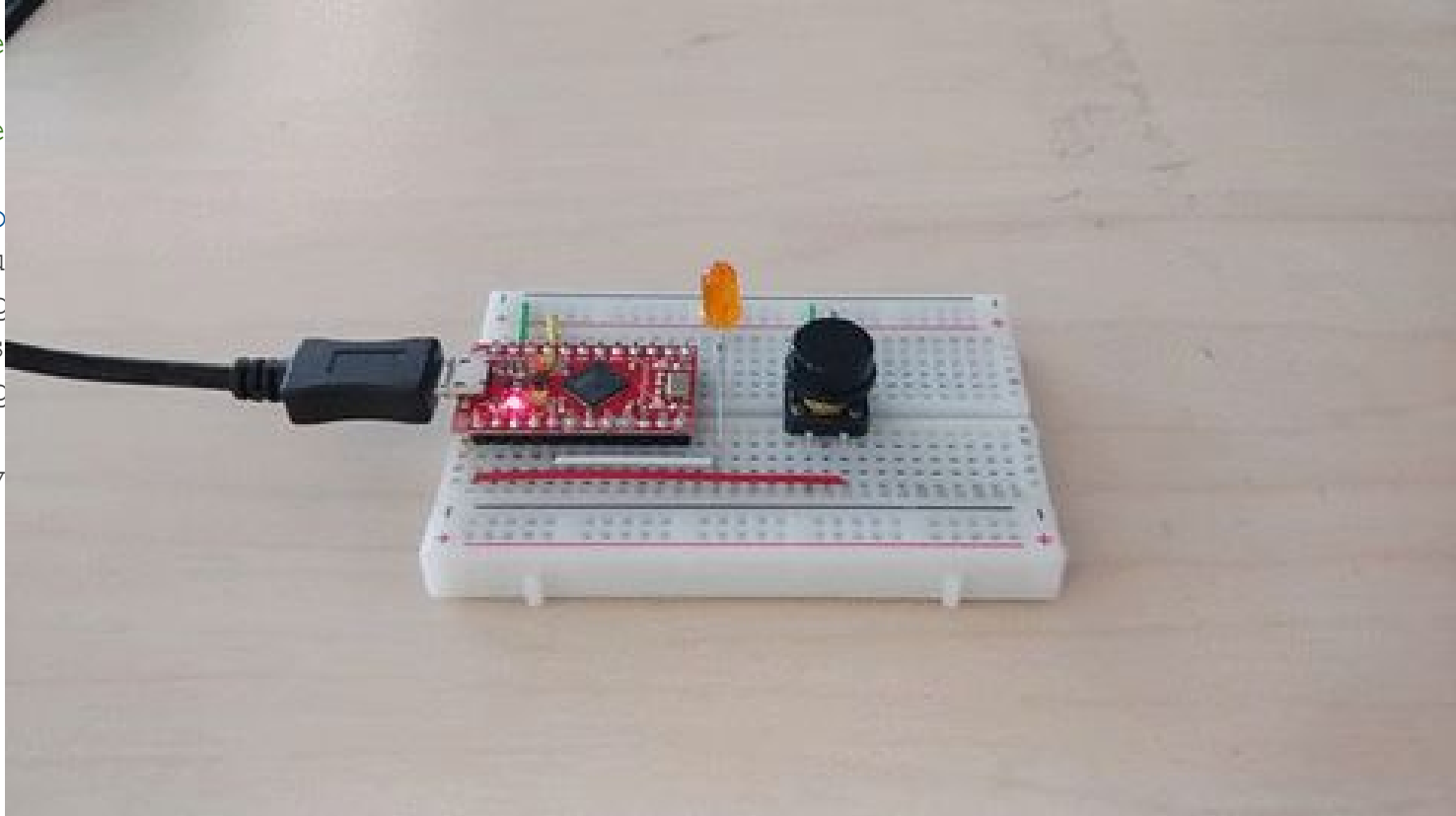
```
} els
```

```
lig
```

```
}
```

```
delay
```

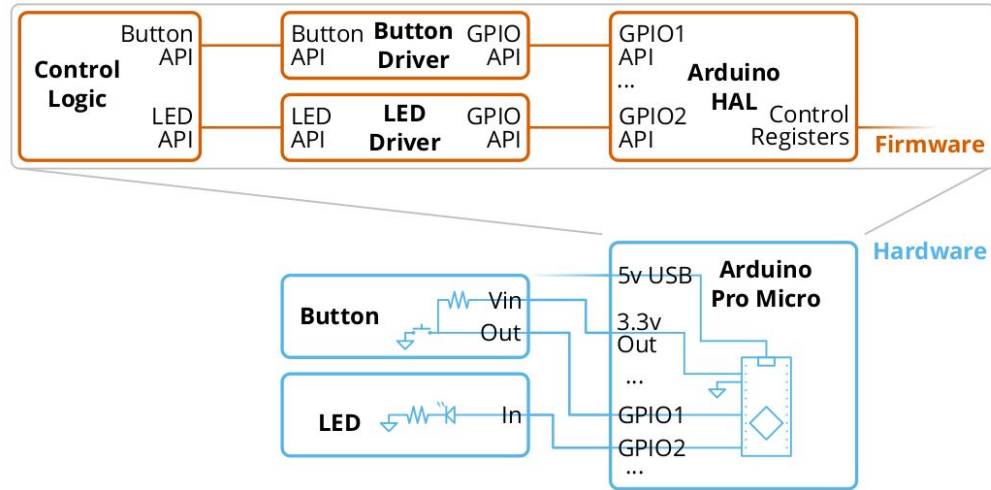
```
}
```



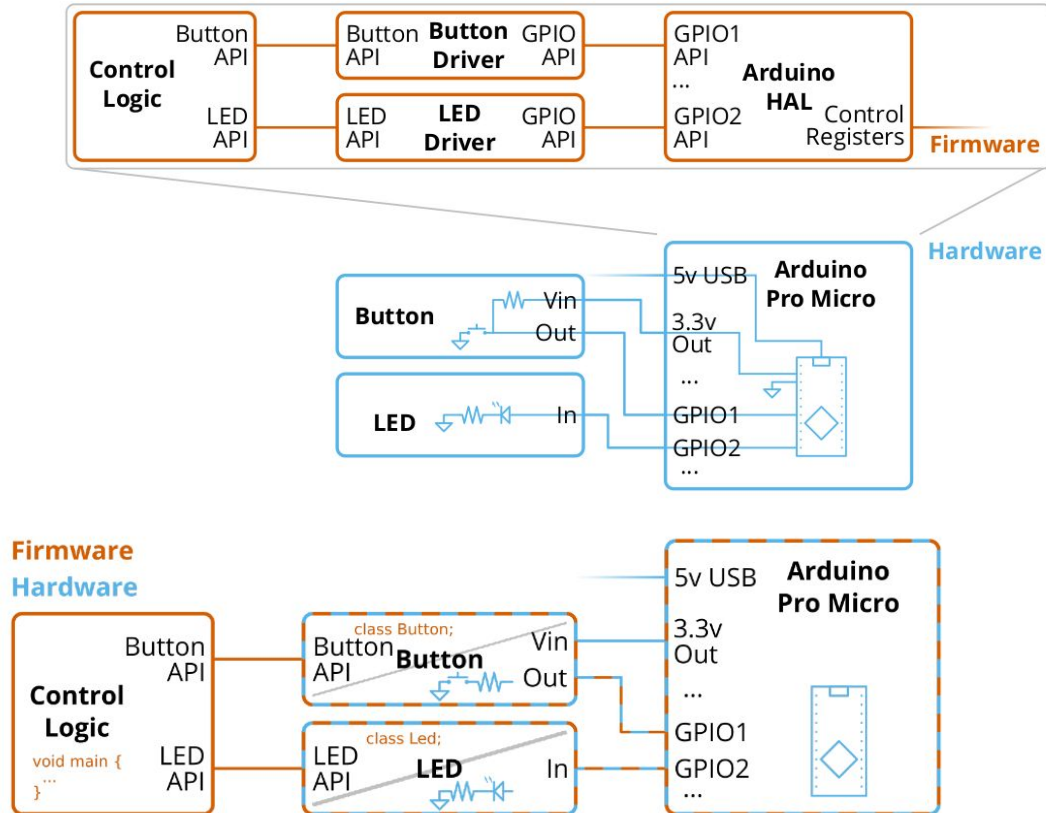
Firmware

Hardware

# We use symmetries to collapse the rest of our diagrams further.



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# Can the user specify exact parts? Handle complex power systems?

```
AdafruitChassisMotor motorL();  
AdafruitChassisMotor motorR = duplicate motorL;  
ReflectanceSensor lineSenseL(distance >= 1cm);  
ReflectanceSensor lineSenseR = duplicate lineSenseL;  
Battery power(duration >= 10mins);
```

```
const int baseSpeed = 100;  
const int baseTurn = 25;  
const int lightThreshold = 70;
```

```
int loop(){  
    static int differential = 0;  
    ...
```

# Can the user specify exact parts? Handle complex power systems?

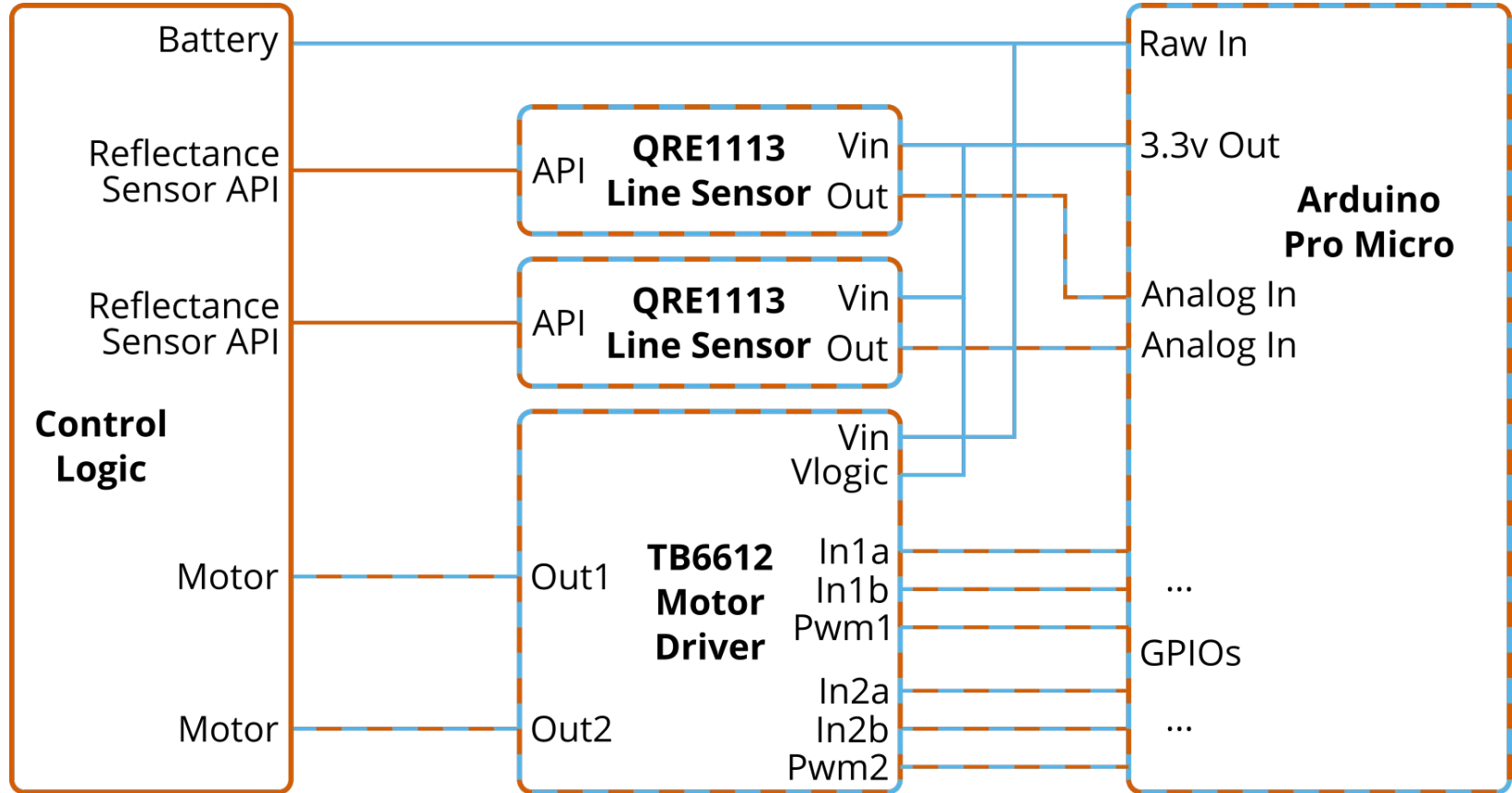
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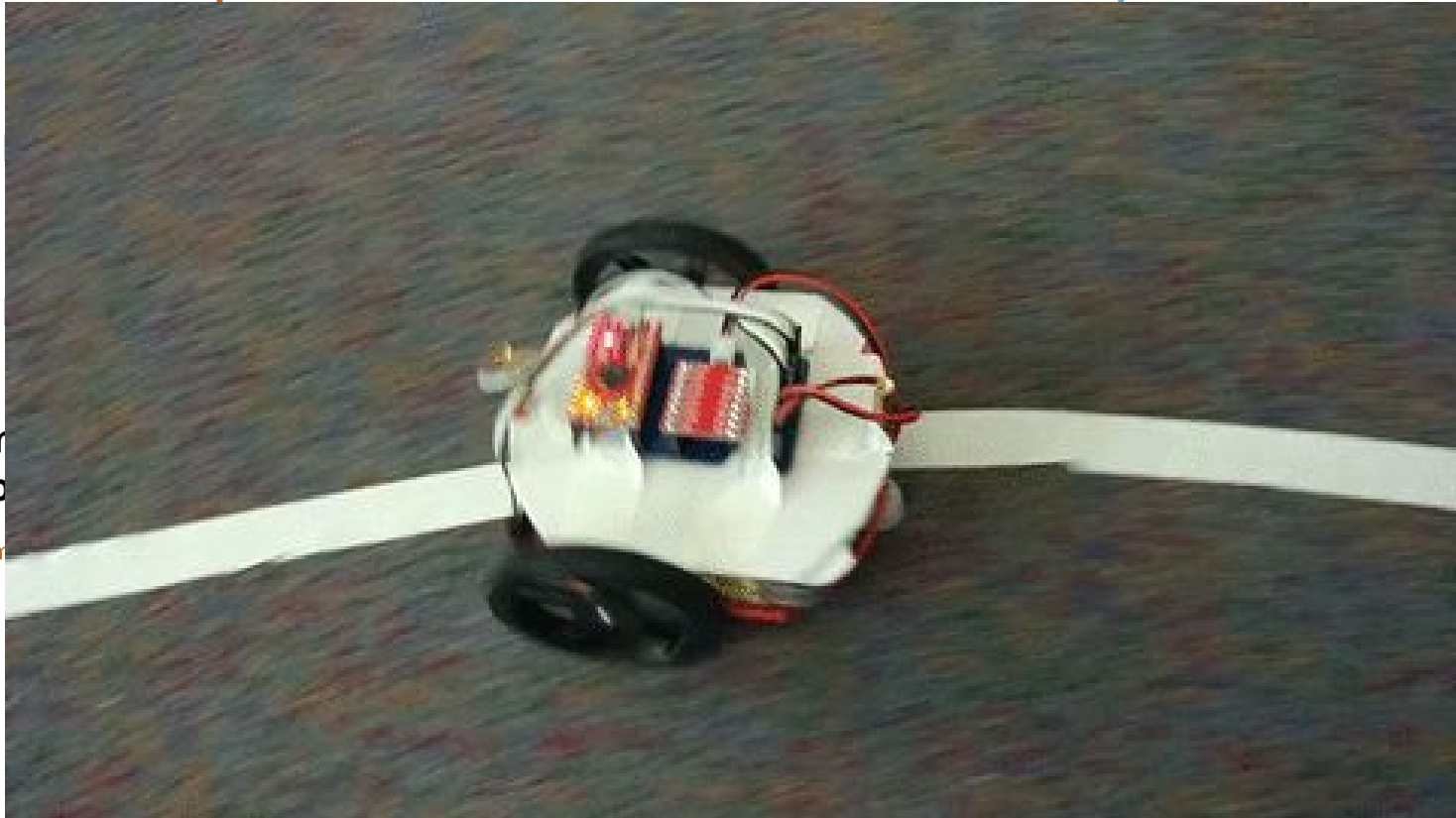




# Can the user specify exact parts? Handle complex power systems? Yes

Con  
Lo

```
void m  
...  
}
```

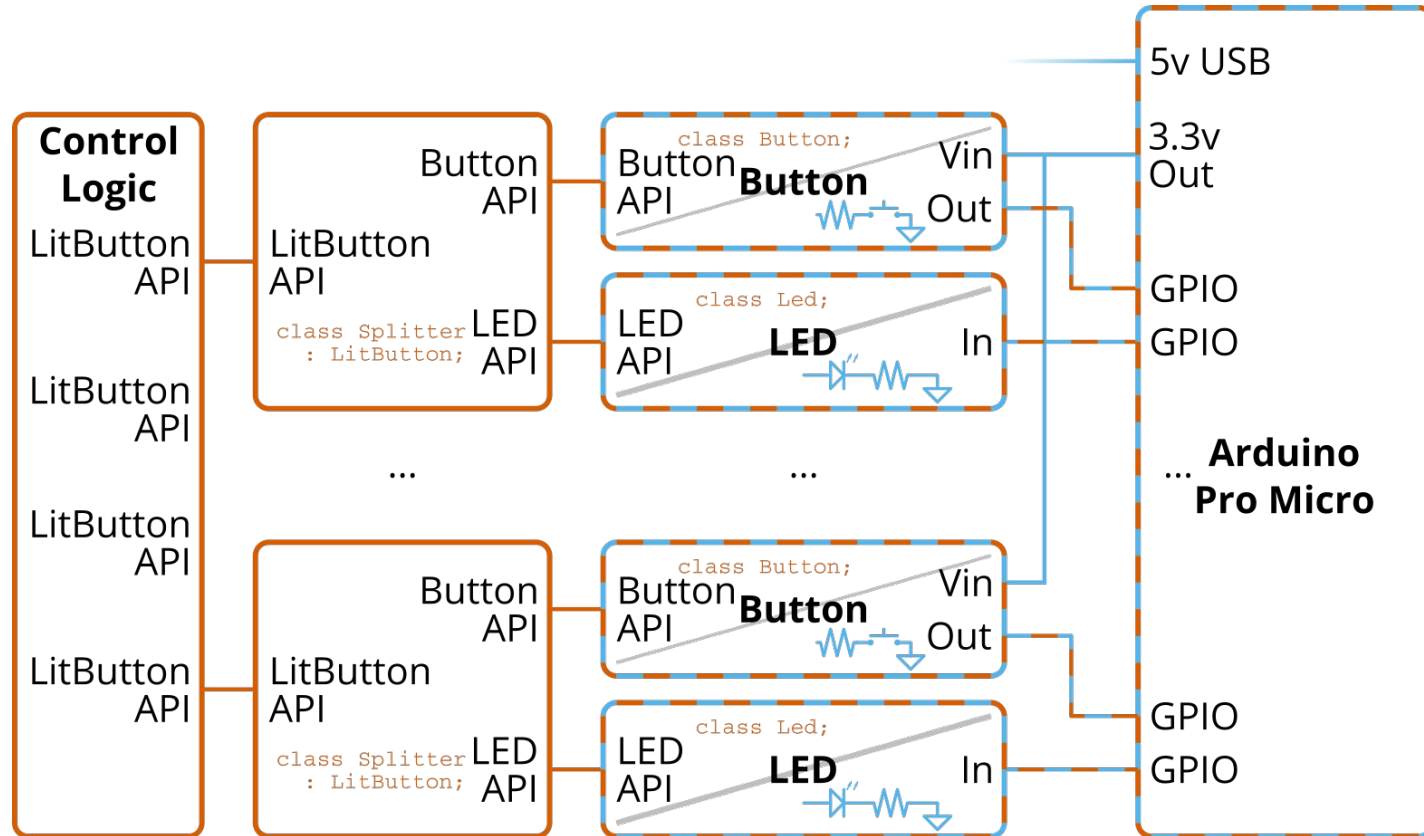


# Does our system preserve functionality even when the library changes?

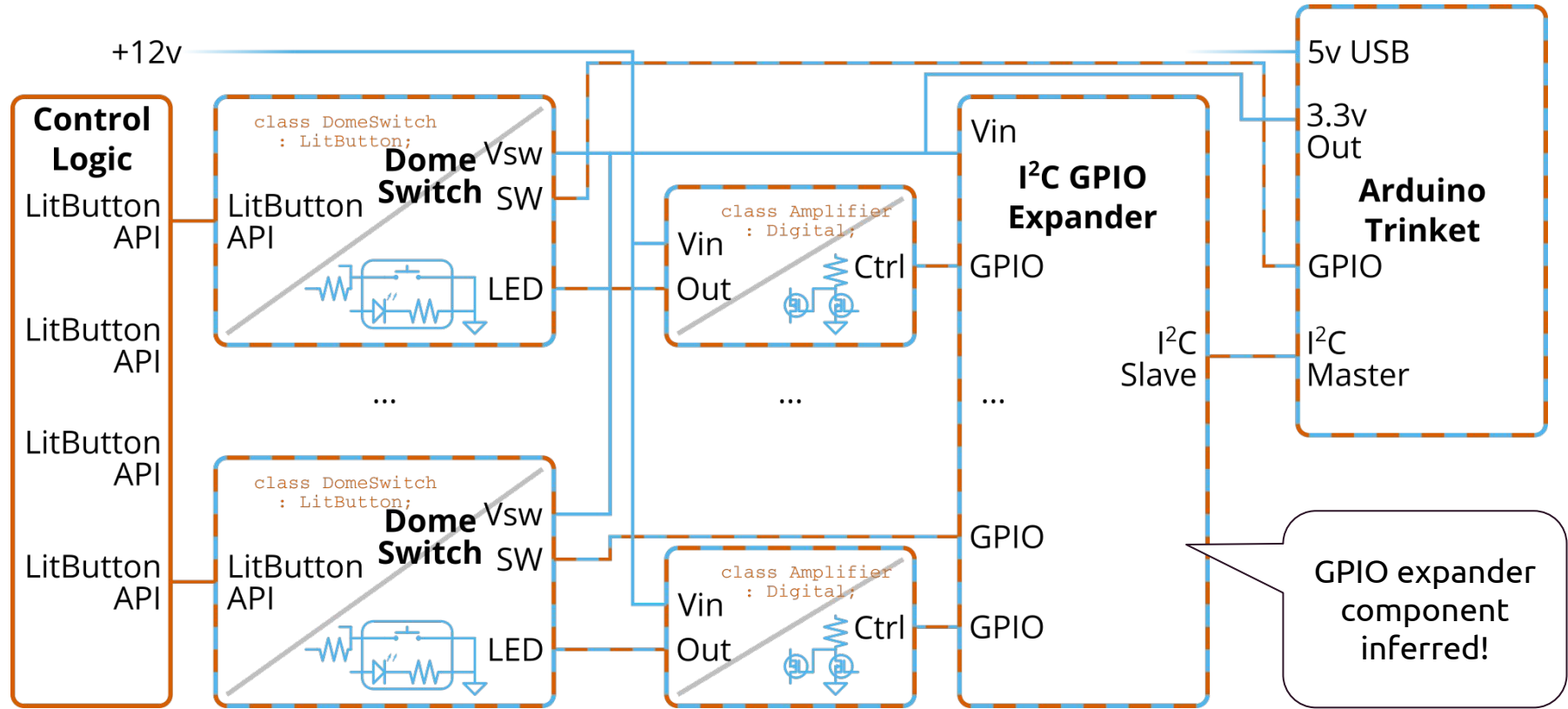
- 4 colored, light-up buttons
- Play a pattern, ask the user to press the buttons in the same order.



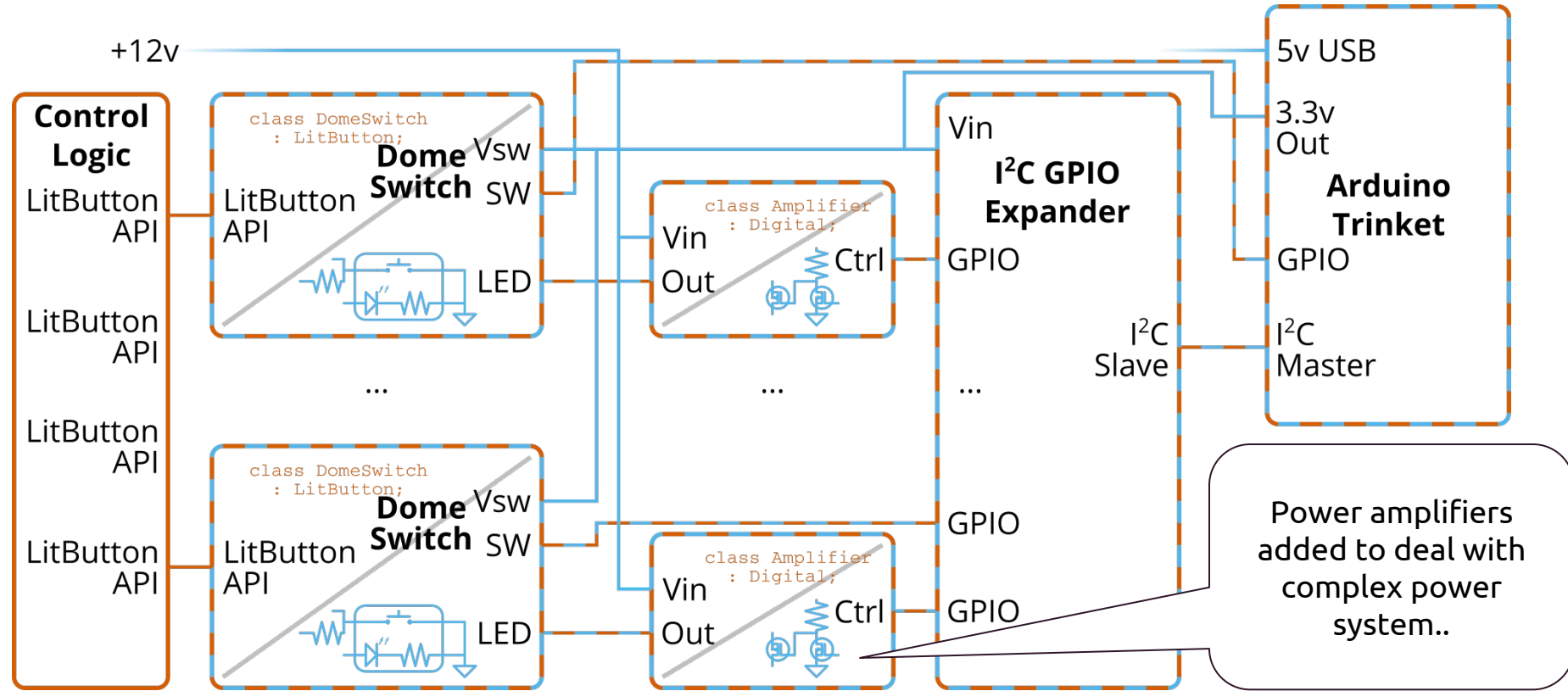
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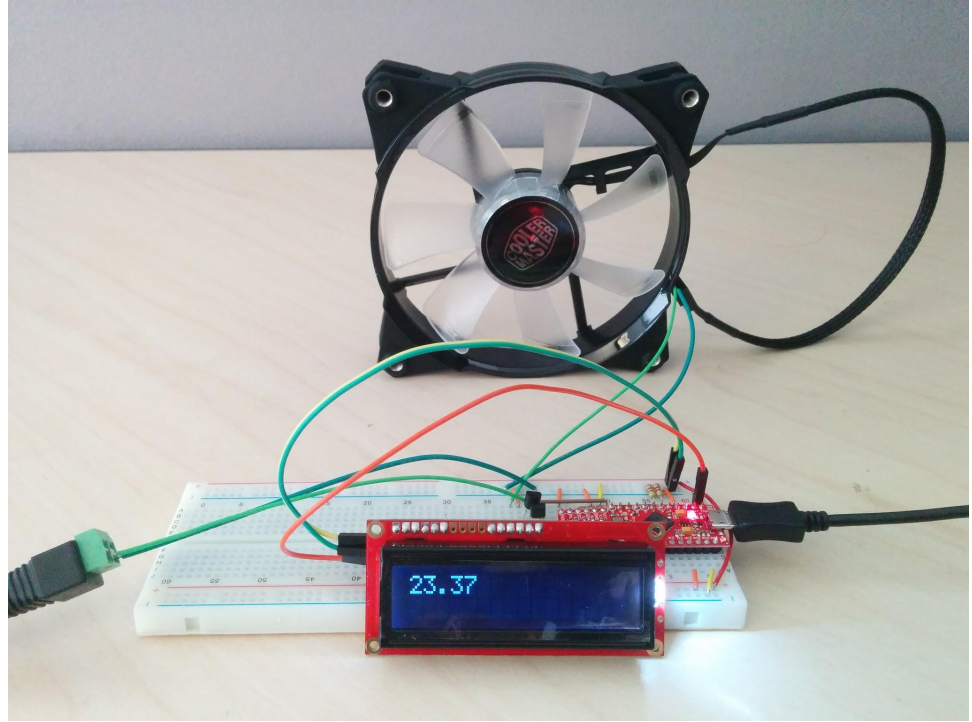
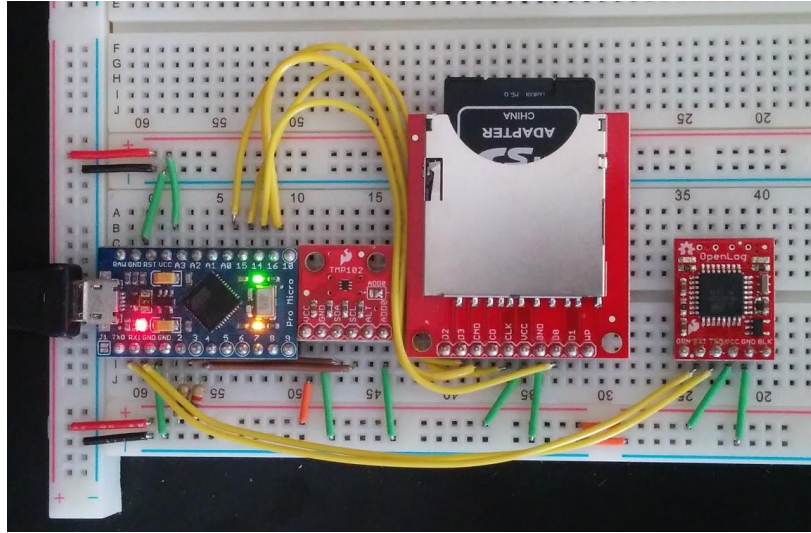
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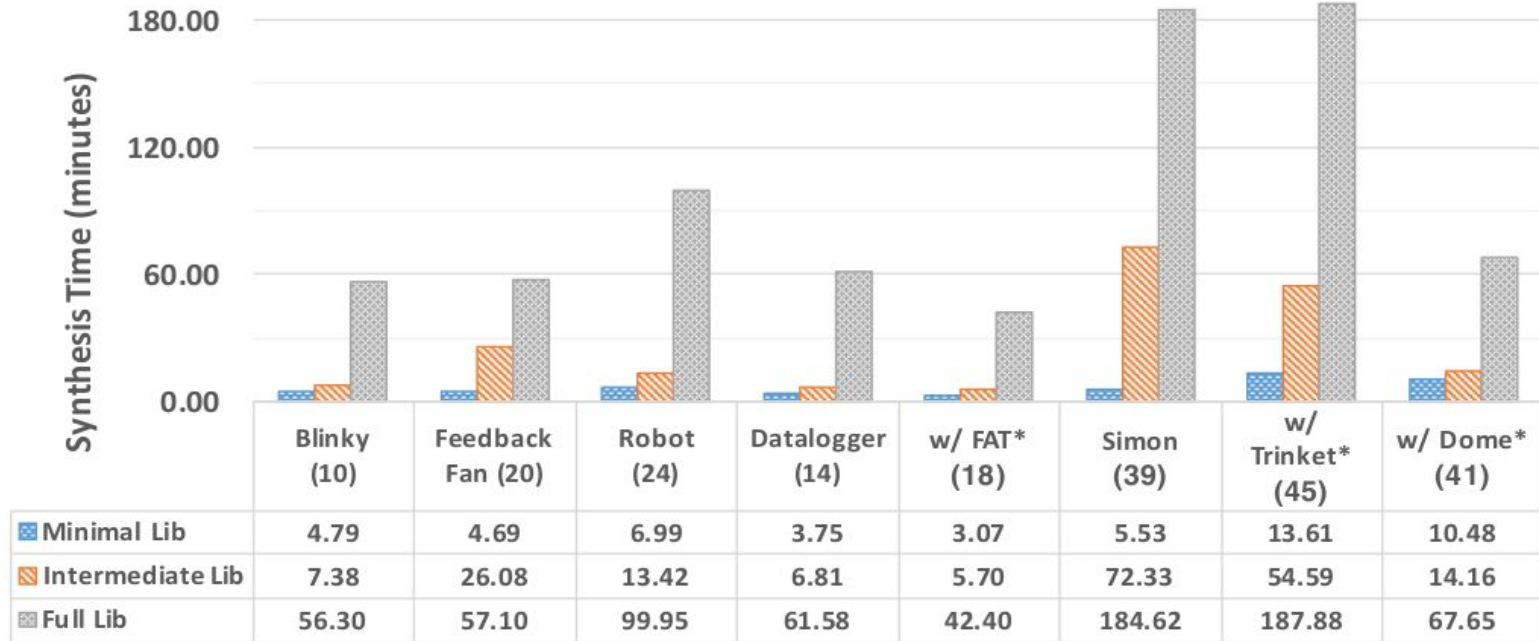


We also built other designs, including a datalogger and control system.





# Performance is decent for small problem sizes.



For simple designs, our tool has taken around 3 hours in the most realistic tests. Mind, that's still time you could be doing something else.



# Discussion and Future Work

# There are many things that need further research.

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  - But there's still immense room for optimization. Including library pruning, and more efficient encoding of constraints.

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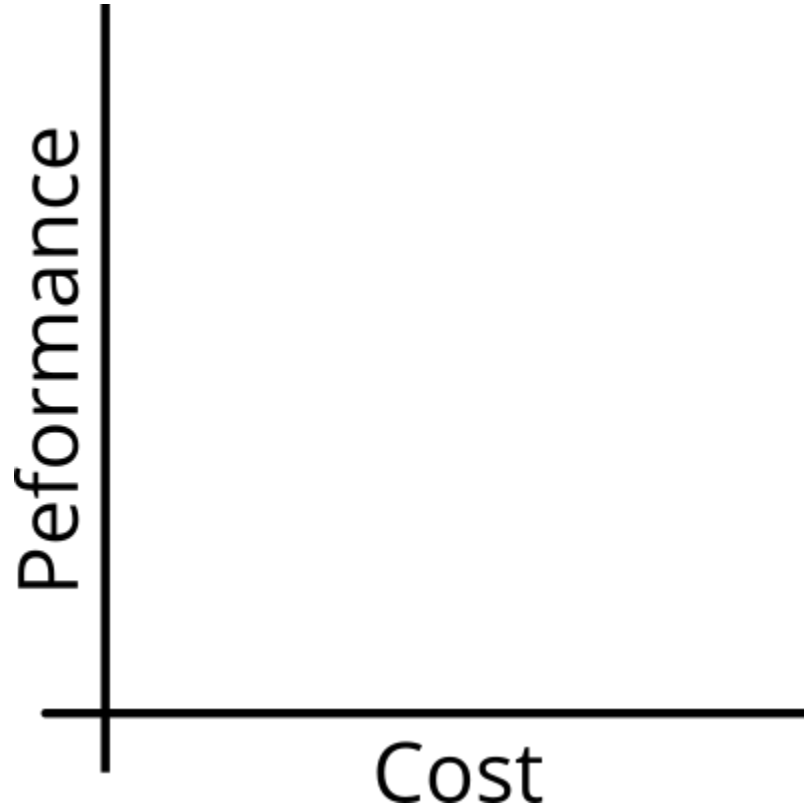
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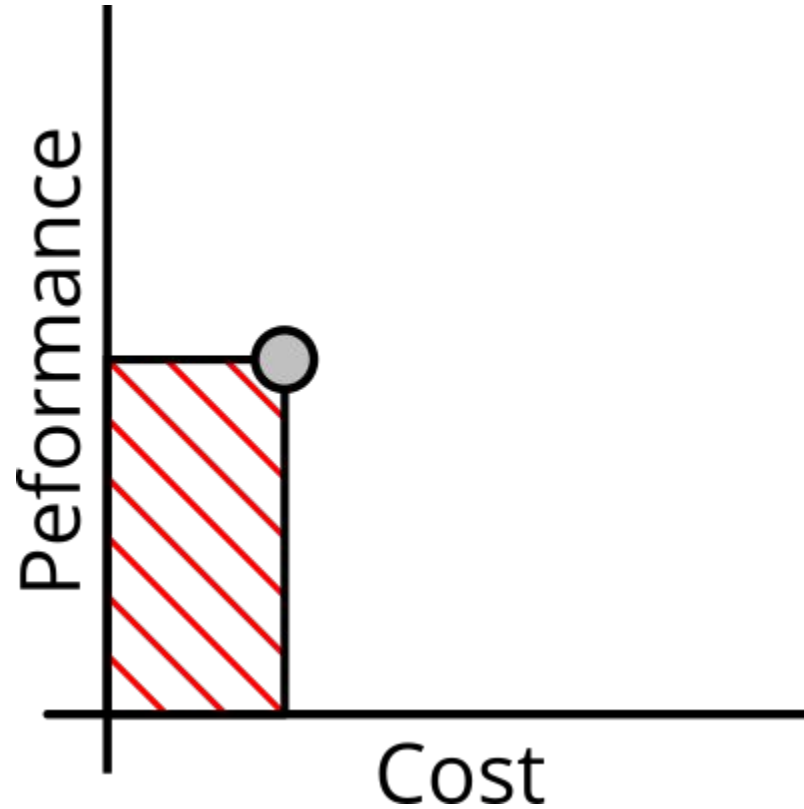
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  - But there's still immense room for optimization. Including library pruning, and more efficient encoding of constraints.
- We need some way for the user to figure out what their specifications actually mean, especially for large classes of wildly different parts.
- We need to do research on making our system more expressive, as of now it cannot handle things like timing requirements or isolated power domains.

# EDG can help automate optimization

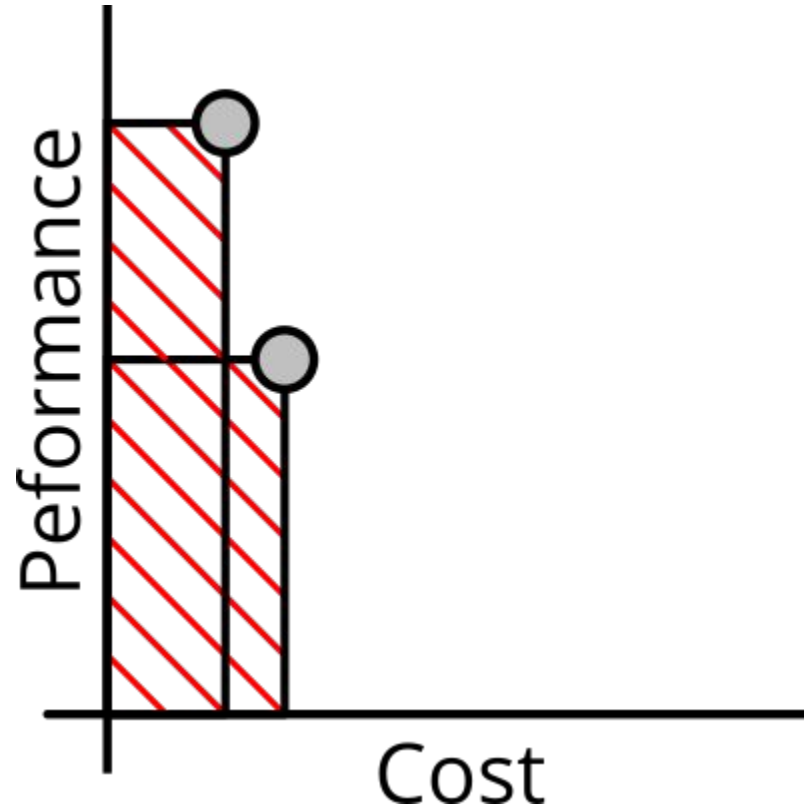
## EDG can help automate optimization



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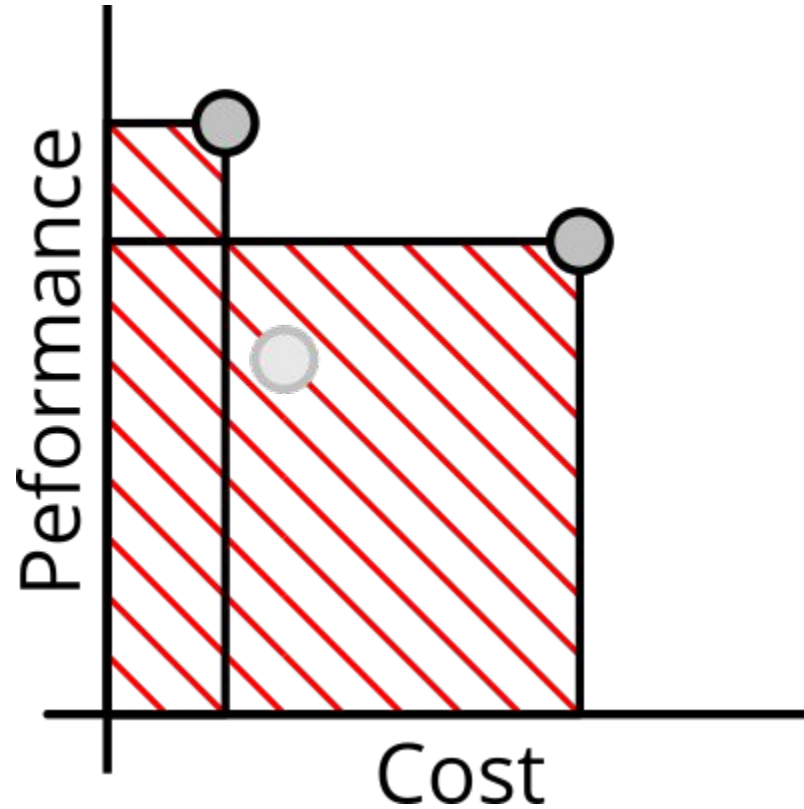


## EDG can help automate optimization

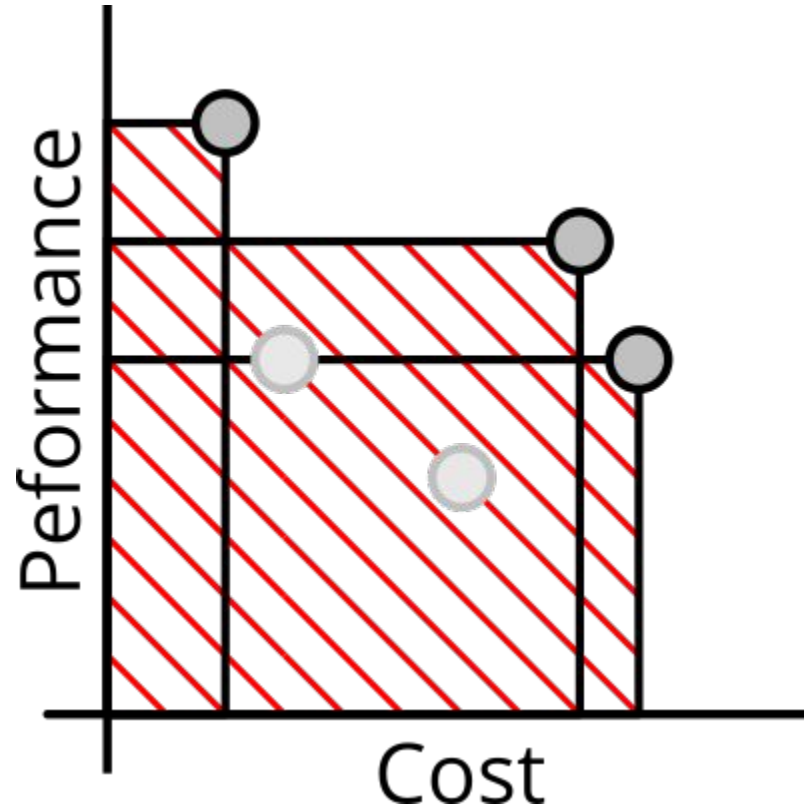




## EDG can help automate optimization



## EDG can help automate optimization



# Questions?

