Nonbinary Quasi-Cyclic LDPC Cycle Codes with Low-Density Systematic Quasi-Cyclic Generator Matrices*

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SUMMARY In this letter, we propose an appealing class of nonbinary quasi-cyclic low-density parity-check (QC-LDPC) cycle codes. The parity-check matrix is carefully designed such that the corresponding generator matrix has some nice properties: 1) systematic, 2) quasi-cyclic, and 3) sparse, which allows a parallel encoding with low complexity. Simulation results show that the performance of the proposed encoding-aware LDPC codes is comparable to that of the progressive-edge-growth (PEG) constructed nonbinary LDPC cycle codes.


In this letter, we propose a special class of nonbinary QC-LDPC cycle codes over finite fields, which are featured with efficient encoding method for general QC-LDPC codes by finding out the generator matrix in systematic-circulant form [12]. This method facilitates parallel encoding and can be generalized to encode nonbinary codes [13]. However, the resultant generator matrix is in general dense [14], which makes the encoder quite resource-consuming especially for codes over high-order finite fields, a motivation for us to exploit appropriate code structure to make the generator matrix also sparse.

In this letter, we propose a special class of nonbinary QC-LDPC cycle codes over finite fields, which are featured by the following properties:

- Ultra-sparse quasi-cyclic parity-check matrix.
- Sparse systematic quasi-cyclic generator matrix.
- Parallel encoding with low complexity.

2. Nonbinary Quasi-Cyclic LDPC Codes

Binary QC-LDPC codes have been widely investigated in the literature. The code construction is usually described in two steps: 1) build a binary base matrix; 2) lift the base matrix with zero matrices and sparse circulant matrices such as circulant permutation matrices (CPMs).

2.1 Definition of Nonbinary QC-LDPC Codes

Nonbinary QC-LDPC codes can be seen as a generalization of binary QC-LDPC codes. To lift the base matrix, Lin et al. introduced a special type of CPM called \( \alpha \)-multiplied CPM (\( \alpha \) is a primitive field element) in which each row is the right cyclic-shift of the row above it multiplied by \( \alpha \) and the first row is the right cyclic-shift of the last row multiplied by \( \alpha \) [13]. Peng et al. generalized it to \( \beta \)-multiplied CPM (\( \beta \) is some definite power of \( \alpha \)) [15]. In both constructions, the nonzero entries of a CPM are assigned with different nonzero field elements. In this letter, we consider another simple QC lifting method, in which each CPM is assigned with a single nonzero field element.

Mathematically, the parity-check matrix has a compact representation and can be defined by

\[
H = \begin{pmatrix}
\epsilon_{0,0} P_{0,0} & \epsilon_{0,1} P_{0,1} & \cdots & e_{0,m-1} P_{0,m-1} \\
\epsilon_{1,0} P_{1,0} & \epsilon_{1,1} P_{1,1} & \cdots & e_{1,m-1} P_{1,m-1} \\
\vdots & \vdots & \ddots & \vdots \\
e_{m-1,0} P_{m-1,0} & e_{m-1,1} P_{m-1,1} & \cdots & \epsilon_{m-1,m-1} P_{m-1,m-1}
\end{pmatrix}
\]
where $e_{i,j} \in GF(2^b)$, $q_{i,j} \in \{0,1,\ldots,L-1\}$. $P_{i,j}^{q_{i,j}}$ is an $L \times L$ zero matrix if $q_{i,j} = \infty$; otherwise, it is an $L \times L$ CPM obtained by cyclically shifting the rows of the identity matrix $I$ to the right by $q_{i,j}$ times. Thus, each CPM $e_{i,j}P_{i,j}^{q_{i,j}}$ in $H$ is specified by a field element $e_{i,j}$ and a shift factor $q_{i,j}$, and can be denoted by a 2-tuple $(e_{i,j}, q_{i,j})$.

As is proved in [12], if the rightmost $m$ block-columns (i.e. the rightmost $mL$ columns) of $H$ are linearly independent, the corresponding generator matrix can be written as

$$G = \begin{bmatrix} 1 & \mathbf{G}_p \end{bmatrix}$$

where $k = n - m$; $I$ and $\mathbf{G}_p$ correspond to the systematic part and the parity part, respectively; $\mathbf{G}_{i,j}$ is an $L \times L$ circulant matrix, which usually has high-density.

### 2.2 Two Optimization Methods

Two optimization methods for code construction were proposed in the literature in terms of cycle property and minimum distance property.

**Theorem 1** ([9]). Let $P_1 \rightarrow P_2 \rightarrow \cdots \rightarrow P_{2l} \rightarrow P_1$ be the chain corresponding to a length-$2l$ block-cycle in $H$, if $r$ is the least positive integer such that

$$r \cdot \sum_{i=1}^{2l} (-1)^i q_i \equiv 0 \quad \text{(mod $L$)} \quad (3)$$

then the block-cycle leads to $L$ length-$2lr$ cycles in $H$.

From Theorem 1, we can optimize the shift factors $\{q_i\}$ to eliminate short cycles in $H$.

**Theorem 2** ([8],[15]). Let $(e_1, e_2, \cdots, e_{2l}) \in GF(q)$ be the nonzero field elements assigned to a length-$2l$ cycle in $H$, then the cycle provides $(q-1)$ weight-1 codewords if and only if

$$\prod_{i=1}^{l} e_{2i-1}e_{2i}^{-1} = 1. \quad (4)$$

From Theorem 2, we can optimize the nonzero field elements assigned to the nonzero entries in $H$ to avoid low-weight codewords and thus improve the minimum distance.

### 3. Code Structure and Encoding Circuit

In this section, we exploit a special code structure which enables every $G_{i,j}$ in $G$ of (2) to be a CPM, thus resulting in a low-density generator matrix.

#### 3.1 A Useful Matrix

Consider the following matrix which will be used in our code construction

$$M = \begin{bmatrix} a_0P_0^0 & b_0P_0^0 & 0 & \cdots & 0 \\ 0 & a_1P_1b_1P_1^0 & \cdots & \vdots & \vdots \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ 0 & \cdots & 0 & a_{m-2}P_{m-2}^0b_{m-2} & 0 \\ b_{m-1}P_{m-1}^s & 0 & \cdots & \cdots & 0 \\ a_{m-1}P_{m-1}^s \\ \end{bmatrix} \quad (5)$$

where $a_i, b_i \in GF(2^b)$, $a_i, b_i \neq 0$; $P_0^0, P_0$ are $L \times L$ CPMs. We give a sufficient condition for $M$ to be non-singular, under which the inverse of $M$ is composed of CPMs.

**Proposition.** $M$ is non-singular if

$$\prod_{i=0}^{m-1} (t_i - s_i) \equiv 0 \quad \text{(mod $L$)} \quad \text{and} \quad \prod_{i=0}^{m-1} (a_i^{-1}b_i) \neq 1. \quad (6)$$

**Proof.** Using elementary row operations, $M$ is transformed to an equivalent matrix $M'$.

$$M' = \begin{bmatrix} a_0P_0^0 & b_0P_0^0 & 0 & \cdots & 0 \\ 0 & a_1P_1b_1P_1^0 & \cdots & \vdots & \vdots \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ 0 & \cdots & 0 & a_{m-2}P_{m-2}^0b_{m-2} & 0 \\ b_{m-1}P_{m-1}^s & 0 & \cdots & \cdots & 0 \\ 0 & \cdots & \cdots & a_{m-1}P_{m-1}^s & 0 \\ \end{bmatrix} \quad (7)$$

where $D = a_{m-1}^{-1}P_{m-1}^{-s}I + \left[ \prod_{i=0}^{m-1} a_i^{-1}b_i \right] \left( P_{m-1}^{-s} \sum_{i=0}^{m-1} (t_i - s_i) \right)$. Since $\det(M) = \det(M') = \det(D) \neq 0$, where $\det$ denotes the matrix determinant. If $\det(M) \neq 0$ if and only if $\det(D) \neq 0$. If $\sum_{i=0}^{m-1} (t_i - s_i) \equiv 0 \quad \text{(mod $L$)} \text{ and } \prod_{i=0}^{m-1} (a_i^{-1}b_i) \neq 1$, $\det(D) \neq 0$, and therefore $\det(M) \neq 0$. □

By performing rather cumbersome elementary row operations on the augmented matrix $[M | I]$ until it reaches reduced row echelon form $[I | M^{-1}]$, the inverse matrix $M^{-1}$
can be obtained as in (8) where \( \alpha = 1 + \prod_{i=0}^{m-1} (a_i^{-1} b_i) \). It can be easily seen that \( M^{-1} \) is composed of CPMs.

When carefully examining the conditions in (6), we note that \( M \) provides \( L \) length-2\(m'\) cycles and these cycles do not lead to low-weight codewords, according to Theorem 1 and Theorem 2.

3.2 Code Structure

The parity-check matrix of our proposed code is defined as

\[
H = \begin{bmatrix}
H_s & H_p
\end{bmatrix} = \begin{bmatrix}
H_{s1} & H_{p1} & 0 & \cdots & 0 \\
H_{s2} & 0 & H_{p2} & \cdots & 0
\end{bmatrix}
\]  

(9)

where \( H_s \) and \( H_p \) correspond to the systematic part and the parity part, respectively; \( H_{pi} \) and \( H_{pj} \) are both in the form of matrix \( M \); \( H_{s1} \) and \( H_{s2} \) are both composed of \( L \times L \) CPMs and \( L \times L \) zero matrices.

The corresponding generator matrix is defined as

\[
G = \begin{bmatrix} I \vert G_p \end{bmatrix} = \begin{bmatrix} I \vert G_{p1} & G_{p2} \end{bmatrix}.
\]  

(10)

Then by \( H G^T = 0 \), we have

\[
G_{p1}^T = H_{p1}^{-1} H_{s1} \quad \text{and} \quad G_{p2}^T = H_{p2}^{-1} H_{s2}.
\]  

(11)

**Theorem 3.** Let \( H \) be given in (9), if the column-weights of \( H_s \) and \( H_p \) are both one, then there exists a corresponding \( G \) in the form of (2) with each \( G_{i,j} \) being a CPM.

**Proof.** If \( H_s \) and \( H_p \) satisfy the condition in Theorem 3, then there is only one \( L \times L \) CPM in each block-column of \( H_s \) and \( H_p \). And since \( H_{s1}^{-1} \) and \( H_{p1}^{-1} \) are both composed of \( L \times L \) CPMs as shown in (8), it can be easily checked from (10)–(11) that \( G \) has the form of (2) and each \( G_{i,j} \) is an \( L \times L \) CPM.

**Remark.** The parity-check matrix constructed according to Theorem 3 has a uniform column-weight of two, thus giving a nonbinary cycle code. And most interestingly, the parity part of the corresponding generator matrix is composed of CPMs, which makes the generator matrix much sparser than an ordinary one.

3.3 Encoding Circuit

Encoding is the process of determining the parity sequence \( v \) given the information sequence \( u \). To encode, the information sequence \( u \) is divided into \( k \) sections, \( u = (u^{(0)}, u^{(1)}, \cdots, u^{(k-1)}) \), and each section consists of \( L \) consecutive components of \( u \). Likewise, the parity sequence \( v \) is divided into \( m \) sections, \( v = (v^{(0)}, v^{(1)}, \cdots, v^{(m-1)}) \), and each section consists of \( L \) consecutive components of \( v \).

Then from (2), we have

\[
v^{(j)} = u^{(0)} G_{0,j} + u^{(1)} G_{1,j} + \cdots + u^{(k-1)} G_{k-1,j}.
\]  

(12)

The \( j \)th parity section \( v^{(j)} \) can be computed by a shift-register-multiplier-adder (SRMA) circuit, as shown in Fig. 1. The tapping point in the \( i \)th cyclic shift register is determined by the shift factor of the CPM \( G_{i,j} \), and \( g_{i,j} \) is the non-zero field element specifying \( G_{i,j} \).

The entire encoder is obtained by parallel concatenating \( m \) SRMA circuits, as shown in Fig. 2. Note that the \( m \) SRMA circuits share the same set of \( k \) cyclic shift registers. After \( u^{(0)}, u^{(1)}, \cdots, u^{(k-1)} \) are loaded into the \( k \) cyclic shift registers, the \( m \) SRMA circuits generate \( v^{(0)}, v^{(1)}, \cdots, v^{(m-1)} \) in parallel in \( L \) clock-cycles. Thanks to the low-density characteristic of the proposed generator matrix, only \( km \) field elements are needed to be stored and only \( km \) two-input finite field multipliers plus \( (k-1)m \) two-input finite field adders are needed to be implemented in the encoder, much fewer than the logic and storage requirements of encoders based on ordinary generator matrices. These resource savings due to the sparse generator matrix may compensate for the resource-consuming computations over high-order finite fields.

4. Simulation Results

In this section, using the proposed structure, we construct a QC-LDPC cycle code over GF(64) of length 168 and rate-1/2. The parity-check matrix is defined as

\[
H = \begin{bmatrix}
(32,6) & (22,4) & (44,0) \\
(22,2) & (28,5) & (36,0) \\
(62,1) & (32,1) & (35,1)
\end{bmatrix}
\]
According to (8), the parity part of the corresponding generator matrix is

\[
G_p = \begin{bmatrix}
(36,3) & (41,4) & (47,6) & (35,4) & (29,4) & (25,4) & (21,4) & (17,4) \\
(46,5) & (30,5) & (36,6) & (43,6) & (49,6) & (55,6) & (61,6) & (67,6) \\
(34,6) & (20,6) & (26,6) & (12,6) & (18,6) & (24,6) & (30,6) & (36,6) \\
(52,6) & (48,6) & (54,6) & (60,6) & (66,6) & (72,6) & (78,6) & (84,6) \\
(40,0) & (46,0) & (52,0) & (58,0) & (64,0) & (70,0) & (76,0) & (82,0) \\
(60,5) & (56,5) & (52,5) & (48,5) & (44,5) & (40,5) & (36,5) & (32,5) \\
(50,4) & (46,4) & (42,4) & (38,4) & (34,4) & (30,4) & (26,4) & (22,4) \\
(44,2) & (40,2) & (36,2) & (32,2) & (28,2) & (24,2) & (20,2) & (16,2) \\
(36,3) & (41,3) & (46,3) & (41,3) & (36,3) & (31,3) & (26,3) & (21,3) \\
(46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) \\
(36,6) & (41,6) & (46,6) & (51,6) & (56,6) & (61,6) & (66,6) & (71,6) \\
(40,0) & (46,0) & (52,0) & (58,0) & (64,0) & (70,0) & (76,0) & (82,0) \\
(50,4) & (46,4) & (42,4) & (38,4) & (34,4) & (30,4) & (26,4) & (22,4) \\
(44,2) & (40,2) & (36,2) & (32,2) & (28,2) & (24,2) & (20,2) & (16,2) \\
(36,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) \\
(46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) \\
(36,6) & (41,6) & (46,6) & (51,6) & (56,6) & (61,6) & (66,6) & (71,6) \\
(40,0) & (46,0) & (52,0) & (58,0) & (64,0) & (70,0) & (76,0) & (82,0) \\
(50,4) & (46,4) & (42,4) & (38,4) & (34,4) & (30,4) & (26,4) & (22,4) \\
(44,2) & (40,2) & (36,2) & (32,2) & (28,2) & (24,2) & (20,2) & (16,2) \\
(36,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) \\
(46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) \\
(36,6) & (41,6) & (46,6) & (51,6) & (56,6) & (61,6) & (66,6) & (71,6) \\
(40,0) & (46,0) & (52,0) & (58,0) & (64,0) & (70,0) & (76,0) & (82,0) \\
(50,4) & (46,4) & (42,4) & (38,4) & (34,4) & (30,4) & (26,4) & (22,4) \\
(44,2) & (40,2) & (36,2) & (32,2) & (28,2) & (24,2) & (20,2) & (16,2) \\
(36,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) & (46,3) & (41,3) \\
(46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) & (46,5) & (41,5) \\
\end{bmatrix}
\]

The size of each CPM is $7 \times 7$. We use the primitive polynomial $P(x) = x^8 + x + 1$ over GF$(2)$ to construct GF$(64)$. Field elements are represented by integer notation. For comparison, we also construct a progressive-edge-growth (PEG) LDPC cycle code with the same length, rate, and field order. Shift factors in the parity-check matrix are optimized according to Theorem 1 for our proposed code. For both codes, nonzero field elements in the parity-check matrices are optimized according to Theorem 2.

The simulation scenario is as follows. The codes are modulated using BPSK and transmitted over the AWGN channel. FFT-QSPA is employed as the decoding algorithm and the maximum iteration number is set to be 80. For each simulation point, we run until at least 100 frame errors are detected. The BER and FER performances are shown in Fig. 3. It is seen that the proposed code has almost the same performance as the PEG constructed code in the low to moderate signal-to-noise ratio (SNR) region, and performs slightly better in the high SNR region.

5. Conclusion

We have proposed a special class of nonbinary QC-LDPC cycle codes with sparse systematic quasi-cyclic generator matrices that allow low-complexity parallel encoding. The encoder can be implemented through simple shift-register circuits with low logic and memory requirements. Simulation results show that the proposed codes have no performance degradation due to the constraint on their special structure, as compared to the PEG constructed nonbinary LDPC code.

6. References


Fig. 3 Performance comparison between the proposed LDPC code and the PEG constructed LDPC code.