The 16th IEEE International Conference on High Performance Computing and Communications
HPCC 2014

The 11th IEEE International Conference on Embedded Software and Systems
ICESS 2014

The 6th International Symposium on Cyberspace Safety and Security
CSS 2014

August 20-22, 2014
Paris, France

Conference Program and Information Booklet

Organized by
FEMTO-ST Institute, Ecole Centrale Paris, Ecole des Mines de Paris
France

Sponsored by
IEEE, IEEE TCSC, IEEE Computer Society, CNRS, FEMTO-ST Institute, Ecole
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HPCC / ICESS / CSS 2014 Program at a glance

Wednesday, August 20

Thursday, August 21

Friday, August 22
IEEE INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING AND COMMUNICATIONS (HPCC 2014)

HPCC DAR1: DISTRIBUTED ARCHITECTURE
Thursday 10:30, Main room  session chair:

Enabling PGAS Productivity with Hardware Support for Shared Address Mapping; a UPC Case Study
Olivier Serres, Abdullah Kayi, Ahmad Anbar and Tarek El-Ghazawi.

HoL-blocking Avoidance Policies in Direct Topologies
Roberto Péñaranda, Crispín Gomez Requena, María Gomez, Pedro Lopez and José Duato.

Analyzing the Optimal Voltage/Frequency Pair in Fault-Tolerant Caches
Vincent Lorente, Alejandro Valero, Salvador Petit and Julio Sahuquillo.

HPCC DAR2: DISTRIBUTED ARCHITECTURE
Thursday 15:00, Main room  session chair:

Dynamic WCET Estimation for Real-Time Multicore Embedded Systems Supporting DVFS
José Luis March, Salvador Petit, Julio Sahuquillo, Houcine Hassan and José Duato.

A Flexible and Scalable Affinity Lock for the Kernel (SP)
Benlong Zhang

Remapping NUCA: Improving NUCA Cache’s Power Efficiency (SP)
Hui Wang, Chunrong Lai, Yicong Huang, Shih-Lien Lu, Rui Wang, Zhongzhi Luan and Depei Qian.

SANAM: An Energy- and Cost-Efficient Multi-GPU Supercomputer (SP)

HPCC DAL1: DISTRIBUTED ALGORITHMS
Wednesday 10:30, V106a  session chair:

Kim Feldhoff, Martin Flehmig, Ulf Markwardt, Wolfgang E. Nagel, Maria Schütte and Andrea Walther.

Accelerating solution of Helmholtz equations with Iterative Krylov Methods on GPU
Abal-Kassim Cheik Ahamed and Frédéric Magoules.

Spectral Domain Decomposition Method for Natural Lighting and Medieval Glass Rendering
Guillaume Gbikpi-Benissan, Rémi Cerise, Patrick Callet and Frédéric Magoules.

A synchronous parallel max-flow algorithm for real-world networks
Guojing Cong.
HPCC DA12: Distributed Algorithms
Wednesday 15:00, V106a

Benefit of Unbalanced Traffic Distribution for Improving Local Optimization Efficiency in Network-on-Chip
Weiwei Fu, Yuan Mingmin, Tianzhou Chen, Qingsong Shi, Li Liu and Minghai Wu.

Research on Mahalanobis Distance Algorithm optimization based on OpenCL
Qingchun Xie, Yunquan Zhang and Haipeng Jia.

HSR: Hierarchical Source Routing Model for Network-on-Chip
Yuan Mingmin, Fu Weiwei, Chen Tianzhao and Wu Minghai.

HPCC DA13: Distributed Algorithms
Wednesday 17:00, V106a

An Exploration on Quantity and Layout of Wireless Nodes for Hybrid Wireless Network-on-chip
Chen Tianzhou, Yuan Mingmin, Fu Weiwei and Wu Minghai.

Acceleration of Stereo-Matching on Multi-core CPU and GPU
Tian Xu, Paul Cockshott and Susanne Oehler.

A technique for the long term preservation of finite element meshes
Peter Ivanyi.

HPCC DA14: Distributed Algorithms
Wednesday 17:00, V107

Parallel Sub-structuring for solving sparse linear systems on a cluster of GPU
Abal-Kassim Cheik Ahamed and Frédéric Magoules.

Fast and Green computing with Graphics Processing Units for the solving of sparse linear systems
Abal-Kassim Cheik Ahamed, Alban Desmaison and Frédéric Magoules.

Coupling and Simulation of Fluid-Structure Interaction Problems for Automotive Sun-roof on Graphics Processing Unit
Liang Simon Lai, Choi-Hong Lai, Abal-Kassim Cheik Ahamed and Frédéric Magoules.

HPCC DA15: Distributed Algorithms
Thursday 10:30, V106a

Comparison of Xeon Phi and Kepler GPU performance for finite element numerical integration (SP)
Krzysztof Banas and Filip Krzuzel.

Efficient Work-Stealing with Blocking Deques (SP)
Chi Liu, Yi Liu and Ping Song.

On the Optimization of HDA* for Multicore Machines. (SP)
Victoria Maria Sanz, Armando De Giusti and Marcelo Naiouf.

HPCC DA16: Distributed Algorithms
Thursday 15:00, V106a

A Scheduling Algorithm to Reduce Energy Consumption for Precedence-constrained Parallel Applications in Data Centers (SP)
Vahid Ebrahimirad, Aboozar Rajabi and Maziar Goudarzi.

Optimizing Cache Locality for Irregular Data Accesses on Many-Core Accelerator Chips (SP)
Nhat-Phuong Tran and Myungho Lee.

LU Factorization of Small Matrices: Accelerating Batched DGETRF on the GPU (SP)
Tingxing Dong, Azzam Haidar, Piotr Luszczek, James Austin Harris, Stan Tomov and Jack Dongarra.

GPU acceleration of Newton's method for large systems of polynomial equations in double double and quad double arithmetic (SP)
Jan Verschelde and Xiangcheng Yu.

HPCC CCWS1: Cloud Computing and Web Services
Wednesday 10:30, V107

An Energy-Efficient VM Placement in Cloud Datacenter
Fei Teng, Daoting Dong, Lei Yu and Frederic Magoules.

Reducing Memory in Software-Based Thread-Level Speculation for JavaScript Virtual Machine Execution of Web Applications (SP)
Jan Kasper Martinsen, Håkan Grahn, Anders Isberg and Henrik Sundström.

Algorithms for Balanced Graph Bi-partitioning (SP)
Jiang Wu, Guiyuan Jiang, Lili Zheng and Shiqing Zhou.

Optimizing the Topologies of Virtual Networks for Cloud-based Big Data Processing
Cong Xu, Jiahai Yang, Hui Yu, Haihuo Liu and Hui Zhang.

HPCC CCWS2: Cloud Computing and Web Services
Wednesday 10:30, V106b

Accelerating Massive VMs Booting Up
Dayang Zheng, Hai Jin and Xiaofei Liao.

Performance Driven Cloud Provisioning
Jay Kiruthika and Souheil Khaddaj.

The HP3 service: reduction of cost and transfer time for storing data on clouds
Jorge Veiga, Guillermo Taboada, Xoán Carlos Pardo and Juan Tourino.

Securing Cloud Users at Runtime via a Market Mechanism: A case for Federated Identity
Giannis Tziakouris, Carlos Joseph Mera Gómez and Rami Bahsoon.

HPCC CCWS3: Cloud Computing and Web Services
Wednesday 10:30, V107

Cost-effective Virtual Machine Image Replication Management for Cloud Data Centers
Dian Shen, Fang Dong, Junxue Zhang and Junzhou Luo.

ZDLC-Based Modelling and Simulation of Enterprise Systems
Bippin Makoond, Steve Ross-Talbot, Souheil Khaddaj and Stefan Franczak.

Virtual Machine Scheduling Considering Both Computing and Cooling Energy (SP)
Xiang Li, Xiaohong Jiang and Yanzhang He.
HPCC CCWS4: CLOUD COMPUTING AND WEB SERVICES
Wednesday 17:00, Main room  
session chair:
Cloud Energy Broker: Towards SLA-driven Green Energy Planning for IaaS Providers
Md Sabbir Hasan, Yousri Kouki, Thomas Ledoux and Jean-Louis Pazat
Enabling Prioritized Cloud I/O Service in Hadoop Distributed File System (SP)
Tsozen Yeh and Yifeng Sun.
Implementation of the KVM hypervisor on several cloud platforms: tuning the Apache
CloudStack agent (SP)
Fernando Gomez-Folgar, Antonio Jesus Garcia-Loureiro, Tomas Fernandez Pena, Jose Isaac
Zablah and Natalie Szecane.

HPCC CCWS5: CLOUD COMPUTING AND WEB SERVICES
Thursday 15:00, L106  
session chair:
Harnessing Memory Page Distribution for Network-Efficient Amortized Live Migration (SP)
Amel Haji, Asma Ben Letaifa and Sami Tabbane.
Service deployment in cloud (SP)
MOBBS: Multi-tier Block Storage System for Virtual Machines using Object-based Storage
(Sp)
Sixiang Ma, Haopeng Chen, Heng Lu, Bin Wei and Pujiang He.

HPCC SEC1: SCIENTIFIC AND ENGINEERING COMPUTING
Wednesday 15:00, V106b  
session chair:
Improving the Scalability of a Hurricane Forecast System in Mixed-Parallel Environments
Thiago Quirino and Javier Delgado.
CESMTuner: An Auto-Tuning Framework for the Community Earth System Model
Ding Nan, 柳丁, Wei Xue, Xu Ji, Haoyu Xu and 宋振亚.
The virtual open page buffer for multi-core and multi-thread processors
Hongwei Zhou, Rangyu Deng and Zefu Dai.

HPCC SEC2: SCIENTIFIC AND ENGINEERING COMPUTING
Wednesday 17:00, V106b  
session chair:
On the performance of the WRF numerical model over complex terrain on a high performance computing cluster
Nicholas Christakis, Theodoros Katsaounis, George Kossioris and Michael Plexousakis.
Power consumption analysis of parallelized algorithms on GPUs
Abal-Kassim Cheik Ahamed, Frederic Magoules, Alban Desmaison, Jean-Christophe Léchenet,
Francois Mayer, Hafifa Ben Salem and Thomas Zhu.
targetDP: an abstraction of lattice based parallelism with portable performance (SP)
Alan Gray and Kevin Stratford.

HPCC SEC3: SCIENTIFIC AND ENGINEERING COMPUTING
Wednesday 17:00, V128  
session chair:
Communication optimal least squares solver (SP)
Pawan Kumar.
FLLOP: A massively parallel solver combining FETI domain decomposition method and
quadratic programming (SP)
Vaclav Hapla, Martin Cermak, Alexandros Markopoulous and David Horak.
Performance implication of multicore cache locking on general-purpose processors (SP)
Matthew Loach and Wei Zhang.

HPCC SEC4: SCIENTIFIC AND ENGINEERING COMPUTING
Thursday 10:30, V107  
session chair:
SRFTL: An adaptive superblock-based real-time flash translation layer for NAND flash memory
Xin Li.
Exploiting hybrid SPM-cache architectures to reduce energy consumption for embedded computing
Wei Zhang and Lan Wu.
Texture directed mobile GPU power management for closed-source games.
Beilei Sun, Li Xi, Jiuchen Song, Zhihan Cheng, Yuan Xu, Xuchai Zhou and Mingming Sun.

HPCC DAT1: DISTRIBUTED APPLICATIONS AND TECHNOLOGIES
Wednesday 10:30, Main room  
session chair:
Predicting performance of hybrid Master/Worker applications using model-based regression trees
Abel Castellanos, Andreu Moreno, Joan Sorribes and Tomas Margalef.
Leveraging hierarchical data locality in parallel programming models (SP)
Ahmad Anbar, Engin Kayraklioglu, Olivier Serres and Tarek El-Ghazawi.
Trajectory pattern mining over a cloud-based framework for urban computing
Albino Altemare, Eugenio Cesario, Carmela Comito, Fabrizio Marozzo and Domenico Talia.
GPU maps for the space of computation in triangular domain problems
Cristobal Navarro and Nancy Hitzfeld.

HPCC DAT2: DISTRIBUTED APPLICATIONS AND TECHNOLOGIES
Wednesday 15:00, V106b  
session chair:
Look before you leap: Using the right hardware resources to accelerate applications
jie Shen, Ana Lucia Varbanescu and Henk Sips.
An integrated hardware-software approach to task graph management
Nina Engelhardt, Tamer Dallou, Ahmed Elhossini and Ben Juurlink.
A metadata update strategy for large directories in wide-area file systems (SP)
Guo-Liang Liu and Jing Huang.
HPCC DAT3: DISTRIBUTED APPLICATIONS AND TECHNOLOGIES
Thursday 15:00, V106b

Modelling and Stochastic Simulation of Synthetic Biological Boolean Gates (SP)
Daven Sanassy, Harold Fellermann, Natalio Krasnogor, Savas Konur, Marian Gheorghe, Christophe Ladroue, Sara Kalvala and Laurentiu Mierla.

High Performance Simulations of Kernel P Systems (SP)
Florentin Eugen Istrate, Marian Gheorghe, Savas Konur, Ionut Mihai Niculescu and Mehmet E Bakir.

Optimizing GPU Virtualization with Address Mapping and Delayed Submission (SP)
Xiaolin Wang, Yan Sang, Zhenlin Wang and Yingwei Luo.

Buffer on Last Level Cache for CPU and GPGPU data sharing (SP)
Licheng Yu, Tianzhou Chen and Minghui Wu.

HPCC MCN1: MOBILE COMPUTING AND NETWORKING
Wednesday 15:00, V127

Conflict-free Opportunistic Centralized Time Slot Assignment in Cognitive Radio Sensor Networks
Ons Mabrouk, Pascale Minet, Hanen Jlouadi and Leila Saidane.

Network Aware and Power-based Resource Allocation in Mobile Ad hoc Computational Grid
Sayed Chhattan Shah.

An Inter-Frame Correlation Based Error Concealment Of Immittance Spectral Coefficients For Mobile Speech And Audio Codecs
Yuhong Yang, Shaolong Dong, Ruimin Hu, Yanye Wang, Li Gao and Maosheng Zhang.

HPCC MCN2: MOBILE COMPUTING AND NETWORKING
Wednesday 17:00, V127

Performance Analysis for New Call Bounding Scheme with SFR in LTE-Advanced Networks
Muhammad Safwat, Hesham El-Badawy, Ahmad Yahya and Hosni El-Motawy.

Adaptive Detection for STBCs in IEEE 802.11ac (SP)

Concealed Data Aggregation for Data Integrity in Wireless Sensor Networks (SP)
Ben Othman Soufiene.

HPCC MCN3: MOBILE COMPUTING AND NETWORKING
Thursday 10:30, V106b

On Delivery Delay-Constrained Throughput and End-to-end Delay in MANETs
Yujian Fang, Yuezi Zhou, Xiaohong Jiang and Yaoxue Zhang.

Source misrouting in King topologies
Esteban Stafford, Carmen Martinez, Jose Luis Bosque, Fernando Vallejo, Cristóbal Camarero, Borja Perez and Ramon Beivide.

Avoiding Tree Saturation in the Face of Many Hotspots with Few Buffers
Bradley Kuszmaul and William Kuszmaul.

HPCC MCN4: MOBILE COMPUTING AND NETWORKING
Thursday 15:00, V106b

Simultaneous Optical Path-Setup for Reconfigurable Photonic Networks in Tiled CMPs (SP)
Paolo Granti and Sandro Bartolini.

Packet storage at multi-gigabit rates using off-the-shelf systems (SP)

SyncSnap: Synchronized Live Memory Snapshots of Virtual Machine Networks
Bin Shi, Bo Li, Lei Cui, Jieyu Zhao and Jianxin Li.

A Multi-layer Hierarchical Inter-Cloud Connectivity Model for Sequential Packet Inspection of Tenant Sessions Accessing BI as a Service
Hussain Al-Aqrabi, Lu Liu, Richard Hill and Nick Antonopoulos.

HPCC SCUC1: SECURITY, COLLABORATIVE AND UBQUITOUS COMPUTING
Thursday 10:30, V107

Blueprint: Exploring a Safe, Scalable Agent Model
Alex Muscar.

Host-based Card Emulation: development, security, and ecosystem impact analysis (SP)
Mouhammad Alattar and Mohammed Athamleh.

A Provably Pairing-free Certificateless Authenticated Group key Agreement Protocol (SP)
Xiaozhuo Gu, Taizhong Xu, Weihua Zhou and Yongming Wang.

HPCC SCUC2: SECURITY, COLLABORATIVE AND UBQUITOUS COMPUTING
Thursday 15:00, V107

Cooperative Caching in Coding-based Data Broadcast Environments (SP)
Houling Ji, Victor C.S. Lee, Chi-Yin Chow, Kai Liu and Guoqiang Wu.

CGK: A Collaborative Group Key Management Scheme (SP)
Fatima Hendaoui, Hamdi Eltaief, Habib Youssef and Abdelbasset Tad.

A Provisioning Service for Automatic Command Line Applications Deployment in Computing Clouds (SP)
Evgeniy Peshykin and Andrey Kuznetsov.

CGSIL: Collaborative Geo-clustering Search-based Indoor Localization (SP)
Thong M. Doan, Han N. Dinh, Nam Nguyen and An T. Pham.
ICESS1: Real-time Scheduling

Scheduling Analysis of TDMA-Constrained Tasks: Illustration with Software Radio Protocols  
Shuai Li, Frank Singhoff, Stéphane Rubini and Michel Bourdèlles

Efficient Online Benefit-Aware Multiprocessor Scheduling Using an Online Choice of Approximation Algorithms  
Behnaz Sanati and Albert M. K. Cheng

Dynamic Reservation-Based Mixed-Criticality Task Set Scheduling  
Zheng Li and Shangping Ren

Feasibility Interval of Real-Time Tasks with Arbitrary Release Offsets Under Fixed Priority Scheduling (Short Paper)  
Yu Jiang, Qiang Zhou, Xingliang Zou and Albert M. K. Cheng

ICESS2: Energy-efficient Scheduling and Resource Allocation

Voltage Island Aware Energy Efficient Scheduling of Real-Time Tasks on Multi-core Processors  
Jun Liu and Jinhua Guo

Energy Efficient Dynamic Core Allocation for Video Decoding in Embedded Multicore Architectures  
Rajesh Kumar Pal, Kolin Paul and Sanjiva Prasad

BATS: An Energy-Efficient Approach to Real-Time Scheduling and Synchronization  
Jun Wu

“CERE”: a Cache Recommendation Engine: Efficient Evolutionary Cache Hierarchy Design Space Exploration  
Gabriel Yessin, Abdel-Hameed Badawy, Vikram Narayana, David Mayhew and Tarek El-Ghazawi

ICESS3: Architecture and Systems

Online Data Allocation for Hybrid Memories on Embedded Tele-Health Systems  
Longbin Chen, Meikang Qiu and Yongxin Zhu

On Formulating Optimized Storage and Memory Space Specifications for Network Embedded Systems (Short Paper)  
Kleomenis Tsiilikos and Apostolos Meliones.

Behaviour and Performance Comparison between FreeRTOS and uC/OS-III (Short Paper)  
Long Peng, Fei Guan, Luc PerneSel and Martin Timmerman

Characterizing Energy Consumption of Real-Time and Media Benchmarks on Hybrid SPM-Caches  
Lan Wu, Yiqiang Ding and Wei Zhang

ICESS4: Energy Measurement and Management

Learning Based Power Management for Periodic Real-Time Tasks  
Fakhruddin Muhammad Mahbub Ul Islam and Man Lin

Energy Consumption Estimation of Software Components based on Program Flowcharts (SP)  
Patrick Heinrich, Hannes Bergler and Dirk Eilers

An Operation Scenario Model for Energy Harvesting Embedded Systems and an Algorithm to Maximize the Operation Quality (Short Paper)  
Kazumi Aono, Atsushi Iwata, Hideki Takase, Kazuyoshi Takagi and Naofumi Takagi

ICESS5: System on Chip (SOC) and Multicore Systems

CABSR: Congestion Agent Based Source Routing for Network-on-Chip  
Yuan Mingsmin, Fu Weiwei, Chen Tianzou and Wu Minghui

On Cache-Aware Task Partitioning for Multicore Embedded Real-Time Systems  
Aaron Lindsay and Binoy Ravindran

Task Migration for Energy Saving in Real-Time Multiprocessor Systems  
Gang Zeng, Yutaka Matsubara, Hiroyuki Tomiyama and Hiroaki Takada

ICESS6: Hardware/Software Co-Design

An FPGA Based Resources Efficient Solution for the OmniVision Digital VGA Cameras Family (Short Paper)  
Elnar Yusifli, Réda Yahiaoui, Saeed Mian Qaisar and Tijan8 Gharbi

A Locality-Preserving Write Buffer Design for Page-Mapping Multichannel SSDs  
Sheng-Min Huang and Li-Pin Chang

The RESCUE Approach – Towards Compositional Hardware/Software Co-Verification (Short Paper)  
Paula Herber

ICESS7: Embedded Security

XGRID: A Scalable Many-Core Embedded Processor (Short Paper)  
Volkan Gunes and Tony Givargis

Advanced DSP Based Narrowband PLC Modem for Smart Grids Applications (Short Paper)  
Mohamed Chaker Balli and Chihab Rebai

A Process for the Detection of Design-Level Hardware Trojans Using Verification Methods  
Christian Krieg, Michael Rathmair and Florian Schupfer
ICESS8: Network Protocols
Thursday 10:30, L109

An Efficient Admission Control Algorithm for Virtual Sensor Networks
Muhammad Ajmal Sawand, Stefano Paris, Zonghua Zhang and Farid Naït-Abdesselam

Wireless Video Sensor Network Platform and Its Application for Public Safety (SP)
Huyntae Cho

Vulnerability Analysis of Clock Synchronization Protocol Using Stochastic Petri Net
Jiajun Shen and Dongqin Feng

ICESS9: Embedded Applications
Thursday 10:30, V128

Contiki80211: An IEEE 802.11 Radio Link Layer for the Contiki OS (SP)
Ioannis Glaropoulos, Vladimir Yukadinovic and Stefan Mangold

Hybrid Routing Algorithms for Navigation Control of a Semi-Autonomous Robotic Platform (SP)
Aleksandr Mil本科yn, Ais Lin, Garth Herman, Manuel Garcia, Charles Liu, Khosrow Rad, Darrell Guillama and Helen Bossalis

Design and Implementation of Low-power Location Tracking System Based on IEEE 802.11 (SP)
Sanghyun Son and Yunju Baeck

ICESS10: Embedded OS
Thursday 15:00, L109

Planning and Optimization of Resources Deployment: Application to Crisis Management
Jason Mahdjoub and Francis Rousseaux

Modeling Basic Aspects of Cyber-Physical Systems, Part II (Extended Abstract)
Yingfu Zeng and Wald Taha

Monitoring Lick Responses in Animal Behavioral Experiments using a PSoC
Qingshan Shan, David Bullock, Christian Sumner and Trevor

Embedded Face Detection Application based on Local Binary Patterns (SP)
Laurentiu Acasandrei and Angel Barriga.

ICESS11: Embedded OS
Thursday 15:00, L107

Deadline-Aware Interrupt Coalescing in Controller Area Network (CAN)
Christian Herber, Andre Richter, Thomas Wild and Andreas Herkersdorf

SmartMig: A Case for Page Migration and Self-interleaving for On-chip Distributed Memory Systems (SP)
Weiwei Fu, Yuan Mingmin, Li Liu and Minghui Wu.

A Temporal Partition-based Linux CPU Scheduler (SP)
Xingliang Zou, Albert M. K. Cheng, Yu Li and Yu Jiang

A Novel Fault Diagnosis in Reversible Logic Circuit (SP)
Bikromaditya Mondal and Susanta Chakraborty.

THE 6TH INTERNATIONAL SYMPOSIUM ON CYBERSPACE SAFETY AND SECURITY (CSS 2014)

CSS1
Wednesday 10:30, V106

UI-Dressing to Detect Phishing
Luigi Lo Iacono, Hoai Viet Nguyen, Tobias Hirsch, Maurice Baiers and Sebastian Möller

EP2AC: An Efficient Privacy-Preserving Data Access Control Scheme for Data-Oriented Wireless Sensor Networks
Piye Yang and Tanveer Zia

Snake: an End-to-End Encrypted Online Social Network
Alessandro Barenghi, Michele Beretta, Alessandro Di Federico and Gerardo Pelosi

CSS2
Wednesday 15:00, V106

Robust Edge Based Image Steganography through Pixel Intensity Adjustment
Saiful Islam and Phalguni Gupta

Online Taint Propagation Analysis System for Detecting Bugs in Binaries
Gen Li, Shuang-Xi Wang and Ying Zhang

Data Interception through Broken Concurrency in Kernel Land
Julian Rrushi

CSS3
Wednesday 17:00, V106

Out-of-Band Authentication Model with Hashcash Brute-Force Prevention
George Violaris and Ioanna Dionysiou

A Secure Two-phase Data Deduplication Scheme
Pierre Meyre, Philippe Raipin, Frédéric Tronel and Emmanuelle Anceau

Bivariate non-Parametric Anomaly Detection
Christian Callegari, Stefano Giordano and Michele Pagano

CSS4: Short Paper Track
Thursday 10:30, L106

Security Mechanisms for a Cooperative Firewall
Hammad Kabir, Raimo Kantola and Jesús Llorente Santos

Virtual firewall performance as a waypoint on a software defined overlay network
Caismer Decusatis and Peter Mueller

Machine Learning based Cross-site Scripting Detection in Online Social Network
Rui Wang, Xiaqi Jia, Qinlei Li and Shengzhi Zhang
CSS5
Thursday 10:30, V127
Secure and Lightweight Handover with High-Altitude Stratospheric Platform
Yongdong Wu
Asynchronous Covert Communication Using BitTorrent Trackers
Mathieu Cunche, Mohamed Ali Kaafar and Roksana Boreli
Cloud Federation? We are not ready yet
Jacques Bou Abdo, Jacques Demerjian, Hakima Chaouchi, Kabalan Barbar and Guy Pujolle

WORKSHOPS AND WORKS IN PROGRESS

ALG&MOD: First International Workshop on Algorithmic and Modeling
session chair:
New Bounds of a Measure in Information Theory
Mihaela-Alexandra Popescu, Oana Slusanschi, Alexandru-Corneliu Olteanu and Florin Pop
A Semantic Rule-Based Approach Supported by Process Mining for Personalised Adaptive Learning
Kingsley Okoye, Abdel-Rahman H. Tewil, Usman Naeem, Rabih Bashroush and Elyes Lamine
SignalPU: A parallel and heterogeneous programming model for DSP applications
Farouk Mansouri, Sylvain Huet and Dominique Honezt

APP1: First International. Workshop on HPC Applications
session chair:
A Secure Electronic Transaction Payment Protocol
Houssam El Ismaili, Hanane Houmani and Hicham Madroumi
Forum-oriented Research on Water army Detection for Bursty topics
Huijie Xu
Hide-as-you-Type: An Approach To Natural Language Steganography Through Sentence Modification
Charles Clarke, Eckhard Pflügel and Dimitris Tsaptsinos

APP2: First International. Workshop on HPC Applications
session chair:
A Soft-Sensing Method of Alumina Concentration in the Electrolyte Based on ELM
Sen Zhang, Xi Chen and Yixin Yin
Structural model for the interactive effects in ERP systems usage
Billy Mathias Kalema
Experience Report: State-Replication-based Matching System
Yiqun Ding, Bo Zhou, Fan Li, Wei Li, Xinyu Wang and Tong Wu

APP3: First International. Workshop on HPC Applications
session chair:
CityPro; An Integrated City Protection Collaborative Platform
Mohamed Dbouk and Ihab Sbeity
Predefined Honeypot Context Based Platform Independent Catering Honeypot System
Wenjun Fan
Real Time Environmental Monitoring and Climatic Modeling
Andrei Lapin, Eryk Schiller, Peter Krofp, Oliver Schilling, Philip Brunner, Almerima Jamakovic-Kapic, Torsten Braun and Sergio Maffioletti

AHPCN1: 6th International Symposium on Advances of High Performance Computing and Networking
session chair:
Online Performance Analysis: An Event-based Workflow Design Towards Exascale
Michael Wagner, Tobias Hilbrich and Holger Brunst
Towards Self-aware Service Composition
Abdessalam Elhabbash, Rami Bahsoon and Peter Tino
PseudoNUMA for Reducing Memory Interference in Multi-core Systems
Gangyong Jia
Analysis of header usage patterns of HTTP request messages
Maria Carla Calzarossa and Luisa Massari

AHPCN2
Friday
session chair:
Comparison of the Predictive Powers of Phenotypes Combined by Anthropometric Index and Triglyceride for Hypertension Diagnosis based on Data Mining
Bum Ju Lee and Jong Yeol Kim
A speculative mechanism for barrier synchronization
Tianzhou Chen, Meng Jinglei, Pan Ping, Yao Jun and Wu Minghui
PCA-based Network-wide Correlated Anomaly Event Detection and Diagnosis
Yuanxun Zhang, Prasad Calyam and Mukundan Sridharan
Masaaki Terai, Peter Bryzgalov, Toshiyuki Maeda and Kazuo Minami
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<td>A Performance Analysis of Long-Term Archiving Techniques</td>
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<td>Simulation of Asynchronous Iterative Algorithms Using SimGrid</td>
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M2A2: 6TH INT. WORKSHOP ON MULTICORE AND MULTITHREADED ARCHITECTURES AND ALGORITHMS
Performance Characterization and Evaluation of HPC Algorithms on Dissimilar Multicore Architectures
Krishnan S. P. T. and Bharadwaj Veeravalli
Managing Hybrid On-chip Scratchpad and Cache Memories for Multi-tasking Embedded Systems
Zimeng Zhou, Lei Ju, Zhiping Jia and Xin Li
Dual-page mode: exploring parallelism in MLC flash SSDs
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Rhymes: A Shared Virtual Memory System for Non-Coherent Tiled Many-Core Architectures
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A Dynamically Adaptive Approach for Speculative Loop Execution in SMT Architectures
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WCT1: FIRST INTERNATIONAL WORKSHOP ON CLOUD TECHNOLOGIES
Performance Characterization and Evaluation of WRF Model on Cloud and HPC Architectures
Krishnan S. P. T., Bharadwaj Veeravalli, Hari Krishna Vethareanun and Chia Sheng Wu
Sunflower: A Multi-objective Optimizer of MapReduce Performance for Users’ Requirements in Cloud Environment
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WCT2:
Selective Task Scheduling for Time-targeted Workflow Execution on Cloud
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Cost-Optimized Resource Provision for Cloud Applications
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Trusted Platforms to secure Mobile Cloud Computing
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Clustering-based Query Result Authenticaiton for Encrypted Databases in Cloud
Miyoung Jang, Min Yoon, Deulnyeok Youn and Jae-Woo Chang
Cloud Brokerage Model for Resource Management
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Analysis and Detection of DoS Attacks in Cloud Computing by Using QSE Algorithm
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Design and implementation of a new Cloudlet Allocation strategy for QoS improvement in Cloud
Sourav Banerjee, Mainak Adhikari and Utpal Biswas
Design and Implementation of a New Load Estimation Strategy in Cloud
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A Density-aware Data Encryption Scheme for Outsourced Databases in Cloud Computing
Min Yoon, Miyoung Jang, Young-Sung Shin and Jae-Woo Chang

WCT5:
Migrating Scientific Workflows to the Cloud
Satish Narayana Sirimala and Jaagup Viil
Towards an Easy-to-Use Web Application Server and Cloud PaaS for Web Development Education
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Universal Design Connector Model for Context-aware Pervasive Computing using Cloud Technologies
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WNet 1:

Two New Multicast Algorithms in 3D Mesh and Torus Networks
Hovhannes Harutyunyan and Shengjiang Wang

Optimizing a calibration software for radio astronomy
Souley Madougou, Ana L. Varbanescu and Rob van Nieuwpoort

Deterministic Blocker Tag Detection Scheme by Comparing Expected and Observed Slot Status in UHF RFID Inventory Management Systems
Ryo Hattori, Kentaroh Toyoda and Iwao Sasase

WNet 2:

S Pramatha, N.K Sakthivel and S Subasree

Improving Vertical Handover over Heterogeneous Technologies Using A Cross Layer Framework
Thaalbi Mariem and Tabbane Nabil

Throughput Enhancement in Cooperative Wireless Ad hoc Networks
Muhammad Khalil Afzal, Byung-Seo Kim and Sung Won Kim

WNet 3:

Active aggregation scheduling using aggregation-degree control in sensor network
Misook Bae and Seong Ro Lee

Bounding Worst-Case Inter-Core Communication Latency for CMPs with 2D-Mesh NoC
Yiqiang Ding and Wei Zhang

Concurrent Moving-based Connection Restoration Scheme between Actors to Ensure the Continuous Connectivity in WSANs
Yuya Tamura, Takuma Koga, Shinichiro Hara, Kentaroh Toyoda and Iwao Sasase

PPCSS: 6th Int. Symposium on Cyberspace Safety and Security Workshop

Privacy Risks in Publication of Taxi GPS Data
Pei Pei Sui, Tianyu Wo, Zhangle Wen and Xianxian Li

Security Evaluation for Cyber Situational Awareness, Igor Kotenko and Elena Doynikova
Predefined Honeypot Context Based Platform Independent Catering Honeypot System, Wenjun Fan and David Fernández

A Novel Revocation Game-Theoretic Model for a Secure Data Storage in Clouds
Maha Jebalia, Asma Ben Letaifa, Mohamed Hamdi and Sami Tabbane

WiP 1: Work-in-Process

Deterministic L2 Cache Design and Its WCET Analysis
Jun Yan and Wei Zhang

Iterative improvement methodology for Hardware/software co-synthesis of embedded systems based on genetic programming
Adam Görski and Maciej Ogorzalek

ROP-EDF: Reservation-Based OP-EDF Scheduling for Automotive Data Stream Management System
Jaeryong Rho, Akihiro Yamauchi, Kenya Sato, Takuya Azumi and Nobuhiko Nishio.

A Load Balancing Real-Time Periodic Task Scheduling Algorithm for Multiprocessor Environment
Divya Bairathi and Sushil Chandra Jain.

WiP 2: Work-in-Process

Interaction between Human and Smart Objects via Twitter by Utilizing the Web-of-Things Concept
Giandomenico Spezzano, Andrea Giordano and Harry Sunarsa.

A Fast Mapping Approach to Address Binary Compatibility for VLIW Machines
Wei Zhang

Static Worst Case Execution Time Analysis of Functional Reactive Systems
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The Registration Desk will be open to assist you at the following times:

- Wednesday, 20 August 2014, 08:00-18:00
- Thursday, 21 August 2014, 08:30-17:00
- Friday, 22 August 2014, 08:00-12:00

Location: MINES ParisTech, 60 boulevard Saint-Michel, F-75272 Paris cedex 06.

Conference materials, name badges, receipt bills will be distributed at the Registration Desk.

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All delegates, sponsors and speakers of HPCC/ICESS/CSS 2014 and associated workshops will be provided with a name badge, to be collected upon registration. This badge must be worn at all times as it is your official pass to all sessions of the conferences, lunches, morning and afternoon teas, and banquets.

Social events

Welcome reception

Gala Dinner: dinner cruise on the Seine, discover the heart of Paris around a delicious traditional meal.

Presentation Instruction

You are required to arrive at the room (in which you will deliver your talk) at least 15 minutes before the commencement of the session. Upon arrival please confirm your attendance with the Session Chair and familiarize yourself with the venue.

Please bring with you a single paragraph summary, including your name (as you would like to be introduced), affiliation and research interests (maximum 100 words). Please present this to the session Session Chair upon arrival, for use for introductory purposes, prior to your talk.

Upon arrival, please copy your slides file to the presentation computer. If you plan to use your own equipment, please ensure it is ready to go prior to the session commencing, since there is very little time between presentations. If you have requested optional equipment,
ensure that is in the room. For all assistance, please speak to the Session Chair.

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Any program changes or urgent announcements from the secretariat and private messages will be posted on the message board in the registration area. Please check the message board occasionally.