

## IMPROVEMENT OF POWER SUPPLY ELECTROMAGNETIC COMPATIBILITY BY EXTENSION OF CHAOS ANTICONTROL

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Switch-mode power supplies usually emit electromagnetic interferences at the switching frequency and its harmonics. Inducing chaos in these systems has recently been suggested as a means of reducing these spectral emissions, yet at the expense of aggravating the overall magnitude of the ripple in the output voltage. We propose here a new nonlinear feedback control method, which induces chaos, and which is able at the same time to achieve low spectral emission and to maintain a small ripple in the output. The feasibility and usefulness of this new and simple method is shown here with a numerical example, which includes a comparison with the previous control method.

*Keywords:* Anticontrol of chaos; Buck converter; power spectrum; output voltage ripple.

### 1. Introduction

DC–DC converters are some of the most widely used circuits in power electronics.<sup>1,2</sup> They are usually used in situations where there is the need of stabilizing a given input DC voltage to a higher (Boost), a lower (Buck) or a generic (Buck–Boost) output voltage.<sup>3</sup> This is generally achieved by chopping and filtering the input voltage through an appropriate switching action, mostly implemented via a pulse width modulation.

Switch-mode power supplies generate electromagnetic interference at the switching frequency and its harmonics. Interference emission create significant electromagnetic compatibility difficulties,<sup>4</sup> especially when high currents or high voltages are switched rapidly. The reduction of power supply emissions can be achieved through various methods that modulate the switching frequency, a technique that reduces spectral emissions.<sup>5,6</sup>

The work of Ref. 4 presents the idea that chaos, a phenomenon which can naturally occur in switch-mode power supplies, might be used to improve their electromagnetic compatibility by reducing spectral peaks. The benefit of this kind of chaotic operation is the reduction of the electromagnetic interference; this advantage

is unfortunately counterbalanced by the increase of the ripple (the peak to peak output voltage).<sup>7,8</sup>

The task of intentionally creating chaos (a transition between order to chaos) — sometimes called chaotification or anticontrol of chaos — has attracted a lot of attention in recent years due to its great potential in nontraditional applications for physical, electrical, optical, mechanical, chemical, biological, and medical systems.<sup>9,10</sup>

The anticontrol reference method designs a simple nonlinear feedback controller with an arbitrarily small amplitude<sup>11–13</sup> obtaining chaotic dynamics in the controlled system. The application of the classical method<sup>11,12</sup> of chaos anticontrol to switch-mode power supplies reduces the output voltage spectral emissions, but aggravates the overall magnitude of the ripple.<sup>14</sup>

The purpose of this paper is to introduce a method of chaos generation in switching systems in general and power electronic circuits in particular to reduce the electromagnetic interferences at the switching frequency and its harmonics.

Therefore, we use here a new nonlinear feedback controller and show that it maintains a small ripple in the output and is able at the same time to achieve a low spectral emission. Finally, the combination of this feedback controller with the standard anticontrol method reduces even more the power contained in the spectral peaks, without deteriorating the output voltage ripple. Indeed, applying chaos anticontrol to switch-mode power supplies improves the performance both in the frequency-domain (spectrum) and in the time-domain (ripple). The feasibility and usefulness of this new and simple method is shown here with a numerical example, which includes a comparison with the classical anticontrol method.

## 2. The Buck Converter

Figure 1 shows the diagram of a Buck converter that uses a pulse width modulated voltage loop.<sup>15</sup> The circuit has two states determined by the position of the switch  $S$ . When  $S$  is closed, the input voltage  $E$  provides energy to the load  $R$  as well as to the inductor  $L$ . When  $S$  is open, the inductor current  $i$ , which flows through diode  $D$ , transfers some of its stored energy to the load  $R$ . The amplifier  $A_2$  has a gain  $a$ . The simplest feedback converter is obtained when the switch  $P$  in the zero position. The control law  $v_c(t)$  can then be written as

$$v_c(t) = v_{c0}(t) = u_1(t) = a(v(t) - V_{\text{ref}}). \quad (1)$$

The control law  $v_c(t)$  is applied to the inverting input of the comparator  $A_1$ . The noninverting input is connected to an independent voltage ramp generator: its output voltage periodically rises from a low voltage  $V_L$  to an upper voltage  $V_U$  in a time  $T$ , and then instantaneously returns to  $V_L$ . This ramp voltage  $v_r(t)$  can be expressed as

$$v_r(t) = V_L + (V_U - V_L) \frac{t \bmod T}{T}. \quad (2)$$



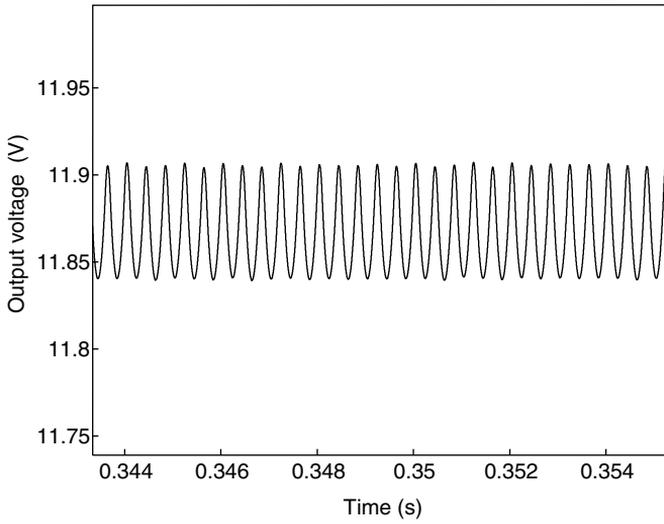


Fig. 2. The periodic output voltage  $v(t)$  of the Buck converter with the control law  $v_{c0}(t)$  of Eq. (1) (70 mV ripple).

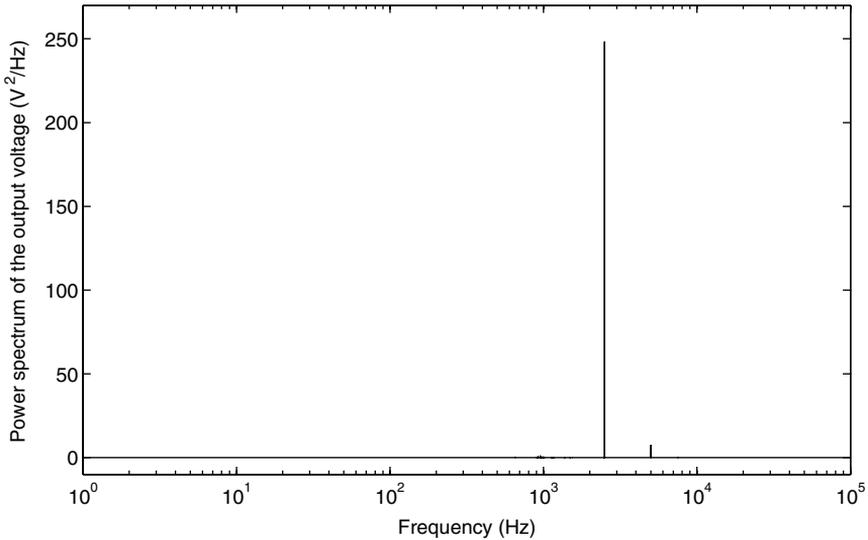


Fig. 3. Power spectrum of the output voltage  $v(t)$  with the control law  $v_{c0}(t)$  of Eq. (1).

### 3. Generating Chaos

Purposely chaotifying a stable nonlinear system can be achieved by using the state feedback control with a small magnitude.<sup>11–13</sup> The feedback control in Refs. 11 and 12 is a simple sinusoidal function of the system state. The new control law that

we propose here has the expression

$$v_c(t) = v_{c1}(t) = u_1(t) + u_2(t), \quad (5)$$

where  $u_1(t)$  is defined in Eq. (1) and  $u_2(t)$  is based on Ref. 11

$$u_2(t) = c_2 \sin[\omega_2(v(t) - V_{\text{ref}})]. \quad (6)$$

The anticontrol method of Ref. 11 considers the use of an additional nonlinear feedback controller *NC1*, i.e., with switch *P* of Fig. 1 in position 1. The application of this method to switch-mode power supplies leads to a chaotic output voltage  $v(t)$ .

Varying the values of the parameters  $c_2$  and  $\omega_2$ , we have observed that the optimum values of the ripple and of the maximum power spectrum magnitude are 200 mV and 50 V<sup>2</sup>/Hz respectively.<sup>14</sup>

Even though many other spectral lines appear, the frequency-domain (spectral) performance is improved. References 7 and 8 also lead to the conclusion: unfortunately, the time-domain (ripple) performance is worsened because the output voltage ripple has increased, thus reducing the efficiency of the DC–DC converter.

One can wonder why we need to apply a method to generate chaos, when chaos is a phenomenon which can occur naturally in switch-mode power supplies.<sup>15</sup> Indeed, these circuits exhibit chaos as parameters (like the input voltage  $E$ , the load resistance  $R$ , the inductance  $L$  or the ramp period  $T$ ) vary. The studied converter presents a chaotic behavior depending on the input voltage  $E$  (when  $E \geq 32.34$  V).<sup>2,15,16</sup> Figure 4 shows that the ripple is increased again ( $\approx 1$  V) and therefore, another solution has to be found.

#### 4. Reducing the Output Voltage Ripple

We introduce a new nonlinear controller *NC2*, aiming at improving the power spectrum and ripple obtained with the previous control laws  $v_{c0}(t)$  and  $v_{c1}(t)$ . We propose the following control law:

$$v_c(t) = v_{c2}(t) = u_1(t) + u_3(t), \quad (7)$$

where

$$u_3(t) = c_3 v(t) \sin(\omega_3 t) = c_3 v(t) \sin(2\pi f_3 t). \quad (8)$$

This time, the expression (8) includes a multiplication of the state feedback with a sine. The amplitude and the frequency of the resulting signal  $u_3(t)$  determine the number of times  $S$  switches. If  $f_3$  is much greater than the frequency ( $1/T$ ) of the ramp generator, then  $S$  switches many times during one period of the ramp, as shown in Fig. 5. In this case, the output voltage  $v(t)$  does not have enough time to rise or to decrease too much, because of these multiple switches. Furthermore, the output voltage  $v(t)$  is not sensitive to the variation of the initial conditions: the introduction of the nonlinear controller *NC2* determines a nonchaotic behavior of the converter.

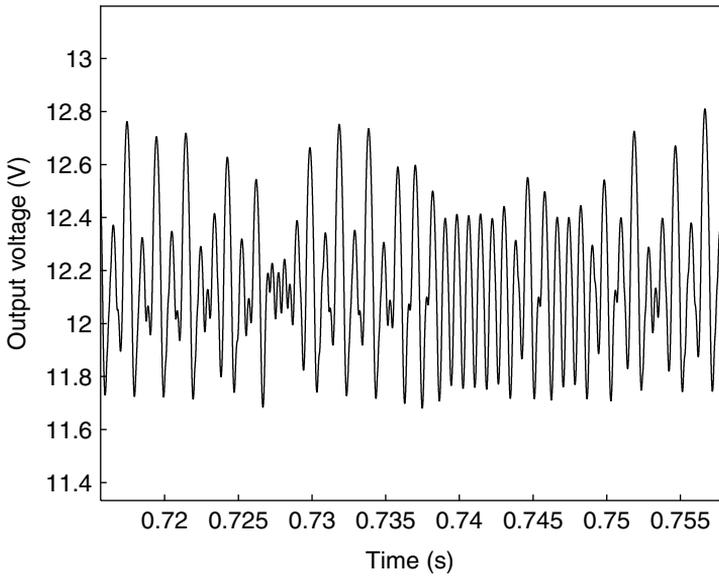


Fig. 4. The output voltage  $v(t)$  of the Buck converter with the control law  $v_{c0}(t)$  of Eq. (1): chaotic behavior (1 V ripple).

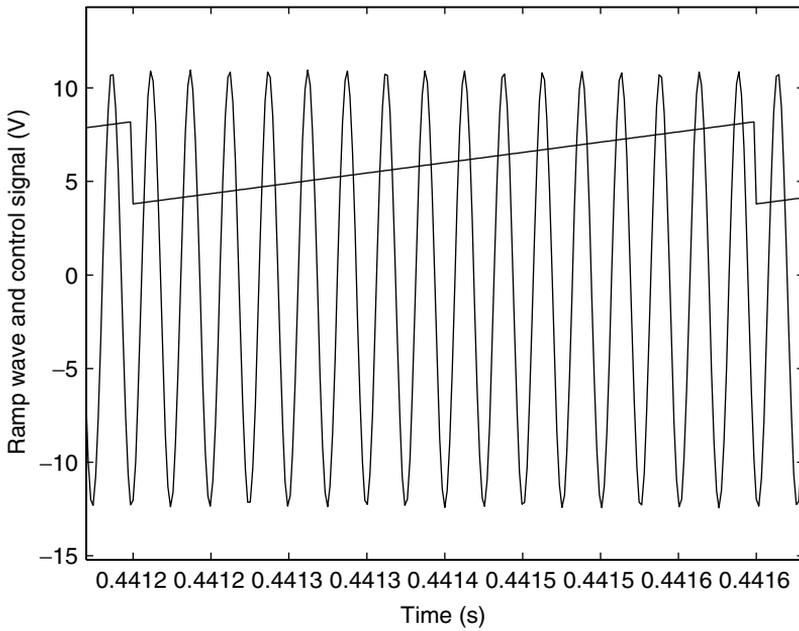


Fig. 5. Ramp wave  $v_r(t)$  of Eq. (2) and control signal  $v_{c2}(t)$  of Eq. (7).

Let us determine the angular frequency  $\omega_3$  and the amplitude  $c_3$ , in order to find the best possible power spectrum and ripple.  $\omega_3$  must be much greater than the angular frequency ( $2\pi/T$ ) of the ramp generator, whereas the switching frequency of  $S$  is limited to [80 kHz, 100 kHz] in practice. Therefore,

$$\frac{1}{T} \ll f_3 = \frac{\omega_3}{2\pi} < 80 \text{ kHz}. \tag{9}$$

Figure 6 shows that the ripple decreases rapidly for low values of  $\omega_3$  and then stabilizes for higher values of  $\omega_3$ . Therefore, any  $\omega_3$  in the domain [200 000 rad/s, 500 000 rad/s] is appropriate. This corresponds to the interval [32 kHz, 80 kHz] for the frequency  $f_3$  of  $u_3(t)$ . We chose  $\omega_3 = 250\,000$  rad/s ( $f_3 \approx 40$  kHz).

The choice of the coefficient  $c_3$  must be done to minimize the amplitude of the power spectrum. Figure 7 shows the power spectrum (obtained as the Fourier transform of the autocorrelation) of the output voltage  $v(t)$ , in function of  $c_3$ . The power contained in the peak of the switching frequency  $1/T$  decreases when  $c_3$  increases, whereas lower frequencies give high amplitudes of the power spectrum. The minimum value of the power spectrum amplitude is obtained for  $c_3 = 1.04$ : let us call it  $\min(\max\text{FFT})$ . It represents the minimum of all the maxima of the power spectrum when  $c_3$  varies.

Figure 8 presents the power spectrum of the output voltage  $v(t)$ , for  $c_3 = 1.04$  and  $\omega_3 = 250\,000$  rad/s:  $\min(\max\text{FFT})$  is equal to  $3.77 \text{ V}^2/\text{Hz}$ . We can observe that

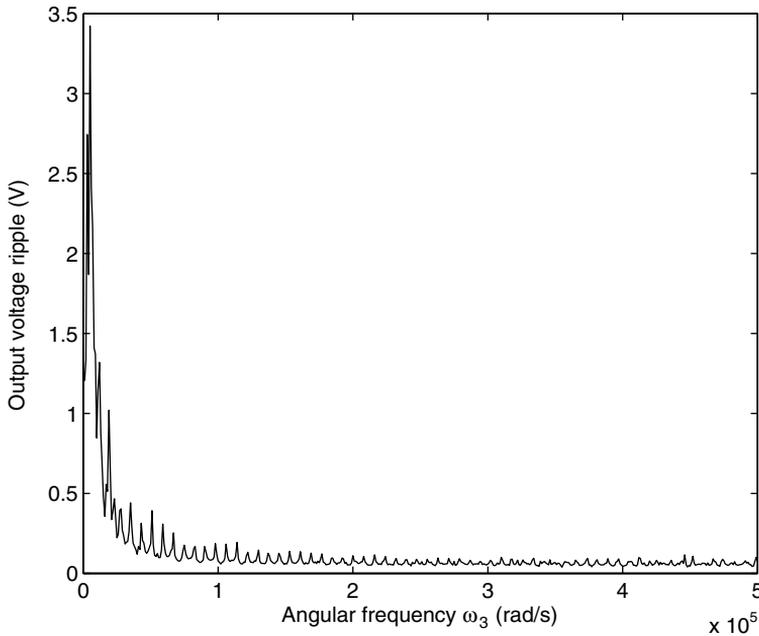


Fig. 6. Ripple of the output voltage  $v(t)$  in function of  $\omega_3$  (control law  $v_{c2}(t)$  of Eq. (7)).

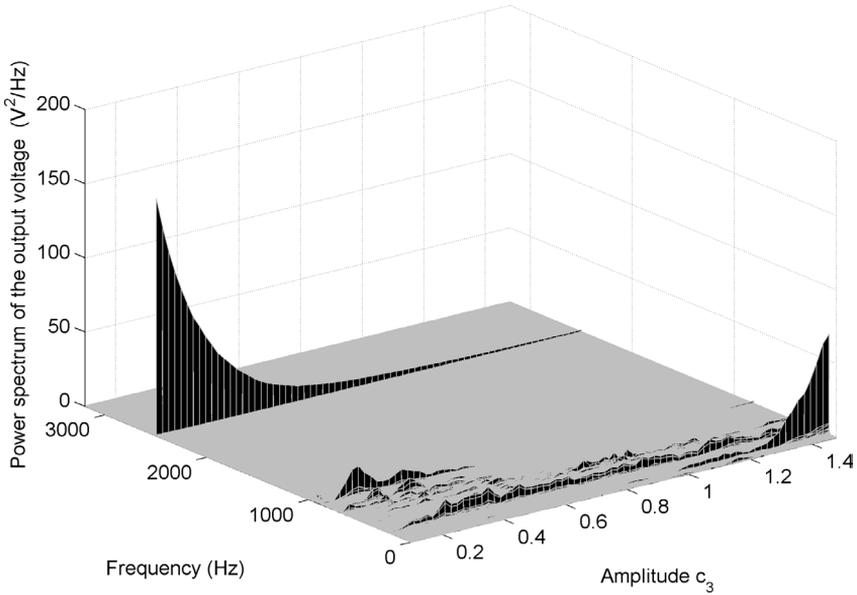


Fig. 7. Power spectrum of the output voltage  $v(t)$  with the control law  $v_{c2}(t)$  of Eq. (7) ( $c_3$  as parameter).

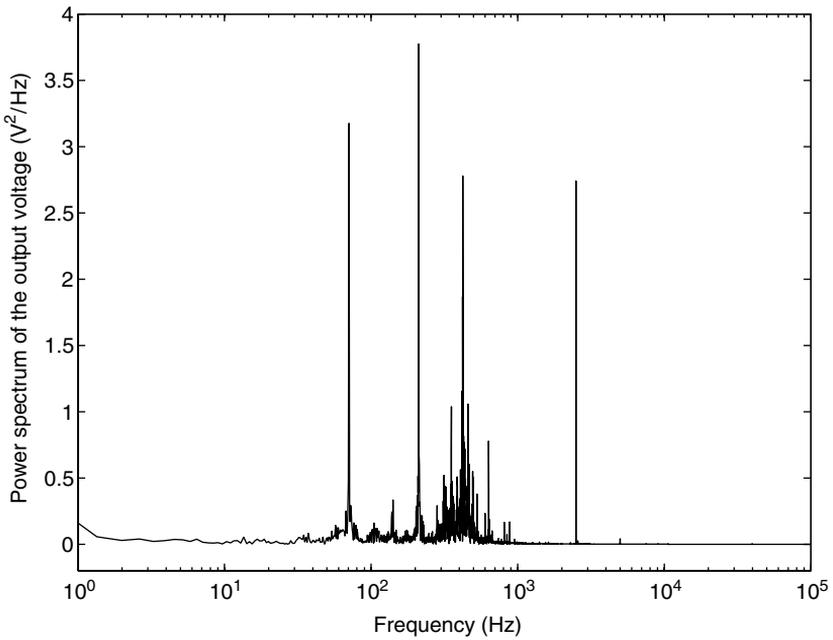


Fig. 8. Power spectrum of the output voltage  $v(t)$  obtained for  $c_3 = 1.04$  with the control law  $v_{c2}(t)$  of Eq. (7) ( $\min(\max\text{FFT}) = 3.77 \text{ V}^2/\text{Hz}$ ).

this spectrum is no more composed of a unique peak at the switching frequency  $1/T$  (or at its harmonics): many spectral lines appear at lower frequencies, therefore widening the band. Figure 9 finally presents the temporal representation of the output voltage  $v(t)$  and shows that the ripple is 55 mV.

The various frequencies in the output voltage  $v(t)$  arise from the very irregular form of  $v(t)$  due to the intrinsic nonlinear dynamics driven by the *on* and *off* duration of the switch  $S$  which varies during a period of the ramp, as shown in Fig. 5. On Fig. 9, a sequence is pointed out (inside the interval 1.435s–1.44s) where the temporal representation of the output voltage  $v(t)$  can be approximated by a sine. The period of 2.5 ms gives a 400 Hz peak on the power spectrum of  $v(t)$ . In the same way, the concentration of all the frequencies around 70 Hz–1 kHz can be justified.

The  $u_3(t)$  proposed here is introduced to maintain a small ripple of the output. Indeed, the two control laws  $v_{c0}(t)$  and  $v_{c2}(t)$  almost lead to the same ripple amplitude (70 mV, respectively 55 mV).  $u_3(t)$ , as system feedback, is able at the same time to reduce the power contained in the peaks of the switching harmonics of the DC–DC converter. We can say that  $v_{c2}(t)$  has better performances than  $v_{c0}(t)$ .

The new (nonchaotical) nonlinear feedback controller  $NC2$  improves the frequency-domain (spectrum) performance, without deteriorating the time-domain (ripple) performance.

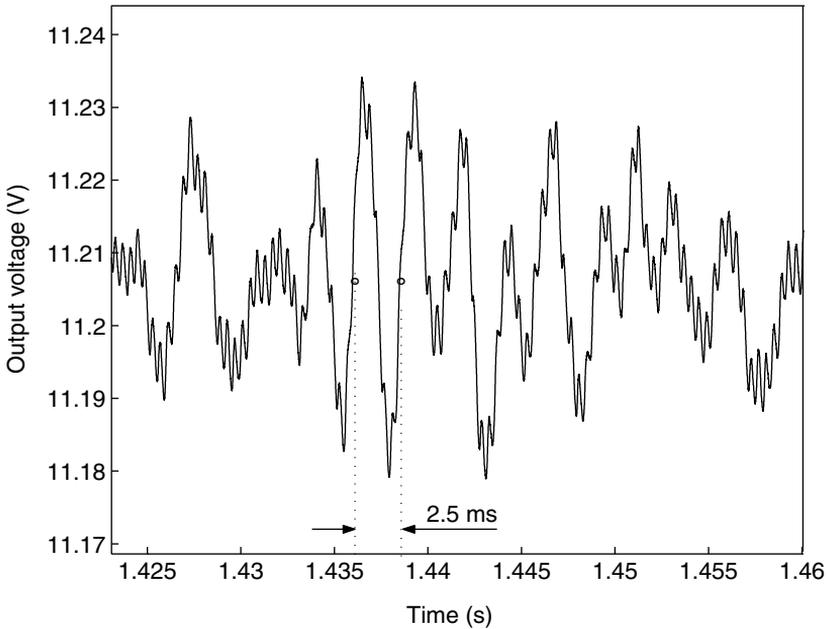


Fig. 9. Output voltage  $v(t)$  obtained for  $c_3 = 1.04$  with the control law  $v_{c2}(t)$  of Eq. (7) (55 mV ripple).

### 5. Generating Chaos and Reducing the Output Voltage Ripple

With the controller *NC2*, the Buck converter shows overall good performance but we want to investigate whether the introduction of chaos can improve the electromagnetic compatibility by further reducing spectral peaks. Therefore, we add the nonlinear controller *NC1*. The control law becomes

$$v_c(t) = v_{c3}(t) = u_1(t) + u_2(t) + u_3(t). \tag{10}$$

The two controllers *NC1* and *NC2* use the same state variable (the output voltage  $v(t)$ ) to provide the control laws  $u_2(t)$  and  $u_3(t)$ . The parameters of  $u_3(t)$  are chosen as in Sec. 4. The choice of parameters  $c_2$  and  $\omega_2$  of  $u_2(t)$  is done to minimize the power spectrum amplitude.

Figure 10 presents the maximum of the power spectrum amplitude (maxFFT) of the output voltage  $v(t)$  for  $0 < c_2 < 1.4$  V and  $50 \text{ rad/V} < \omega_2 < 550 \text{ rad/V}$ . Every maxFFT lower than min(maxFFT) obtained in the previous section ( $3.77 \text{ V}^2/\text{Hz}$ ) is represented with a black star (\*). The resulting black area is associated with  $\omega_2$  and  $c_2$  values for which the use of  $v_{c3}(t)$  is an improvement. The minimum value of all maxFFT is obtained for  $c_2 = 0.1$  V and  $\omega_2 = 320 \text{ rad/V}$ :  $\text{min}(\text{maxFFT}) = 2.69 \text{ V}^2/\text{Hz}$ . Using these values, we represent the power spectrum of the output voltage  $v(t)$  and its time waveform in Figs. 11 and 12, respectively. These figures show that the output voltage ripple remains almost constant (55 mV) when the control law  $v_{c2}(t)$  is replaced by  $v_{c3}(t)$  as feedback of the Buck converter. Therefore,

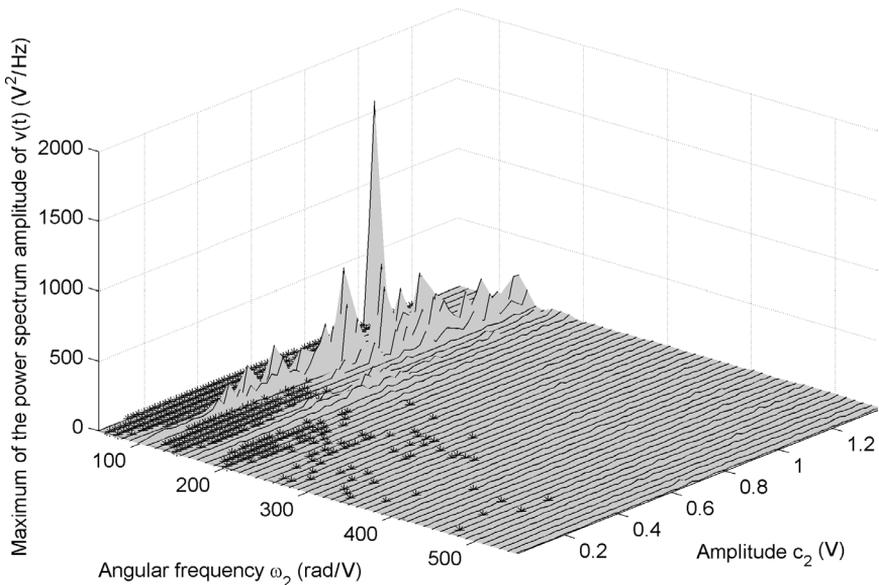


Fig. 10. The maximum of the power spectrum of the output voltage  $v(t)$  with the control law  $v_{c3}(t)$  of Eq. (10) ( $c_2$  and  $\omega_2$  as parameters).

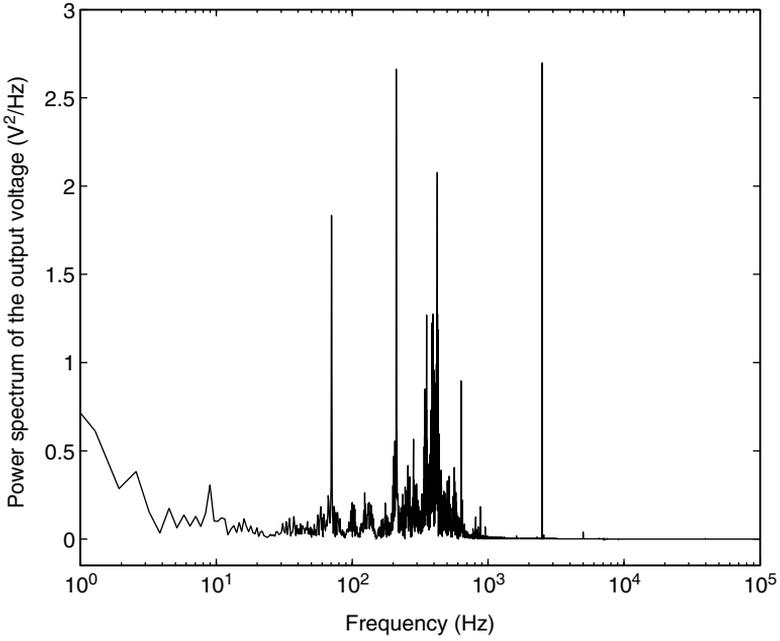


Fig. 11. Power spectrum of the output voltage  $v(t)$  obtained with the control law  $v_{c3}(t)$  of Eq. (10), and with the parameters  $c_2 = 0.1 \text{ V}$  and  $\omega_2 = 320 \text{ rad/V}$  ( $\min(\max\text{FFT}) = 2.69 \text{ V}^2/\text{Hz}$ ).

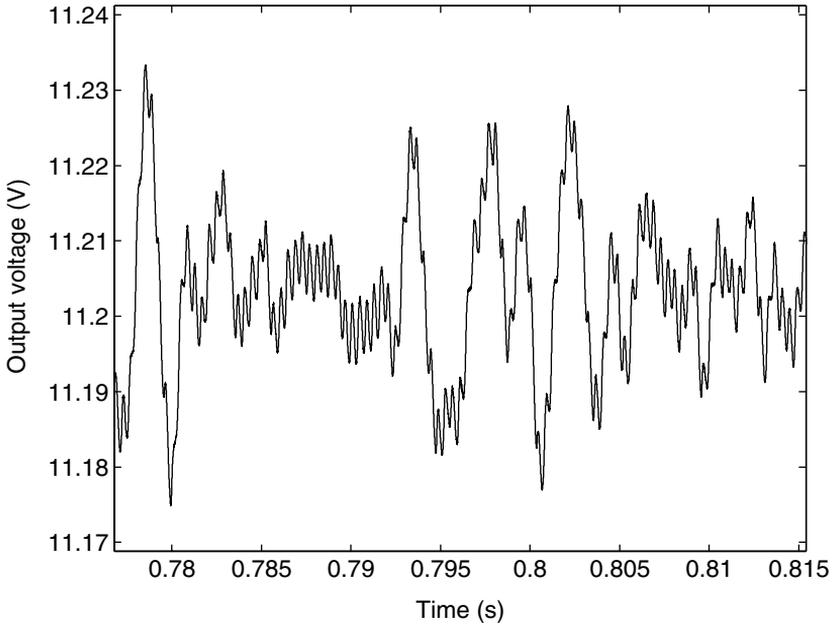


Fig. 12. Output voltage  $v(t)$  obtained with the control law  $v_{c3}(t)$  of Eq. (10), and the parameters  $c_2 = 0.1 \text{ V}$  and  $\omega_2 = 320 \text{ rad/V}$  (55 mV ripple).

Table 1. The performances of the converter with the control laws ( $v_{c0}, v_{c1}, v_{c2}, v_{c3}$ ).

Control law	Maximum of power spectrum ( $V^2/\text{Hz}$ )	Ripple (mV)	Chaos	Wide band
$v_{c0}$	250	70	No	No
$v_{c1}$	50	200	Yes	Yes
$v_{c2}$	3.77	55	No	Yes
$v_{c3}$	2.69	55	Yes	Yes

the influence of  $u_2(t)$  on the ripple is negligible. On the contrary,  $u_2(t)$  decreases further the amplitude of the spectrum (from  $3.77 V^2/\text{Hz}$  to  $2.69 V^2/\text{Hz}$ ):  $v_{c3}(t)$  has a better frequency-domain (spectral) performance than  $v_{c2}(t)$ .

Table 1 summarizes the ripple and the power spectrum of the output voltage  $v(t)$ , using the four control laws ( $v_{c0}, v_{c1}, v_{c2}, v_{c3}$ ) as feedback. It shows that the power spectrum amplitude of the converter governed by the first control law  $v_{c0}(t)$  is reduced while the output voltage ripple is kept constant. The second control law  $v_{c1}(t)$  of Eq. (1) reduces the power contained in the peaks harmonics of the output voltage (down to five times), but leads to a large ripple. The control law  $v_{c2}(t)$  of Eq. (7) we propose ensures a good ripple and causes a spectacular decrease of the power spectrum amplitude. Finally, chaotifying the converter with the last control law  $v_{c3}(t)$  of Eq. (10) leads to a higher power spectrum amplitude decrease.

Of course, the reduction of the maximum of the power spectrum (Fig. (3)) of  $v(t)$  can be achieved by increasing the basic switching frequency  $1/T$  of the converter governed by the control law  $v_{c0}(t)$ . Then, the control law  $v_{c0}(t)$  gives similar results as  $v_{c3}(t)$  if the generator ramp frequency of the converter is  $f_3$ . This classical method has few properties; this justifies the use of chaos.

Chaos is a very interesting nonlinear phenomenon, which has been intensively studied during the three last decades. Chaotification is theoretically attractive and yet technically very challenging, since it involves generating abundant and complex dynamical behaviors. The understanding of chaotic behavior opens up new possibilities of operating regimes that can help to optimize design. Chaos provides a system designer with a variety of special properties, richness of flexibility and a multitude of opportunities.

The target of the paper is to show that chaos reduces the maximum of power spectrum. But chaos, either occurring naturally or purposely created, increases the output voltage ripple. This is why we introduce chaos in the converter only after an increase of the switching frequency (with the control law  $v_{c3}(t)$ ), which reduces the output voltage ripple.

## 6. Conclusion

The purpose of this paper is to introduce an original method of chaos generation in switching systems in general and power electronic circuits in particular.

Indeed, applying chaos anticontrol (i.e., with a nonlinear feedback controller  $NC1$ ) to switch-mode power supplies improves the frequency-domain (spectrum) performance while the time-domain (ripple) performance degrades. Our method consists in designing a new nonlinear controller ( $NC2$ ) which maintains a small ripple in the output and which is able at the same time to achieve low spectral emission. The parameters of this controller are chosen in order to have a power spectrum improvement of the output voltage. Finally, the combination of our method ( $NC2$ ) with the standard anticontrol method ( $NC1$ ) reduces even more the power contained in the spectral lines, without deteriorating the output voltage ripple. We are currently working on a closed-form iterative-map, which describes the behavior of the controlled Buck converter. This map is useful to reduce the computing time for the numerical simulation and represents an approach to the theoretical analysis.

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