

The Properties of Instructions of SCM over Ring

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The articles [22], [29], [12], [23], [19], [30], [8], [9], [7], [2], [3], [24], [1], [27], [10], [5], [11], [20], [4], [6], [16], [28], [17], [18], [25], [21], [14], [13], [26], and [15] provide the notation and terminology for this paper.

For simplicity, we follow the rules: R is a good ring, r is an element of R , a, b are Data-Locations of R , i_1, i_2, i_3 are instruction-locations of $\mathbf{SCM}(R)$, I is an instruction of $\mathbf{SCM}(R)$, s_1, s_2 are states of $\mathbf{SCM}(R)$, T is an instruction type of $\mathbf{SCM}(R)$, and k is a natural number.

Let us mention that \mathbb{Z} is infinite.

Let us mention that $\mathbf{INT.Ring}$ is infinite and good.

Let us note that there exists a 1-sorted structure which is strict and infinite.

Let us observe that there exists a ring which is strict, infinite, and good.

Next we state the proposition

- (1) $\mathbf{ObjectKind}(a)$ = the carrier of R .

Let R be a good ring, let l_1, l_2 be Data-Locations of R , and let a, b be elements of R . Then $[l_1 \mapsto a, l_2 \mapsto b]$ is a finite partial state of $\mathbf{SCM}(R)$.

We now state a number of propositions:

- (2) $a \notin$ the instruction locations of $\mathbf{SCM}(R)$.
- (3) $a \neq \mathbf{IC}_{\mathbf{SCM}(R)}$.
- (4) $\mathbf{Data-Loc}_{\mathbf{SCM}} \neq$ the instruction locations of $\mathbf{SCM}(R)$.
- (5) For every object o of $\mathbf{SCM}(R)$ holds $o = \mathbf{IC}_{\mathbf{SCM}(R)}$ or $o \in$ the instruction locations of $\mathbf{SCM}(R)$ or o is a Data-Location of R .
- (6) If $i_2 \neq i_3$, then $\mathbf{Next}(i_2) \neq \mathbf{Next}(i_3)$.
- (7) If s_1 and s_2 are equal outside the instruction locations of $\mathbf{SCM}(R)$, then $s_1(a) = s_2(a)$.
- (8) $\mathbf{InsCode}(\mathbf{halt}_{\mathbf{SCM}(R)}) = 0$.
- (9) $\mathbf{InsCode}(a:=b) = 1$.
- (10) $\mathbf{InsCode}(\mathbf{AddTo}(a, b)) = 2$.
- (11) $\mathbf{InsCode}(\mathbf{SubFrom}(a, b)) = 3$.
- (12) $\mathbf{InsCode}(\mathbf{MultBy}(a, b)) = 4$.
- (13) $\mathbf{InsCode}(a:=r) = 5$.

- (14) $\text{InsCode}(\text{goto } i_2) = 6.$
- (15) $\text{InsCode}(\text{if } a = 0 \text{ goto } i_2) = 7.$
- (16) If $\text{InsCode}(I) = 0$, then $I = \mathbf{halt}_{\text{SCM}(R)}.$
- (17) If $\text{InsCode}(I) = 1$, then there exist a, b such that $I = a := b.$
- (18) If $\text{InsCode}(I) = 2$, then there exist a, b such that $I = \text{AddTo}(a, b).$
- (19) If $\text{InsCode}(I) = 3$, then there exist a, b such that $I = \text{SubFrom}(a, b).$
- (20) If $\text{InsCode}(I) = 4$, then there exist a, b such that $I = \text{MultBy}(a, b).$
- (21) If $\text{InsCode}(I) = 5$, then there exist a, r such that $I = a := r.$
- (22) If $\text{InsCode}(I) = 6$, then there exists i_3 such that $I = \text{goto } i_3.$
- (23) If $\text{InsCode}(I) = 7$, then there exist a, i_2 such that $I = \text{if } a = 0 \text{ goto } i_2.$
- (24) $\text{AddressPart}(\mathbf{halt}_{\text{SCM}(R)}) = \emptyset.$
- (25) $\text{AddressPart}(a := b) = \langle a, b \rangle.$
- (26) $\text{AddressPart}(\text{AddTo}(a, b)) = \langle a, b \rangle.$
- (27) $\text{AddressPart}(\text{SubFrom}(a, b)) = \langle a, b \rangle.$
- (28) $\text{AddressPart}(\text{MultBy}(a, b)) = \langle a, b \rangle.$
- (29) $\text{AddressPart}(a := r) = \langle a, r \rangle.$
- (30) $\text{AddressPart}(\text{goto } i_2) = \langle i_2 \rangle.$
- (31) $\text{AddressPart}(\text{if } a = 0 \text{ goto } i_2) = \langle i_2, a \rangle.$
- (32) If $T = 0$, then $\text{AddressParts } T = \{0\}.$

Let us consider R, T . One can check that $\text{AddressParts } T$ is non empty.

The following propositions are true:

- (33) If $T = 1$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (34) If $T = 2$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (35) If $T = 3$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (36) If $T = 4$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (37) If $T = 5$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (38) If $T = 6$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1\}.$
- (39) If $T = 7$, then $\text{dom } \prod_{\text{AddressParts } T} = \{1, 2\}.$
- (40) $\prod_{\text{AddressParts } \text{InsCode}(a := b)}(1) = \text{Data-Loc}_{\text{SCM}}.$
- (41) $\prod_{\text{AddressParts } \text{InsCode}(a := b)}(2) = \text{Data-Loc}_{\text{SCM}}.$
- (42) $\prod_{\text{AddressParts } \text{InsCode}(\text{AddTo}(a, b))}(1) = \text{Data-Loc}_{\text{SCM}}.$
- (43) $\prod_{\text{AddressParts } \text{InsCode}(\text{AddTo}(a, b))}(2) = \text{Data-Loc}_{\text{SCM}}.$
- (44) $\prod_{\text{AddressParts } \text{InsCode}(\text{SubFrom}(a, b))}(1) = \text{Data-Loc}_{\text{SCM}}.$
- (45) $\prod_{\text{AddressParts } \text{InsCode}(\text{SubFrom}(a, b))}(2) = \text{Data-Loc}_{\text{SCM}}.$

- (46) $\prod_{\text{AddressParts}} \text{InsCode}(\text{MultBy}(a,b))(1) = \text{Data-Loc}_{\text{SCM}}$.
- (47) $\prod_{\text{AddressParts}} \text{InsCode}(\text{MultBy}(a,b))(2) = \text{Data-Loc}_{\text{SCM}}$.
- (48) $\prod_{\text{AddressParts}} \text{InsCode}(a:=r)(1) = \text{Data-Loc}_{\text{SCM}}$.
- (49) $\prod_{\text{AddressParts}} \text{InsCode}(a:=r)(2) = \text{the carrier of } R$.
- (50) $\prod_{\text{AddressParts}} \text{InsCode}(\text{goto } i_2)(1) = \text{the instruction locations of } \mathbf{SCM}(R)$.
- (51) $\prod_{\text{AddressParts}} \text{InsCode}(\mathbf{if } a=0 \mathbf{ goto } i_2)(1) = \text{the instruction locations of } \mathbf{SCM}(R)$.
- (52) $\prod_{\text{AddressParts}} \text{InsCode}(\mathbf{if } a=0 \mathbf{ goto } i_2)(2) = \text{Data-Loc}_{\text{SCM}}$.
- (53) $\text{NIC}(\mathbf{halt}_{\text{SCM}(R)}, i_1) = \{i_1\}$.

Let us consider R . Note that $\text{JUMP}(\mathbf{halt}_{\text{SCM}(R)})$ is empty.
Next we state the proposition

- (54) $\text{NIC}(a:=b, i_1) = \{\text{Next}(i_1)\}$.

Let us consider R, a, b . Note that $\text{JUMP}(a:=b)$ is empty.
Next we state the proposition

- (55) $\text{NIC}(\text{AddTo}(a,b), i_1) = \{\text{Next}(i_1)\}$.

Let us consider R, a, b . Observe that $\text{JUMP}(\text{AddTo}(a,b))$ is empty.
Next we state the proposition

- (56) $\text{NIC}(\text{SubFrom}(a,b), i_1) = \{\text{Next}(i_1)\}$.

Let us consider R, a, b . Note that $\text{JUMP}(\text{SubFrom}(a,b))$ is empty.
The following proposition is true

- (57) $\text{NIC}(\text{MultBy}(a,b), i_1) = \{\text{Next}(i_1)\}$.

Let us consider R, a, b . Note that $\text{JUMP}(\text{MultBy}(a,b))$ is empty.
We now state the proposition

- (58) $\text{NIC}(a:=r, i_1) = \{\text{Next}(i_1)\}$.

Let us consider R, a, r . One can verify that $\text{JUMP}(a:=r)$ is empty.
Next we state two propositions:

- (59) $\text{NIC}(\text{goto } i_2, i_1) = \{i_2\}$.

- (60) $\text{JUMP}(\text{goto } i_2) = \{i_2\}$.

Let us consider R, i_2 . Observe that $\text{JUMP}(\text{goto } i_2)$ is non empty and trivial.
Next we state three propositions:

- (61) $i_2 \in \text{NIC}(\mathbf{if } a = 0 \mathbf{ goto } i_2, i_1)$ and $\text{NIC}(\mathbf{if } a = 0 \mathbf{ goto } i_2, i_1) \subseteq \{i_2, \text{Next}(i_1)\}$.

- (62) Let R be a non trivial good ring, a be a Data-Location of R , and i_1, i_2 be instruction-locations of $\mathbf{SCM}(R)$. Then $\text{NIC}(\mathbf{if } a = 0 \mathbf{ goto } i_2, i_1) = \{i_2, \text{Next}(i_1)\}$.

- (63) $\text{JUMP}(\mathbf{if } a = 0 \mathbf{ goto } i_2) = \{i_2\}$.

Let us consider R, a, i_2 . One can check that $\text{JUMP}(\mathbf{if } a = 0 \mathbf{ goto } i_2)$ is non empty and trivial.
We now state two propositions:

- (64) $\text{SUCC}(i_1) = \{i_1, \text{Next}(i_1)\}$.

(65) Let f be a function from \mathbb{N} into the instruction locations of $\mathbf{SCM}(R)$. Suppose that for every natural number k holds $f(k) = \mathbf{i}_k$. Then

- (i) f is bijective, and
- (ii) for every natural number k holds $f(k+1) \in \text{SUCC}(f(k))$ and for every natural number j such that $f(j) \in \text{SUCC}(f(k))$ holds $k \leq j$.

Let us consider R . One can check that $\mathbf{SCM}(R)$ is standard.

Next we state three propositions:

- (66) $\text{il}_{\mathbf{SCM}(R)}(k) = \mathbf{i}_k$.
- (67) $\text{Next}(\text{il}_{\mathbf{SCM}(R)}(k)) = \text{il}_{\mathbf{SCM}(R)}(k+1)$.
- (68) $\text{Next}(i_1) = \text{NextLoc } i_1$.

Let R be a good ring and let k be a natural number. The functor $\text{dl}_R(k)$ yields a Data-Location of R and is defined by:

(Def. 1) $\text{dl}_R(k) = \mathbf{d}_k$.

Let us consider R . Observe that $\text{InsCode}(\mathbf{halt}_{\mathbf{SCM}(R)})$ is jump-only.

Let us consider R . Observe that $\mathbf{halt}_{\mathbf{SCM}(R)}$ is jump-only.

Let us consider R, i_2 . Observe that $\text{InsCode}(\text{goto } i_2)$ is jump-only.

Let us consider R, i_2 . Observe that $\text{goto } i_2$ is jump-only.

Let us consider R, a, i_2 . Note that $\text{InsCode}(\mathbf{if } a = 0 \mathbf{goto } i_2)$ is jump-only.

Let us consider R, a, i_2 . Note that $\mathbf{if } a = 0 \mathbf{goto } i_2$ is jump-only.

In the sequel S denotes a non trivial good ring, p, q denote Data-Locations of S , and w denotes an element of S .

Let us consider S, p, q . One can verify that $\text{InsCode}(p:=q)$ is non jump-only.

Let us consider S, p, q . Observe that $p:=q$ is non jump-only.

Let us consider S, p, q . One can verify that $\text{InsCode}(\text{AddTo}(p, q))$ is non jump-only.

Let us consider S, p, q . One can check that $\text{AddTo}(p, q)$ is non jump-only.

Let us consider S, p, q . Note that $\text{InsCode}(\text{SubFrom}(p, q))$ is non jump-only.

Let us consider S, p, q . Note that $\text{SubFrom}(p, q)$ is non jump-only.

Let us consider S, p, q . Observe that $\text{InsCode}(\text{MultBy}(p, q))$ is non jump-only.

Let us consider S, p, q . Note that $\text{MultBy}(p, q)$ is non jump-only.

Let us consider S, p, w . One can verify that $\text{InsCode}(p:=w)$ is non jump-only.

Let us consider S, p, w . One can check that $p:=w$ is non jump-only.

Let us consider R, a, b . Note that $a:=b$ is sequential.

Let us consider R, a, b . Note that $\text{AddTo}(a, b)$ is sequential.

Let us consider R, a, b . One can verify that $\text{SubFrom}(a, b)$ is sequential.

Let us consider R, a, b . One can check that $\text{MultBy}(a, b)$ is sequential.

Let us consider R, a, r . Note that $a:=r$ is sequential.

Let us consider R, i_2 . Note that $\text{goto } i_2$ is non sequential.

Let us consider R, a, i_2 . One can check that $\mathbf{if } a = 0 \mathbf{goto } i_2$ is non sequential.

Let us consider R, i_2 . One can verify that $\text{goto } i_2$ is non instruction location free.

Let us consider R, a, i_2 . One can verify that $\mathbf{if } a = 0 \mathbf{goto } i_2$ is non instruction location free.

Let us consider R . Note that $\mathbf{SCM}(R)$ is homogeneous and has explicit jumps and no implicit jumps.

Let us consider R . One can check that $\mathbf{SCM}(R)$ is regular.

The following two propositions are true:

- (69) $\text{IncAddr}(\text{goto } i_2, k) = \text{goto } \text{il}_{\mathbf{SCM}(R)}(\text{locnum}(i_2) + k)$.
- (70) $\text{IncAddr}(\mathbf{if } a = 0 \mathbf{goto } i_2, k) = \mathbf{if } a = 0 \mathbf{goto } \text{il}_{\mathbf{SCM}(R)}(\text{locnum}(i_2) + k)$.

Let us consider R . Note that $\mathbf{SCM}(R)$ is IC-good and Exec-preserving.

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