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### Deep-dive Analysis of the Data Analytics Workload in CloudSuite

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## Preface

- ISCA 2013 Analysis Methodologies Tutorial
  - https://sites.google.com/site/analysismethods/isca2013/program-1
- A workload: CloudSuite [1]
  - Scale-out apps: Data Serving, **Data Analytics**, Media Streaming, Web etc
  - Different Characteristics:
    - Higher i-cache misses
    - Lower ILP and MLP
    - Bigger working sets
    - Low Memory BW and sharing
  - No root-cause
- A tool: Top Down Analysis [2]
  - A structured, accurate and fast method for critical bottleneck identification in out-of-order cores

[1] M. Ferdman, et al. "Clearing the clouds: a study of emerging scale-out workloads on modern hardware," ASPLOS 2012.

[2] A. Yasin, "A Top-Down Method for Performance Analysis and Counters Architecture," ISPASS 2014



## Motivation

- Exponential data growth
- Massively-parallel hardware systems
- Orchestration software layers
  - Hadoop, Spark
- New scale-out applications
  - Store and process big data
  - Different Characteristics
- No understanding of the root causes
- Data Analytics (key for big data  $\rightarrow$  value)

Small improvement at a compute engine → large impact on datacenter



## Scope

- Data Analytics (aka BDA)
  - Of-the-shelf setup from CloudSuite 2.0
  - Utilizes popular packages: Hadoop, Mahout
  - In-memory DB  $\rightarrow$  CPU Bound
  - Balanced compute and memory demands
  - Blessed by other works: HiBench, CMU [4]
- Single-workload single-machine
  - Intentional to permit deep understanding
  - Proof-by-optimization approach
  - Future work: multi-node setup

[4] K. Ren, Y. Kwon, M. Balazinska, and B. Howe, "Hadoop's adolescence: an analysis of Hadoop usage in scientific workloads,", VLDB 2013.





## Agenda

- $\checkmark$  Introduction
- The workload
- Threefold Analysis
- Findings
- Vs other workloads
- Conclusions



## **Experimental Setup**

### Hadoop

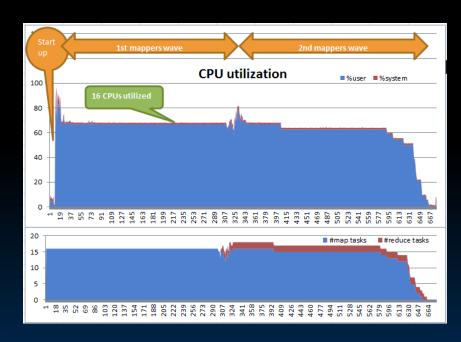
- 16 mappers
- 2 reducers
- 2GB heap/job
- 3 JVMs
- CPU
  - Keep unused cores intact
  - Turbo enabled
- Each result is average of 3 runs

ē	Ŋ	uarch	Intel Xeon E5-2697 v2, Ivy Bridge µarch, 30MB LLC			
war	СРО	Frequency	2.7 GHz (Turbo→3.5)			
Hardware		# sockets/ cores/ threads	2 / 12 / 1 or 2 (threads)			
	M-		1600 MHz.			
	I™Ie	mory	Max BW 60 GB/s			
	OS		Centos 6.5,			
	03		Kernel 2.6.32			
		Oracle	HotSpot JDK 6u29			
are	M	OpenJDK	IcedTea6 1.13.0pre			
Software		IBM	J9 2.4			
Sof	Ha	doop	Version 0.20.2			
	110	doob	2GB Java heap per job			
	Ma	hout	Version 0.6,			
	1 10	nout	Naive Bayes algo.			



## The Data Analytics Workload

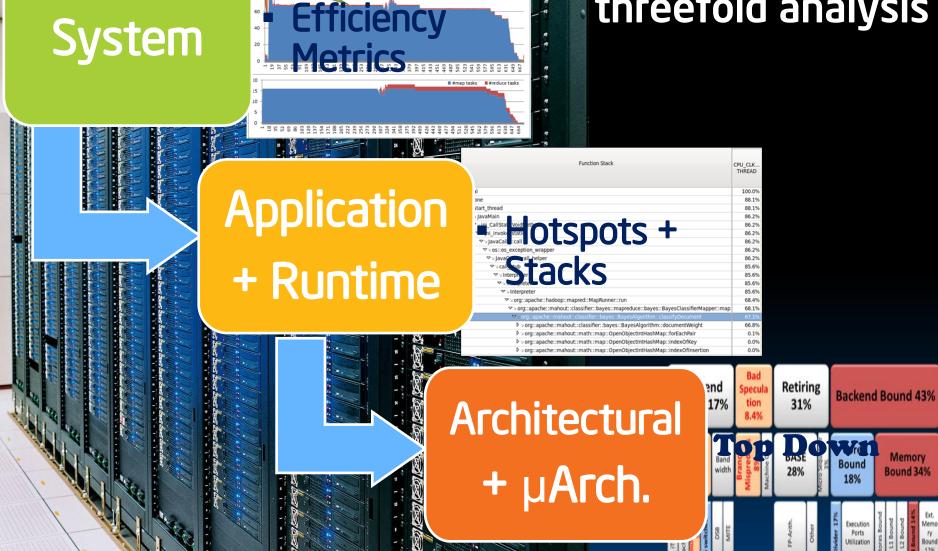
- Map-Reduce model
- Classifies Wikipedia pages into categories using Mahout Bayesian classification
- 32 data partitions distributed by Hadoop, map is dominant
- Negligible system impact
  - OS, I/O, Hadoop system





### **Customized** threefold analysis

IV.



Suger Scot

**CPU** utilization





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  - System
  - Application
  - Architecture
- Vs other workloads
- Conclusions



## System

- Characterization Metrics
  - System: CPU Utilization, Effective CPUs, Mem BW
  - Core: IPC, UPI (Uops Per Instruction)
  - Memory: MLP, Off-core Bound
- JVM generated code efficiency is critical
  - System: CPU over utilization due to GC scheduling

#### - Core: inefficient instruction selection

			System	l	Mer	nory	Core	
JVM	Speed	CPU	Effecti	IPC	Off-	Miss	UPI	MS
type	up	Utiliz	ve		core	ratio		Switc
		ation	CPUs		Boun			hes
HotSpot								
(Baseline)	1.43x	77%	12.4	1.17	27%	12%	1.03	5%
IBM J9	1.38x	91%	14.6	1.28	24%	9%	1.02	6%
OpenJDK	1.00x	128%	20.5	0.77	12%	9%	1.40	25%



### **Call-stacks**

#### **Application Level**

#### u/Architectural Level

Function Stack	CPU_CLK THREAD
ານ Total	100.0%
▼ y clone	88.1%
マ → start_thread	88.1%
マッ JavaMain	86.2%
マッjni_CallStaticVoidMethod	86.2%
▼ 」jni_invoke_static	86.2%
マッJavaCalls::call	86.2%
v → os::os_exception_wrapper	86.2%
▼ y JavaCalls::call_helper	86.2%
マ v call_stub	85.6%
マ → Interpreter	85.6%
マ → Interpreter	85.6%
▽ y Interpreter	85.6%
	68.4%
マッorg::apache::mahout::classifier::bayes::mapreduce::bayes::BayesClassifierMapper::map	68.1%
org::apache::mahout::classifier::bayes::BayesAlgorithm::classifyDocument	67.1%
▷ > org::apache::mahout::classifier::bayes::BayesAlgorithm::documentWeight	66.8%
v org::apache::mahout::math::map::OpenObjectIntHashMap::forEachPair	0.1%
v org::apache::mahout::math::map::OpenObjectIntHashMap::indexOfKey	0.0%
Image: Section 2 and Sectio	0.0%

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General Exploration General Exploration viewpoint (change) @									
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Grouping: Function / Call Stack									
	Hardware Even	Har.		Filled Pipe	eline Slots	Unfilled Pip	eline Slot		
	*			>	≫	2	$\gg$		
Function / Call Stack	CPU_CLK_UN THREAD	INS. ANY	CPI Rate	Retiring	Bad Specul	Back-e Bound	Front-e Bound		
apply	19.8%	53	0.945		1				
indexOfKey	18.7%	51	0.918		Ò		Ĭ		
indexOfInsertion	14.3%	41	0.886			0			
	9.9%	64	0.395						
	3.2%	60,	1.376		0				
	2.7%	91,	0.753						
	2.6%	77,	0.857						
get	2.6%	73,	0.903			libjvm.s	o!JavaCalls::		
Selected 3 row(	52.8%	1,4	0.919	0.340	0.111	0.374			

#### Examine where application's most time is spent



# Application

#### Issue:

- WordCount is performed for each category!
- Severely harms big caches

### • Optimization:

- Hoist WordCount loop
- 50% speedup
  - Enabled LLC data reuse
  - LLC misses reduced by 2x, Miss Ratio:  $12\% \rightarrow 7\%$

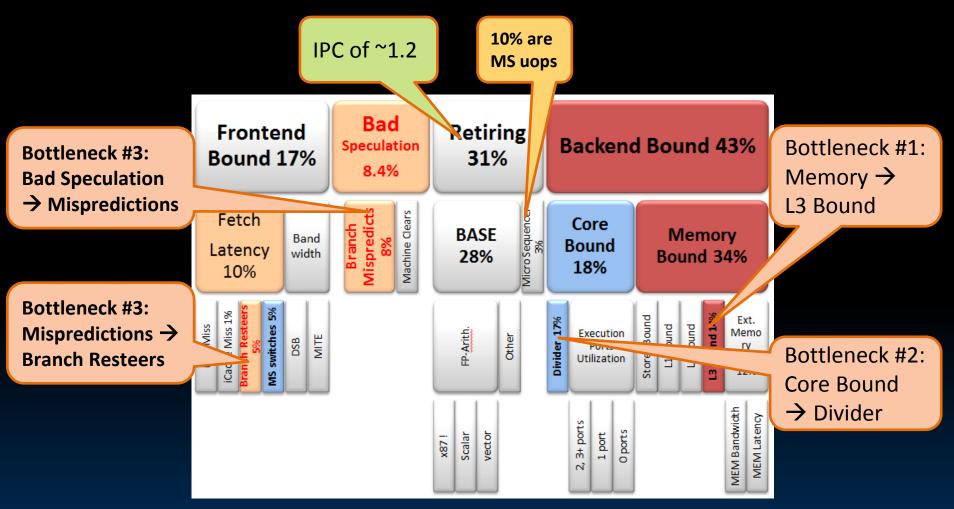
#### Hadoop Applications are widely inefficient

 CMU[4] surveyed apps of 3 Hadoop scientific setups, and reported large inefficiencies. Mahout was actually the "most optimized"

```
Listing 1: Main classification loop pseudo-code
(1) Label classifyDocument(document) {
      label = default label;
(2)
(3)
      foreach (category : categories) {
(4)
        hash = new HashMap<String><int>;
(5)
        foreach (word : document)
(6)
          hash.update(word, 1);
(7)
(8)
        result = 0;
(9)
        foreach (pair-of [word, frequency] : hash)
(10)
          result += frequency *
            featureWeight(category, word);
(11)
        if (result is a maximum)
(12)
(13)
          label = category;
(14) }
(15) return label;
(16)
  where featureWeight is:
(17) double featureWeight(label, word) {
       return - log[(getW("weight", label, word) +
(18)
    getW("params","αi")) /(getW("labelWeight",label) +
    getW("sumWeight", "vocabCount"))];
(10) 1
```



## **Top Down Performance Analysis**



[2] A. Yasin, "A Top-Down Method for Performance Analysis and Counters Architecture", ISPASS 2014



### Per hotspot drill down

BayesAlgorithm\$1::apply         24.8%         1.09         18%         5.1%         60%         17%         [Compiled Java code]           HashMap::indexOfKey         23.0%         1.15         34%         5.2%         46%         17%         [Compiled Java code]           shMap::indexOfInsertion         16.2%         1.27         42%         15.5%         21%         26%         compiled Java code]           HashMap::get         3.1%         1.41         30%         15.4%         30%         27%         [Compiled Java code]          Datastore::getWeight         3.1%         1.18         41%         0.8%         46%         14%         [Compiled Java code]           Level#2         BASE         Microcode Sequencer         BadSpec         Memory         Core         Frontend         Frontend           18%         4%         5%         51%         34%         13%         4%           35%         3%         5%         32%         44%         7%         10%           49%         4%         16%         14%         43%         12%         14%		Fu		on / Cal	l Stack	% time	IPC	Retir ing	BadSı culati		Back Bour		Fronte Boune		Mod	ule		
shMap::indexOfInsertion16.2%1.2742%15.5%21%26%Compiled Java codeHashMap::get3.1%1.1130%15.4%0%27%[Compiled Java codeDatastore::getWeight3.1%1.1841%0.8%46%14%[Compiled Java codeLevel#2BASEMicrocode SequencerBadSpec ulationMemory BoundCore BoundFrontend Bandwidth18%4%5%51%34%13%4%35%3%5%32%44%7%10%	Ē	Bayes	Algor	ithm\$1	::apply	24.8%	6 1.0	9 18%	5	.1%		<mark>60%</mark>	X	7%	[Comp	iled Jav	/a code]	
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BASESequencerulationBoundBoundLatencyBandwidth18%4%5%51%34%13%4%35%35%35%32%44%7%10%					_					-			_	1	_			
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35% 3% 5% 32% 44% 7% 10%										Bou						Band		
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					Level#	3.1											Cycles	
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3%         0%         0%         0%         0%         1%         30%         3%           3%         0%         0%         15%         0%         0%         4%         0%         41%         0%					1											-		



## Example#1: Cache misses in Hashing

- Java:
  - final int length=table.length;
- Bytecode:
  - \*getfield table
  - \*arraylength
- ASM: 4.9% of time spent in two loads:
  - mov r13d, dword ptr [r12+r9\*8+0x2c]
  - mov ecx, dword ptr [r12+r13\*8+0xc]
- Fix  $\rightarrow$  speedup
  - SW workaround to cache a copy of table's length
  - 5% app-level speedup

Listing 2	2: OpenObjectIntHashMap original source code
Listing 2 (1) (2) (3) (4) (5) (6) (7) (8) (9) (10) (11) (12) (13)	<pre>2: OpenObjectIntHashMap original source code int indexOfKey(T key) { int length = table.length; int hash = key.hashCode() &amp; 0x7FFFFFF; int i = hash % length; int decrement=hash % (length - 2); if (decrement == 0) decrement == 0) decrement == 1; while ((state[i] != FREE) &amp;&amp;) { i -= decrement; if (i &lt; 0)</pre>
(13)	}

			System	n Memory			Core			
f	Optimizatio	Speed	IPC	Insts.	Mem	Offcore	UPI	Divider	Frontend	
	n/setting	up		reduction	BW	Bound		Active	Bound	
	4 manners	0.32r	1.20	n/a	117	26%	1.03	16%	17%	
	Baseline	1.00x	1.17	n/a	2.91	27%	1.03	17%	17%	
	Opt. 5.2	1.05x	1.18	0%	3.16	26%	1.03	17%	20%	
	Opt. 5.3	1.07x	1.18	-3%	3.22	27%	1.00	13%	16%	
	Opt. 5.4	1.14x	1.08	-21%	3.25	29%	1.01	14%	14%	



## Example#2: Computation in Hashing

- Integer Divide is inefficient
  - Implemented as 9-uop flow
  - Contention with FP divides (transcendentals)
  - Contention w/ sibling thread
- In BDA
  - TopDown tags 2<sup>nd</sup> hotspot as Backend→Core Bound→Divider
  - Divider Busy 17%, Frontend Bound 20%, UPI 1.03
- Fix  $\rightarrow$  speedup
  - SW workaround to avoid IDIV should there be no collision
  - 2% app-level additional speedup

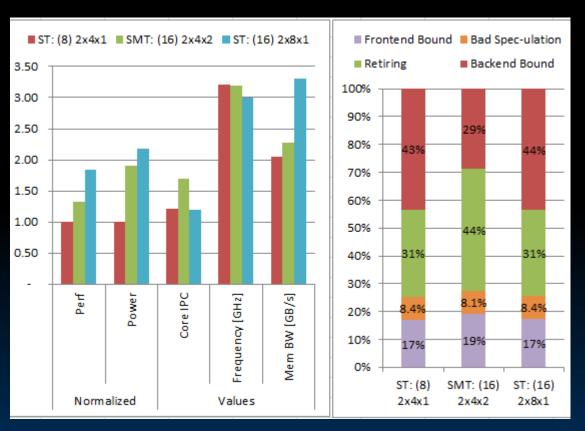
(1)	<pre>int indexOfKey(T key) {</pre>
(2)	<pre>int length = table.length;</pre>
(3)	<pre>int hash = key.hashCode() &amp; 0x7FFFFFF;</pre>
(4)	<pre>int i = hash % length;</pre>
(5)	<pre>int decrement=hash % (length - 2);</pre>
(6)	if (decrement == 0)
(7)	decrement = 1;
(8)	while ((state[i] != FREE) &&) {
(9)	<pre>i -= decrement;</pre>
(10)	if (i < 0) i += length;
(11)	}
(12)	if (state[i] == FREE) return -1;
(13)	return i;
(14)	}
	-

		System	1	Me	mory	Core			
Optimizatio	Speed	IPC	Insts.	Mem	Offcore	UPI	Divider	Frontend	
n/setting	up		reduction	BW	Bound		Active	Bound	
4 mappers	0.32x	1.20	n/a	1.17	26%	1.03	16%	17%	
Baseline	1 00x	117	n/a	2 91	27%	1.03	17%	17%	
Opt. 5.2	1.05x	1.18	0%	3.16	26%	1.03	17%	20%	
Opt. 5.3	1.07x	1.18	-3%	3.22	27%	1.00	13%	16%	
Opt. 5.4	1.14x	1.08	-21%	3.25	29%	1.01	14%	14%	



## SMT

- SMT technology
  - 2x threads with improved utilization (or contended) of core resources
- 33% speedup vs 8C
  - 14% power reduction vs 16C
  - Core IPC improved by ~40%
- Implications
  - Optimizations potential (optimizes vs baseline) is higher by 5% thanks to SMT
  - Most datacenters keep SMT enabled





## **Findings Summary**

Level	Parameter	Observation and/or Optimizations' potential			
System	JVM selection	Hotspot/OpenJDK = 1.43x IBM-J9/OpenJDK = 1.38x			
	SMT	MT vs CMP: 35% speedup, poor power reduction			
	Turbo	Benefits reduce- and straggler map-jobs			
Application and	Algorithm	Wide inefficiencies. Demoed 50% speedup with 2x reduction in ext. memory demand			
Language	Programming style	Too abstracted code limits exploiting upcoming JVM and CPU parallelization features			
	Polymorphic Objects	25% uop reduction, 6% sample speedup			
µ/Architect ure and	JVM code generation	Overuses memory dereferences. 6% sample speedup			
Runtime	CPU inefficiencies	Fetch bandwidth and contended (SMT) EUs. e.g. Integer Divides. 2% sample speedup			
	Control flow predication	Data-dependent branch mispredictions. ~16% uops waste power on miss-speculated paths			





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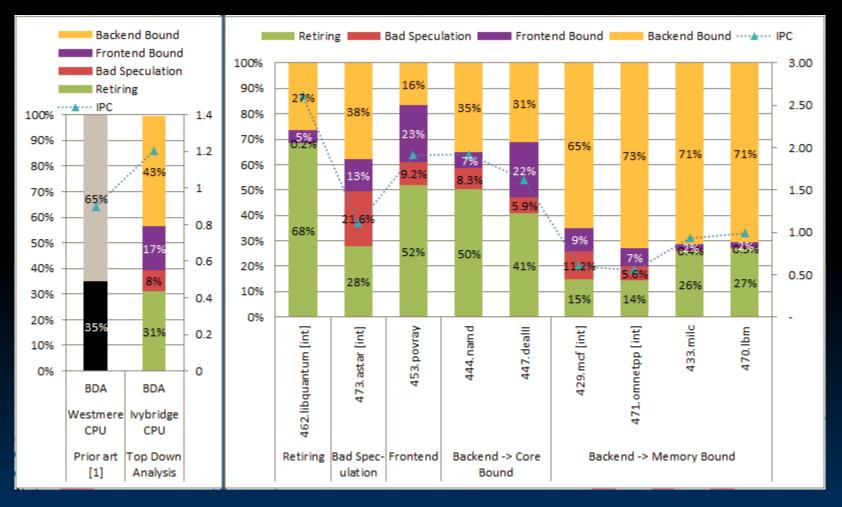
## **Comparison to Traditional Workloads**



- Decent IPC. Low Memory BW demand.
- Metrics: Off-core Bound [TopDown] vs Miss-Ratio [traditional], e.g. 462.libquantum
- Modest Off-core Bound hints on non-memory bottlenecks exist.



## **Microarchitectural Comparison**



\* Most left bar: black is % of cycles with retiring uops; white no retirement



## Summary

- Data Analytics (scale-out) has new characteristics
  - Deep software layers, heavy abstractions, Wide inefficiencies
  - Plenty of software optimizations opportunities
- Presented a customized 3-fold analysis method for System, Application and Architectural Levels
- Revealed BDA performance is limited on *managing* rather than *accessing* the data
  - Root-caused inefficiencies at the three levels
  - Most time is spent in few hotspots, unlike traditional Enterprise
  - Got 65% speedup through sample fixes

#### Try out this method on your favorite workload



# Thank You

## Questions?

(or to get best practices doc to perform analysis on your favorite workload)

### Ahmad.Yasin@intel.com



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