## Contents

**SESSION: NOVEL APPLICATIONS + ALGORITHMS + SUPPORTING SYSTEMS**

  - Robert Gall, Fritz Troester, Gheorghe Mogan
  - Page 3

- Hardware Implementation of Integer-to-integer Discrete Wavelet Transform Using Haar Filter Based on Lifting Scheme
  - Rahul Maheshwari, Nancy Warter-Perez, Jane Dong
  - Page 9

- A Managed Smart Card API for Securing Embedded Environments: Adapting PC/SC Lite for the .NET Micro Framework
  - Kevin Gladstone, Bart Bombay
  - Page 16

- Embedded Microcontroller for Performance Analysis of Mechatronic Devices
  - Akin Cellatoglu, Karuppanan Balasubramanian
  - Page 21

- Stream Processing Extension based on RISC Processors for Multimedia Applications
  - Ji Kong, Peilin Liu, Rendong Ying
  - Page 27

- Computer Controlled RF Based Investigator Robot
  - Page 31

- Multi-Level Embedded Motor Control for Prosthesis
  - Chandrasekhar Potluri, Parmod Kumar, Jeff Molitor, Madhavi Anugolu, Alex Jensen, Kenyon Hart, Steve C. Chiu
  - Page 37

- QSTG: A Quick Search Test Generation Algorithm Based on Boolean Satisfiability Method
  - Saha Mousumi, Gundlapalli Manohar, Ghosh Koel, Mortha Anand
  - Page 43

- The GPS Mobile Monitor System
  - Jing Pang, Joseph Benjamin, Priteshkumar Janakkumar Modi
  - Page 49

- LifeSaverNeo1971X Vehicle
  - Raj Shankar
  - Page 55

**SESSION: OPERATING SYSTEM TOOLS + COMPILERS + SIMULATION SYSTEMS AND SOFTWARE UTILITIES**

- Customizable and Predictable Synchronization in a Component-Based OS
  - Gabriel Parmer, Jiguo Song
  - Page 59
Experience with an eCos RTOS Based Compiler for the RealSpec Real-Time Specification Language
Amir Khwaja, Joseph Urban

Embedded Software and a Sensor-Controlled ECU/FCM System: A Hybrid Vehicle Simulation
Robert Murphy, Patrick Bobbie

SESSION: FPGA + VLIW ARCHITECTURES + SOC
MicroBlaze Soft Core Based FPGA Embedded System Design of Tetris Game
Jing Pang, Manuel Pravin, Robert Tanihaha

The First Clock Cycle Is A Real BIST
Charles Stroud, Bradley Dutton

SoC Methodology an Overview
Antonio F Mondragon-Torres

LADAR System Development Using the Altera DE1 Board
Bassam Shaer, Steven Silvester, Darrell Card

Application Specific Instruction Set Exploration on VLIW Architectures
Chengpin Tseng, Chiu-yun Hung, De-Sheng Chen, Kuei-Chung Chang, Yi-wen Wang

System Level Architectural Synthesis and Compilation Technique in Reconfigurable Computing System

Embedded Systems Performance Modeling using FPGA-based Profiling
Fadi Obeidat, Jose Ortiz, Jeremy Cooper, A. A. R. Bsoul, Robert Klenke

A Power-saving Code-compression Design for the VLIW Embedded Systems
Che Wei Lin, Chang Hong Lin

SESSION: DISTRIBUTED METHODS, SYSTEMS AND SCHEDULING ANALYSIS + REAL-TIME SYSTEMS
Distributed Goal Achievement in Control Systems
Raymond Staron, Francisco Maturana, Dan Carnahan, Kenwood Hall

Model-Based Generation of Fault-Tolerant Embedded Systems
Wolfgang Haberl, Stefan Kugele, Uwe Baumgarten
Implementation of a Lightweight Distributed Consistency Protocol  
Daniel Nitecki, David Bauer, Ray Kresman

Optimized Deadline Assignment and Schedulability Analysis for Distributed Real-Time Systems with Local EDF Scheduling  
Juan Maria Rivas, Jose Javier Gutierrez, Jose Carlos Palencia, Michael Gonzalez Harbour

An Approach for Efficient Real Time Moving Object Detection  
Arnab Roy, Sanket Shinde, Kyung-Don Kang

Testing of Real-time OS based on OSEK/VDX for Certification  
Sung Ho Ahn, Jae Young Kim, HyunWoo Lee

FPGA-based Hybrid Systems in Forth: a Forth Core and Reconfigurable Hardware from Forth  
Darrin Hanna, Richard Haskell

Energy-Conscious Dual-Priority Scheduling for (m,k)-Hard Fault Tolerant Real-Time Systems  
Linwei Niu, Kuhanandha Mahalingam

Agile and Incremental Development Methods for Real-Time Software Systems  
Amir Khwaja, Joseph Urban

SESSION: RECONFIGURABLE SYSTEMS, VIDEO AND TRACKING METHODS, PATH PLANNING, AND APPLICATIONS

Design Method for Scalable DCT Algorithm Suitable for Reconfigurable Systems  
Nabil Kerkiz, Adel Hussein, Amr Elchouemi, Adnan Suleiman, David Akopian

Video Tracking and Embedded Microcontrollers  
Bassam Shaer, David Cudney

Embedded GPS Path Planning for Mobile Robots  
Luis Albert, Victor Planes, Carlos Domínguez, Houcine Hassan